TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

2SK3068

Chopper Regulator DC-DC Converter, and Motor Drive Applications

• Low drain–source ON-resistance : $R_{DS\ (ON)} = 0.4\ \Omega\ (typ.)$ • High forward transfer admittance : $|Y_{fS}| = 9.0\ S\ (typ.)$ • Low leakage current : $I_{DSS} = 100\ \mu A\ (max)\ (V_{DS} = 500\ V)$ • Enhancement mode : $V_{th} = 2.0\ to\ 4.0\ V\ (V_{DS} = 10\ V,\ I_D = 1\ mA)$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	500	(v)
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	500	V
Gate-source voltage		V _{GSS}	±30	V
Drain current	DC (Note 1)	ID	12	A
	Pulse (Note 1)	I _{DP}	48	Α
Drain power dissipation	n (Tc = 25°C)	PD	100	W
Single pulse avalanche	e energy (Note 2)	EAS	364	(mJ
Avalanche current		IAR)) 12	A
Repetitive avalanche e	energy (Note 3)	EAR	10 (mJ
Channel temperature		(T _{ch})	150	<i>\</i> /%c
Storage temperature ra	ange	Tstg	-55 to 150	(e)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	1.25	°C / W
Thermal resistance, channel to ambient	R _{th (ch-a)}	83.3	°C/W

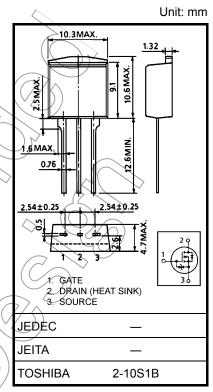
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 4.3 mH, R_G = 25 Ω , I_{AR} = 12 A

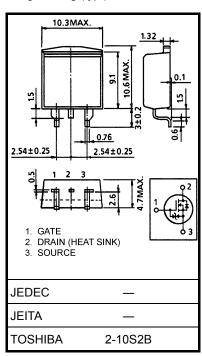
Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



Weight: 1.5 g (typ.)



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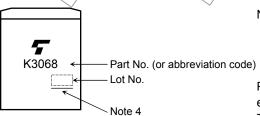
Electrical Characteristics (Ta = 25°C)

Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cui	rrent	I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V		_	100	μΑ
Drain-source br	eakdown voltage	V _{(BR) DSS}	I _D = 10 mA, V _{GS} = 0 V	500			٧
Gate threshold v	oltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	2.0) >_	4.0	٧
Drain-source Ol	N-resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 6 A		0.4	0.52	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 6 A	4.0	9.0	_	S
Input capacitano	e	C _{iss}		_	2040	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	<i>_</i>	200	_	pF
Output capacitance		C _{oss}		_	640	_	
Switching time	Rise time	tr	V _{GS} 10V I _D = 6A V _{OUT}	- (22	∕>	
	Turn-on time	t _{on}	0 V $^{-1}$ $^{-$		58) —	ns
	Fall time	t _f	v _{DD} =200v		36		115
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\rm W} = 10 \mu \rm s$) –	180		
Total gate charge (Gate-source plus gate-drain)		Qg			45	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, V_{D} = 10 \text{ A}$		25	_	nC
Gate-drain ("miller") charge		Qgd		_	20	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	1/DR		-	-	12	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	48	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 12 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 12 A, V _{GS} = 0 V	1	1200	-	ns
Reverse recovery charge	Q _{rr}	dl _{DR} / dt = 100 A / μs	_	16	_	μC

Marking

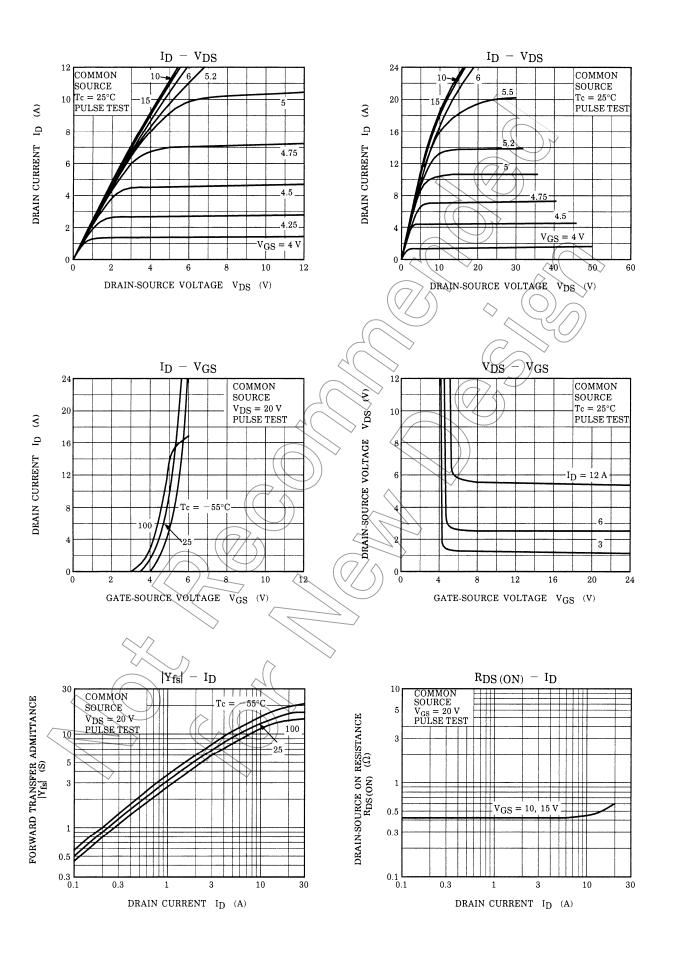


Note 4: A line under a Lot No. identifies the indication of product Labels.

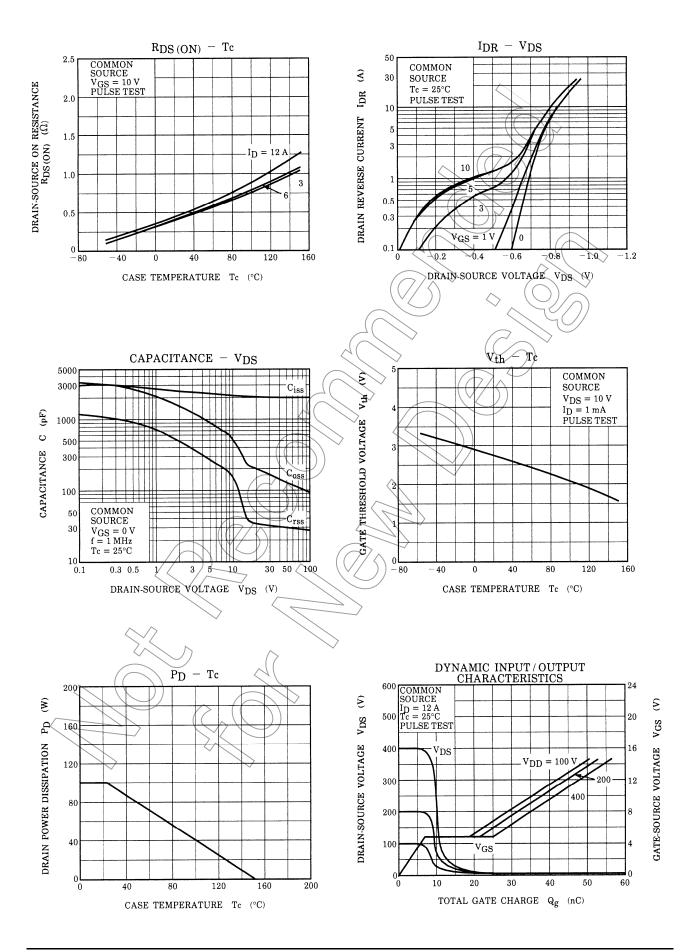
Not underlined: [[Pb]]/INCLUDES > MCV

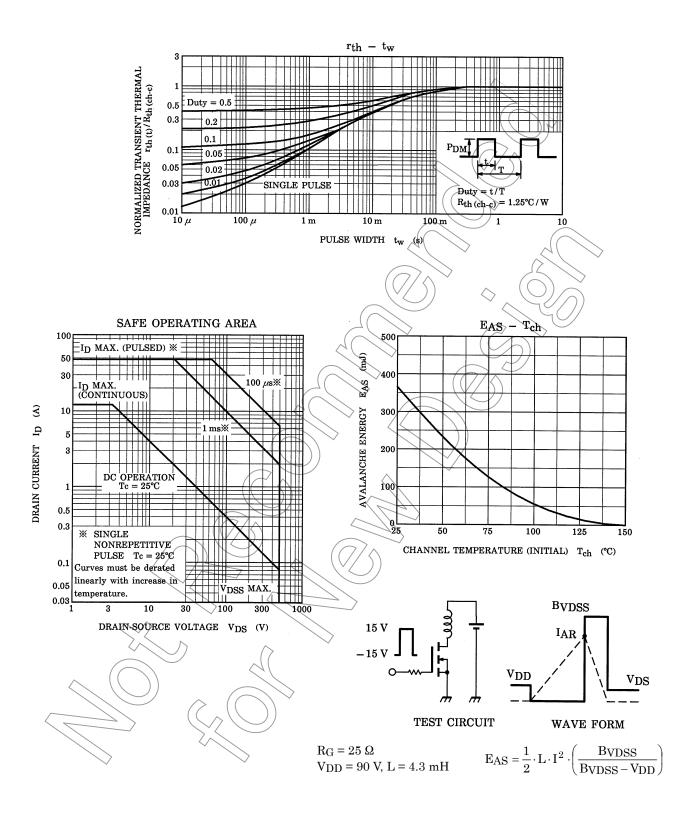
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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