

TOSHIBA Bipolar Digital Integrated Circuit Silicon Monolithic

**ULQ2003AP,ULQ2003AFW
(Manufactured by Toshiba Malaysia)****7-ch Darlington Sink Driver**

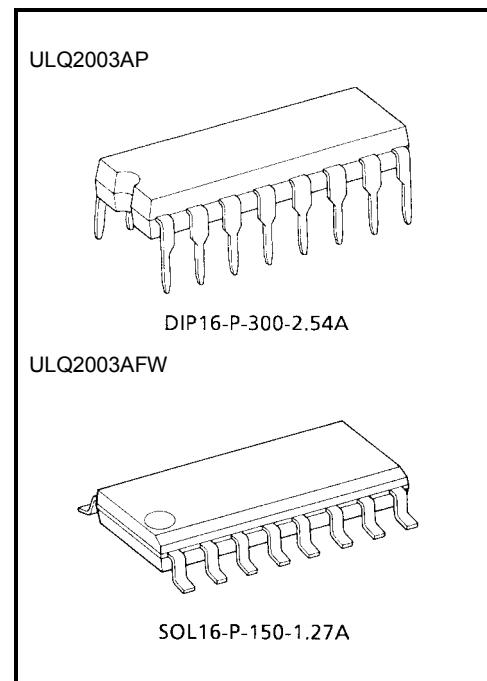
The ULQ2003AP/AFW are high-voltage, high-current darlington drivers comprised of seven NPN darlington pairs.

All units feature integral clamp diodes for switching inductive loads.

Applications include relay, hammer, lamp and display (LED) drivers.

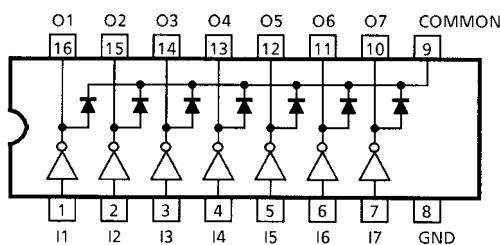
Features

- Output current (single output): 500 mA max
- High sustaining voltage output: 50 V max
- Operating temperature: $T_a = -40$ to 105°C
- Output clamp diodes
- Inputs compatible with various types of logic
- Package type-AP: DIP-16pin
- Package type-AFW: SOL-16pin

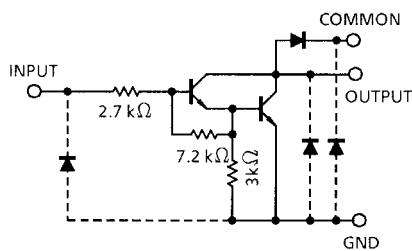
**Weight**

DIP16-P-300-2.54A : 1.11 g (typ.)
SOL16-P-150-1.27A : 0.15 g (typ.)

Type	Input Base Resistor	Designation
ULQ2003AP/AFW	2.7 k Ω	TTL, 5 V CMOS

Pin Connection (top view)

Schematics (each driver)



Note: The input and output parasitic diodes cannot be used as clamp diodes.

Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Rating	Unit
Output sustaining voltage	$V_{CE}(\text{SUS})$	-0.5 to 50	V
Output current	I_{OUT}	500	mA/ch
Input voltage	V_{IN}	-0.5 to 30	V
Clamp diode reverse voltage	V_R	50	V
Clamp diode forward current	I_F	500	mA
Power dissipation	AP	1.47	W
		0.54/0.625 (Note)	
Operating Temperature	T_{opr}	-40 to 105	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55 to 150	$^\circ\text{C}$

Note: On glass epoxy PCB (30 × 30 × 1.6 mm Cu 50%)

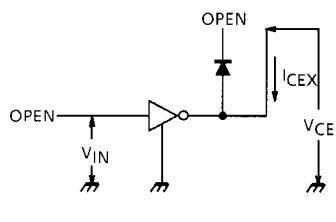
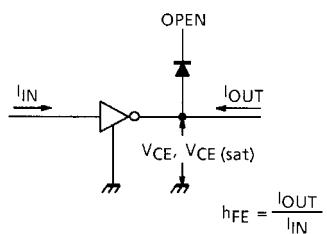
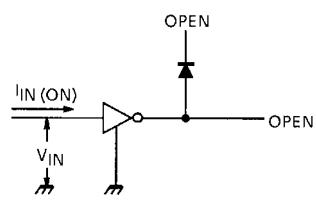
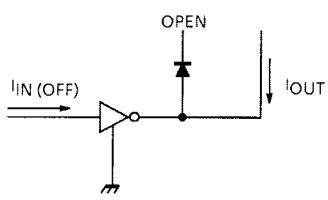
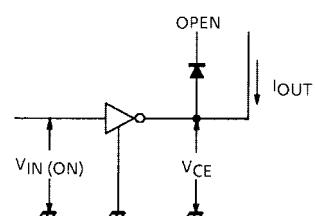
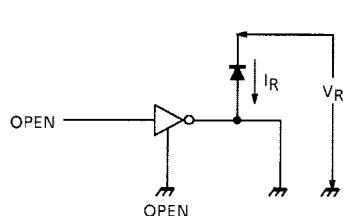
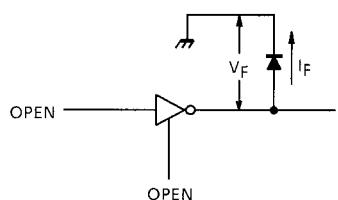
Recommended Operating Conditions ($T_a = -40$ to 105°C)

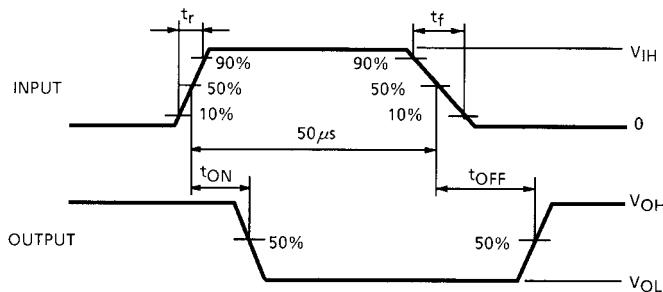
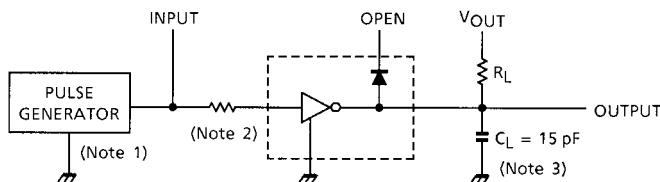
Characteristics	Symbol	Test Condition		Min	Typ.	Max	Unit
Output sustaining voltage	$V_{CE}(\text{SUS})$			0	—	50	V
Output current	AP	$T_{\text{pw}} = 25$ ms 7 Circuits $T_a = 105^\circ\text{C}$	Duty = 10%	0	—	200	mA/ch
			Duty = 50%	0	—	50	
			Duty = 10%	0	—	100	
			Duty = 50%	0	—	25	
Input voltage	V_{IN}			0	—	24	V
Input voltage (output on)	$V_{\text{IN}}(\text{ON})$	$I_{\text{OUT}} = 400$ mA, $h_{FE} = 1000$		2.8	—	24	V
Input voltage (output off)	$V_{\text{IN}}(\text{OFF})$			0	—	0.7	V
Clamp diode reverse voltage	V_R			—	—	50	V
Clamp diode forward current	I_F			—	—	350	mA
Power dissipation	AP	$T_a = 105^\circ\text{C}$		—	—	0.52	W
		$T_a = 105^\circ\text{C}$ (Note)		—	—	0.22	

Note: On glass epoxy PCB (30 × 30 × 1.6 mm Cu 50%)

Electrical Characteristics (Ta = 25°C unless otherwise noted)

Characteristic	Symbol	Test Circuit	Test Condition		Min	Typ.	Max	Unit
Output leakage current	I _{CEX}	1	V _{CE} = 50 V, Ta = 25°C	—	—	50	μA	
			V _{CE} = 50 V, Ta = 105°C	—	—	100		
Collector-emitter saturation voltage	V _{CE} (sat)	2	I _{OUT} = 350 mA, I _{IN} = 500 μA	—	1.3	1.6	V	
			I _{OUT} = 200 mA, I _{IN} = 350 μA	—	1.1	1.3		
			I _{OUT} = 100 mA, I _{IN} = 250 μA	—	0.9	1.1		
DC current transfer ratio	h _{FE}	2	V _{CE} = 2 V, I _{OUT} = 350 mA	1000	—	—		
Input current (output on)	I _{IN} (ON)	3	V _{IN} = 2.4 V, I _{OUT} = 350 mA	—	0.4	0.7	mA	
Input current (output off)	I _{IN} (OFF)	4	I _{OUT} = 500 μA, Ta = 85°C	50	65	—	μA	
Input voltage (output on)	V _{IN} (ON)	5	V _{CE} = 2 V	I _{OUT} = 350 mA	—	—	2.6	V
			h _{FE} = 1000	I _{OUT} = 200 mA	—	—	2.0	
Clamp diode reverse current	I _R	6	V _R = 50 V, Ta = 25°C	—	—	50	μA	
			V _R = 50 V, Ta = 105°C	—	—	100		
Clamp diode forward voltage	V _F	7	I _F = 350 mA	—	—	2.2	V	
Input capacitance	C _{IN}	—		—	15	—	pF	
Turn-on delay	t _{ON}	8	V _{OUT} = 50 V, R _L = 125 Ω C _L = 15 pF	—	0.1	—	μs	
Turn-off delay	t _{OFF}	8	V _{OUT} = 50 V, R _L = 125 Ω C _L = 15 pF	—	0.6	—		

Test Circuit**1. I_{CEX}** **2. $V_{CE}(\text{sat}), h_{FE}$** **3. $I_{IN}(\text{ON})$** **4. $I_{IN}(\text{OFF})$** **5. $V_{IN}(\text{ON})$** **6. I_R** **7. V_F** 

8. t_{ON} , t_{OFF} 

Note 1: Pulse width 50 μs , duty cycle 10%
 Output impedance 50 Ω , $t_r \leq 5 \text{ ns}$, $t_f \leq 10 \text{ ns}$

Note 2: See below

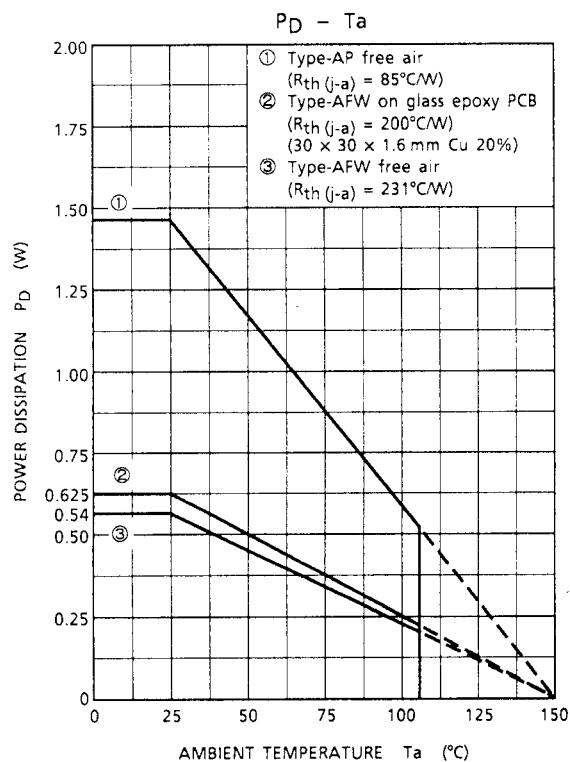
Input Condition

Type Number	R1	V_{IH}
ULQ2003AP/AFW	0	3 V

Note 3: C_L includes probe and jig capacitance.

Precautions for Using

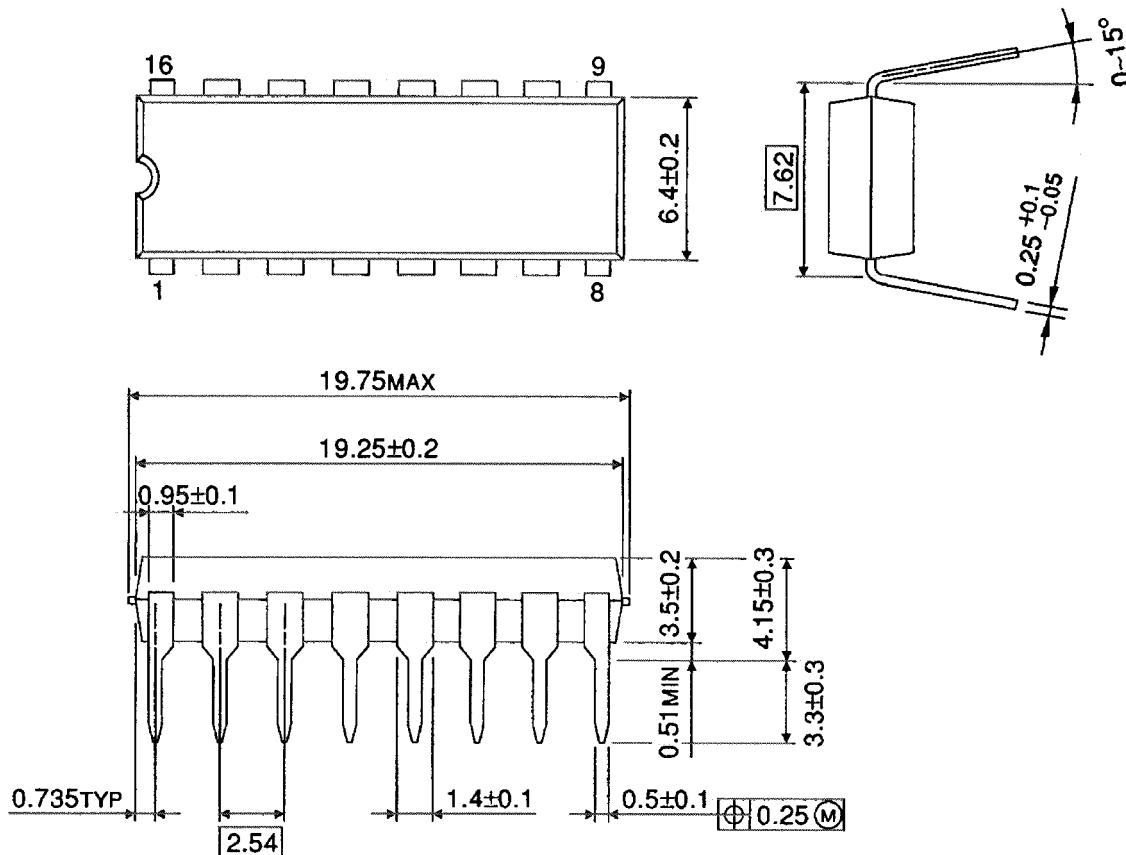
This IC does not include built-in protection circuits for excess current or overvoltage.
 If this IC is subjected to excess current or overvoltage, it may be destroyed.
 Hence, the utmost care must be taken when systems which incorporate this IC are designed.
 Utmost care is necessary in the design of the output line, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.



Package Dimensions

DIP16-P-300-2.54A

Unit : mm

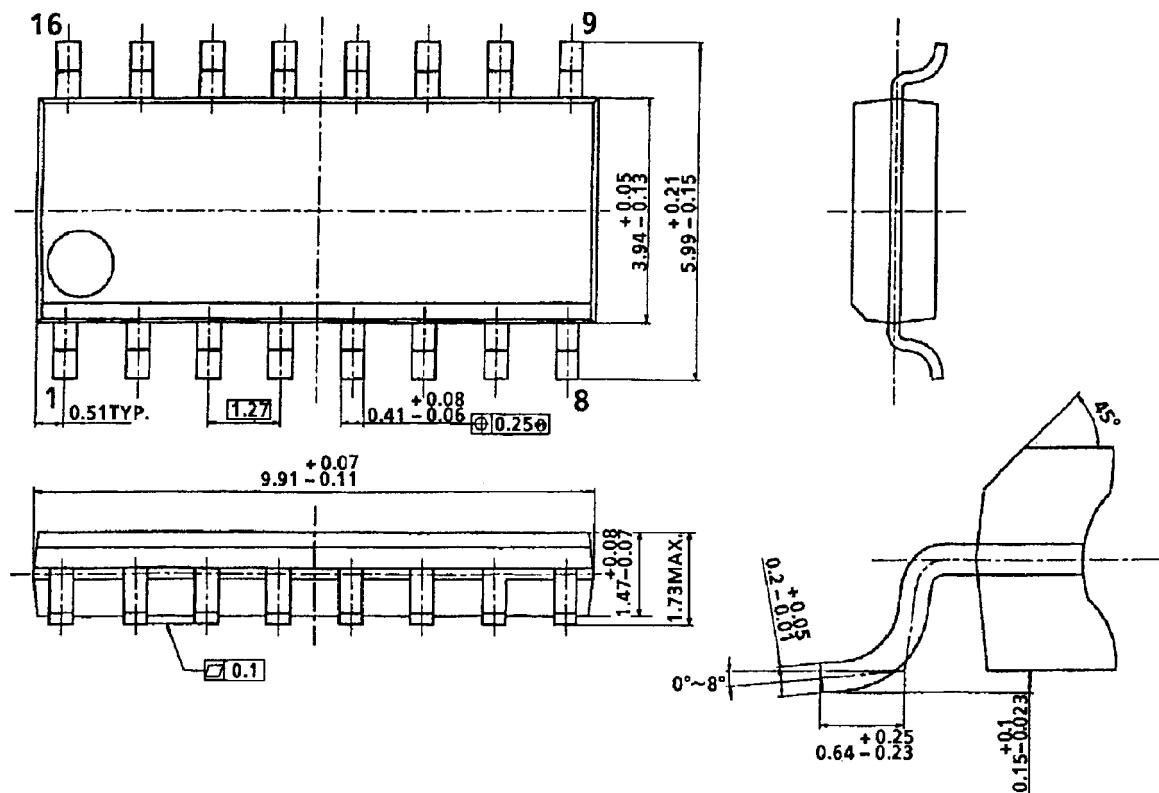


Weight: 1.11 g (typ.)

Package Dimensions

SOL16-P-150-1.27A

Unit : mm



Weight: 0.15 g (typ.)

RESTRICTIONS ON PRODUCT USE

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