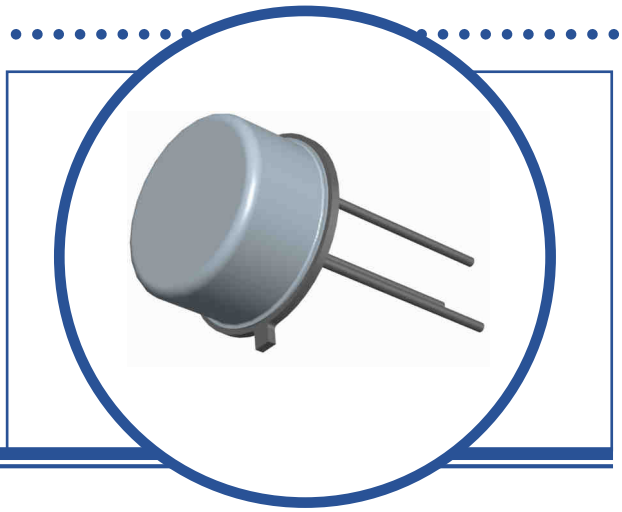
POSITIVE ADJUSTABLE VOLTAGE REGULATOR

TT electronics
Semelab Limited

IP117MH / LM117H
IP117MAH
IP117MHVH / LM117HVH
IP117MAHVH



FEATURES

- 1% Output Voltage Tolerance Guaranteed (A Version)
- 0.3% Load Regulation Guaranteed
- 0.01% Line Regulation Guaranteed
- 0.5A Output Current Guaranteed
- Internal Thermal Overload Protection
- Output Short Circuit Protected
- Hermetic Metal TO39 (TO-205AF) Package
- Screening Options Available

APPLICATIONS

- Adjustable Power Supplies
- System Power Supplies
- Precision Voltage / Current Regulators

DESCRIPTION

The 117M Series regulators are adjustable 3 terminal voltage regulators capable of supplying in excess of 0.5A over an output voltage range of 1.25V to 40V (HV Series, 60V). The regulators are exceptionally easy to apply requiring only 2 external resistors to set the output voltage.

Every effort has been made to make these devices easy to use and difficult to damage. Internal current limiting and power limiting coupled with true thermal limiting prevents damage due to overloads or shorts.

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise stated)

$V_I - V_O$	Input – Output Voltage Differential	Standard	40V
		HV Series	60V
I_O	Output Current		Internally Limited
P_D	Power Dissipation		Internally Limited
T_J	Operating Junction Temperature Range		-55 to +150°C
T_{stg}	Storage Temperature Range		-65 to +150°C

THERMAL PROPERTIES

Symbols	Parameters	Min.	Typ.	Max.	Units
$R_{\theta JC}$	Thermal Resistance, Junction To Case			15	°C/W

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ELECTRICAL CHARACTERISTICS(1)(3) (T_C = 25°C unless otherwise stated)

LM117, IP117M, LM117HV, IP117MHV							
Symbols	**Parameters**	**Test Conditions**		**Min.**	**Typ**	**Max.**	**Units**
V_{REF}	Reference Voltage	I_{OUT} = 10mA		1.200	1.250	1.300	V
		10mA ≤ I_{OUT} ≤ I_{MAX}, P ≤ P_{MAX}, 3V ≤ (V_{IN}-V_{OUT}) ≤ V_{MAX}	•	1.200	1.250	1.300	
$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	Line Regulation (2)	3V ≤ (V_{IN}-V_{OUT}) ≤ V_{MAX}, I_{OUT} = 10mA			0.010	0.020	%/V
			•		0.020	0.050	
$\frac{\Delta V_{OUT}}{\Delta I_{OUT}}$	Load Regulation (2)(3)	10mA ≤ I_{OUT} ≤ I_{MAX}, V_{OUT} ≤ 5V			5	15	mV
		V_{OUT} ≥ 5V			0.1	0.3	%
		V_{OUT} ≤ 5V	•		20	50	mV
		V_{OUT} ≥ 5V	•		0.3	1.0	%
	Thermal Regulation	V_{IN} = 5.25V, I_{OUT} = 0.5A, P_D = 2W, T_A = 25°C, 20ms Pulse			0.030	0.070	%/W
	Ripple Rejection	V_{OUT} = 10V, f = 120Hz, C_{ADJ} = 0			65		dB
		C_{ADJ} = 10µF	•	66	80		
I_{ADJ}	Adjust Pin Current		•		50	100	µA
ΔI_{ADJ}	Adjust Pin Current Change	10mA ≤ I_{OUT} ≤ I_{MAX}, 2.5V ≤ (V_{IN}-V_{OUT}) ≤ V_{MAX}	•		0.2	5	
I_{MIN}	Minimum Load Current	(V_{IN} - V_{OUT}) = 40V	•		3.5	5	mA
		(V_{IN} - V_{OUT}) = 60V **(HV Series)**	•		3.5	7	
I_{CL}	Current Limit	(V_{IN} - V_{OUT}) ≤ 15V	•	0.5	0.8		A
		(V_{IN} - V_{OUT}) = 40V	•	0.15	0.2		
		(V_{IN} - V_{OUT}) = 60V **(HV Series)**	•		0.3		
$\frac{\Delta V_{OUT}}{\Delta TEMP}$	Temperature Stability		•		1.0		%
$\frac{\Delta V_{OUT}}{\Delta TIME}$	Long Term Stability	T_A = 125°C, 1000Hrs			0.3	1.0	
e_n	RMS Output Noise (% of V_{OUT})	T_A = 25°C, 10Hz ≤ f ≤ 10KHz			0.001		

Notes
The ● denotes the specifications which apply over the full operating temperature range.

(1) Test conditions unless otherwise stated: V_{IN} - V_{OUT} = 5V, I_{OUT} = 0.1A, P_{MAX} = 2W, I_{MAX} = 0.5A

(2) Regulation is measured at constant junction temperature, using pulse testing at a low duty cycle. Changes in output voltage due to heating effects are covered under thermal regulation specifications. Load regulation is measured at $^1/_8$" below the base of the package on the output pin.

(3) V_{MAX} = 40V for LM117, IP117M, IP117MA
V_{MAX} = 60V for LM117HV, IP117MHV, IP117MAHV

ELECTRICAL CHARACTERISTICS(1)(3) (T_C = 25°C unless otherwise stated)

IP117MA, IP117MAHV							
Symbols	**Parameters**	**Test Conditions**		**Min.**	**Typ**	**Max.**	**Units**
V_{REF}	Reference Voltage	I_{OUT} = 10mA		1.238	1.250	1.262	V
		10mA ≤ I_{OUT} ≤ I_{MAX}, P ≤ P_{MAX}, 3V ≤ (V_{IN}-V_{OUT}) ≤ V_{MAX}	●	1.220	1.250	1.270	
$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	Line Regulation (2)	3V ≤ (V_{IN}-V_{OUT}) ≤ V_{MAX}, I_{OUT} = 10mA			0.005	0.010	%/V
			●		0.010	0.020	
$\frac{\Delta V_{OUT}}{\Delta I_{OUT}}$	Load Regulation (2)(3)	10mA ≤ I_{OUT} ≤ I_{MAX}, V_{OUT} ≤ 5V			5	15	mV
		V_{OUT} ≥ 5V			0.1	0.3	%
		V_{OUT} ≤ 5V	●		15	50	mV
		V_{OUT} ≥ 5V	●		0.3	1.0	%
	Thermal Regulation	V_{IN} = 5.25V, I_{OUT} = 0.5A, P_D = 2W, T_A = 25°C, 20ms Pulse			0.002	0.020	%/W
	Ripple Rejection	V_{OUT} = 10V, f = 120Hz, C_{ADJ} = 0			65		dB
		C_{ADJ} = 10µF	●	66	80		
I_{ADJ}	Adjust Pin Current		●		50	100	µA
ΔI_{ADJ}	Adjust Pin Current Change	10mA ≤ I_{OUT} ≤ I_{MAX}, 2.5V ≤ (V_{IN}-V_{OUT}) ≤ V_{MAX}	●		0.2	5	
I_{MIN}	Minimum Load Current	(V_{IN} - V_{OUT}) = 40V	●		3.5	5	mA
		(V_{IN} - V_{OUT}) = 60V **(HV Series)**	●		3.5	7	
I_{CL}	Current Limit	(V_{IN} - V_{OUT}) ≤ 15V	●	0.5	0.8		A
		(V_{IN} - V_{OUT}) = 40V	●	0.15	0.2		
		(V_{IN} - V_{OUT}) = 60V **(HV Series)**	●		0.3		
$\frac{\Delta V_{OUT}}{\Delta TEMP}$	Temperature Stability		●		1.0	2	%
$\frac{\Delta V_{OUT}}{\Delta TIME}$	Long Term Stability	T_A = 125°C, 1000Hrs			0.3	1.0	
e_n	RMS Output Noise (% of V_{OUT})	T_A = 25°C, 10Hz ≤ f ≤ 10KHz			0.001		

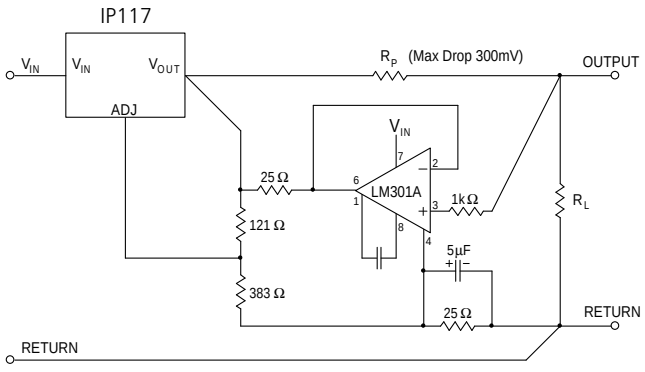
Notes
The ● denotes the specifications which apply over the full operating temperature range.

(1) Test conditions unless otherwise stated: V_{IN} - V_{OUT} = 5V, I_{OUT} = 0.1A, P_{MAX} = 2W, I_{MAX} = 0.5A

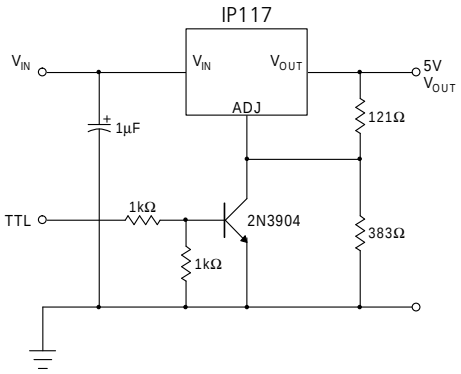
(2) Regulation is measured at constant junction temperature, using pulse testing at a low duty cycle. Changes in output voltage due to heating effects are covered under thermal regulation specifications. Load regulation is measured at $^1/_8$" below the base of the package on the output pin.

(3) V_{MAX} = 40V for LM117, IP117M, IP117MA
V_{MAX} = 60V for LM117HV, IP117MHV, IP117MAHV

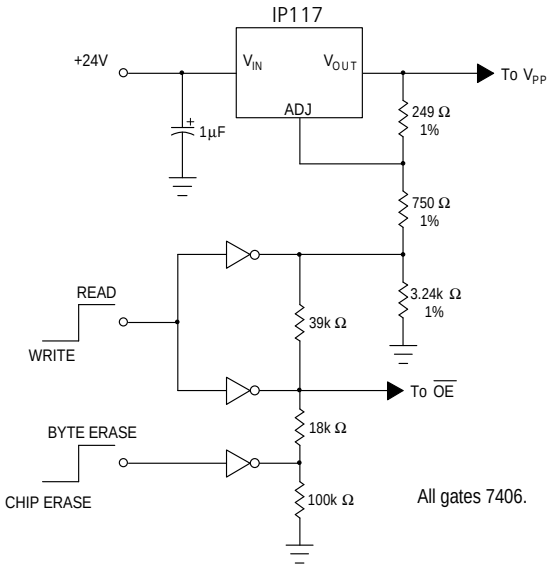
APPLICATIONS INFORMATION



Remote Sensing

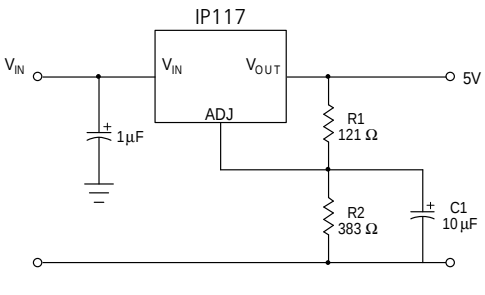


5V Regulator with Shut Down



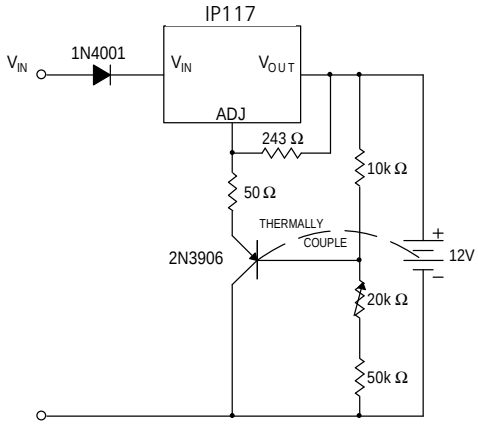
	OE	Vpp
READ	0V	5V
WRITE		
BYTE	5V	21V
CHIP ERASE	12V	21V

2816 EEPROM Supply Programmer for Read / Write Control



C1 improves ripple rejection
Xc should be small compared to R2

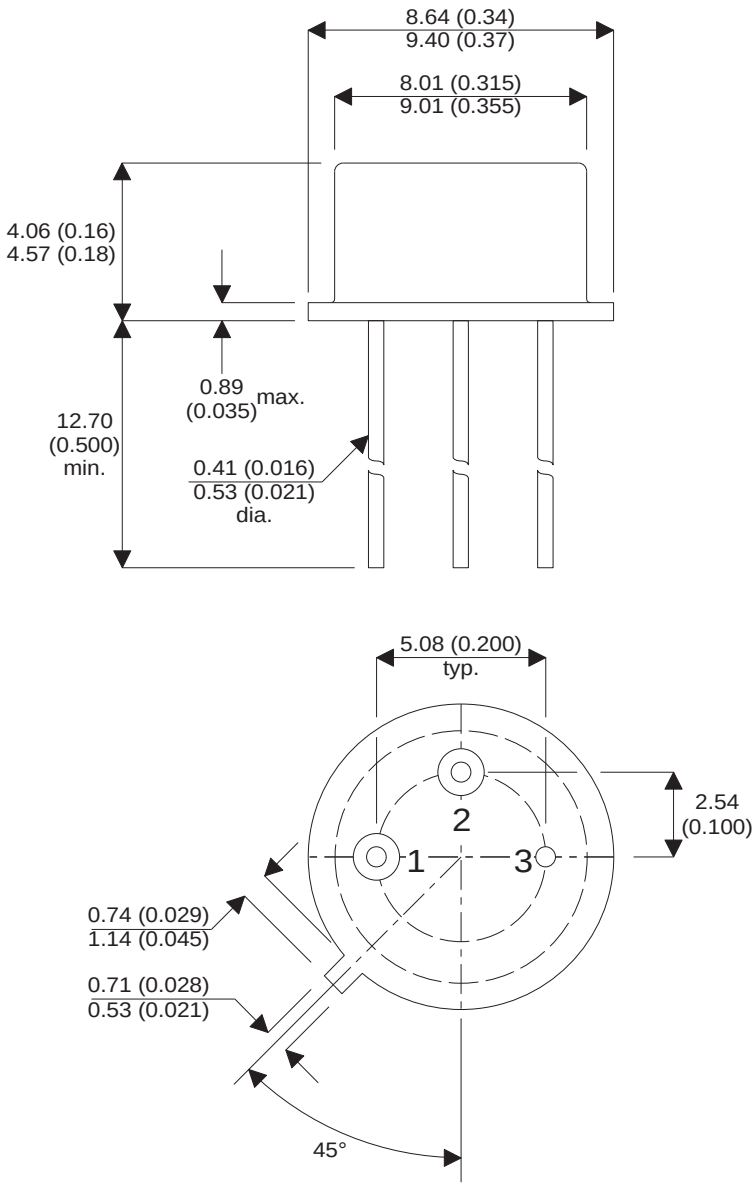
Improving Ripple Rejection



Temperature Compensated Lead-Acid Battery Charger

MECHANICAL DATA

Dimensions in mm (inches)



Low Profile TO39 (TO-205AF)
H Package
Underside View

Pin 1 = V_{IN} Pin 2 = ADJ Pin 3 = V_{OUT}