

# **BGE788**

# 750 MHz, 34 dB gain push-pull amplifier Rev. 04 — 30 March 2005

**Product data sheet** 



# 1.1 General description

Hybrid high dynamic range amplifier module in a SOT115J package operating at a supply voltage of 24 V (DC). The module consists of two cascaded stages both in cascode configuration.

#### **CAUTION**



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

#### 1.2 Features

- Excellent linearity
- Extremely low noise
- High gain
- Excellent return loss properties

## 1.3 Applications

Single module line extender in CATV systems operating in the 40 MHz to 750 MHz frequency range.

#### 1.4 Quick reference data

Table 1: Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Gp	power gain	f = 50 MHz	33.5	-	34.5	dB
		f = 750 MHz	34	-	-	dB
I <sub>tot</sub>	total current consumption (DC)	$V_B = 24 V$	<u>11</u> 290	-	320	mA

<sup>[1]</sup> The module normally operates at  $V_B = 24 \text{ V}$ , but is able to withstand supply transients up to 30 V.



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# 2. Pinning information

Table 2: Pinning

Pin	Description	Simplified outline	Symbol			
1	input					
2	common	1 3 5 7 9	5			
3	common		$\frac{1}{2}$			
5	+V <sub>B</sub>		2 3 7 8			
7	common		sym095			
8	common					
9	output					

# 3. Ordering information

**Table 3: Ordering information** 

Type number	Package				
	Name	Description	Version		
BGE788	-	rectangular single-ended package; aluminium flange; 2 vertical mounting holes; $2 \times 6$ -32 UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads	SOT115J		

# 4. Limiting values

Table 4: Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_B$	supply voltage		-	25	V
Vi	RF input voltage		-	55	dBmV
T <sub>stg</sub>	storage temperature		-40	+100	°C
T <sub>mb</sub>	mounting base temperat	ure	-20	+100	°C

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# 5. Characteristics

**Table 5: Characteristics** 

Bandwidth 40 MHz to 740 MHz;  $V_B = 24~V$ ;  $T_{case} = 30~^{\circ}C$ ;  $Z_S = Z_L = 75~\Omega$ ; unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
G <sub>p</sub>	power gain	f = 50 MHz		33.5	-	34.5	dB
		f = 750 MHz		34	-	-	dB
SL	slope cable equivalent	f = 40 MHz to 750 MHz		0.5	-	2.5	dB
FL	flatness of frequency response	f = 40 MHz to 750 MHz		-	-	±0.5	dB
S <sub>11</sub>	input return	f = 40 MHz to 80 MHz		20	-	-	dB
	losses	f = 80 MHz to 160 MHz		18.5	-	-	dB
		f = 160 MHz to 320 MHz		17	-	-	dB
		f = 320 MHz to 640 MHz		15.5	-	-	dB
		f = 640 MHz to 750 MHz		14	-	-	dB
S <sub>22</sub>	output return losses	f = 40 MHz to 80 MHz		20	-	-	dB
		f = 80 MHz to 160 MHz		18.5	-	-	dB
		f = 160 MHz to 320 MHz		17	-	-	dB
		f = 320 MHz to 640 MHz		15.5	-	-	dB
		f = 640 MHz to 750 MHz		14	-	-	dB
φ <sub>s21</sub>	phase response	f = 50 MHz		135	-	225	deg
СТВ	composite triple beat	110 channels flat; $V_0 = 44 \text{ dBmV}$ ; measured at 745.25 MHz		-	-	-49	dB
X <sub>mod</sub>	cross modulation	110 channels flat; $V_0 = 44 \text{ dBmV}$ ; measured at 55.25 MHz		-	-	<b>–51</b>	dB
CSO	composite second order distortion	110 channels flat; $V_0 = 44 \text{ dBmV}$ ; measured at 746.5 MHz		-	-	-52	dB
d <sub>2</sub>	second order distortion		[1]	-	-	-64	dB
Vo	output voltage	$d_{im} = -60 \text{ dB}$	[2]	58	-	-	dBmV
F	noise figure	f = 750 MHz		-	-	7	dB
PM	positive match	f = 40 MHz to 2 GHz		-	-	3	dB
l <sub>tot</sub>	total current consumption (DC)		[3]	290	-	320	mA

<sup>[1]</sup>  $f_p = 55.25$  MHz;  $V_p = 44$  dBmV;  $f_q = 691.25$  MHz;  $V_q = 44$  dBmV; measured at  $f_p + f_q = 746.5$  MHz.

<sup>[2]</sup> Measured according to DIN45004B;  $f_p$  = 740.25 MHz;  $V_p$  =  $V_o$ ;  $f_q$  = 747.25 MHz;  $V_q$  =  $V_o$  - 6 dB;  $f_r$  = 749.25 MHz;  $V_r$  =  $V_o$  - 6 dB; measured at  $f_p$  +  $f_q$  -  $f_r$  = 738.25 MHz.

<sup>[3]</sup> The module normally operates at  $V_B = 24$  V, but is able to withstand supply transients up to 30 V.



# 6. Package outline

Rectangular single-ended package; aluminium flange; 2 vertical mounting holes; 2 x 6-32 UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads

SOT115J

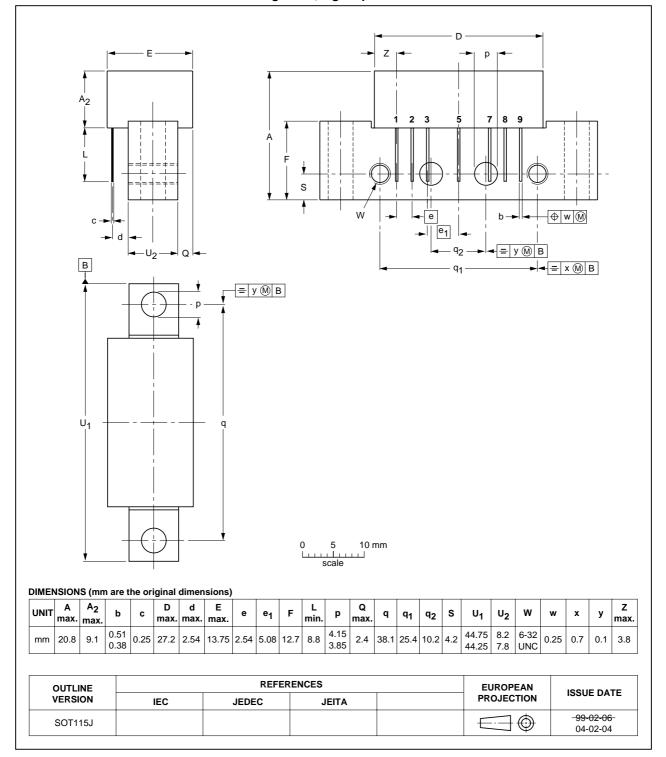


Fig 1. Package outline SOT115J

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# **Revision history**

#### Table 6: **Revision history**

Document ID	Release date	Data sheet status	Change notice	Doc. number	Supersedes
BGE788_4	20050330	Product data sheet	-	9397 750 14433	BGE788_3
Modifications:		t of this data sheet has been standard of Philips Semic		omply with the new	representation and
BGE788_3	20011115	Product specification	-	9397 750 08812	BGE788_2
BGE788_2	19980108	Product specification	-	9397 750 02981	BGE788_N_1
BGE788_N_1	19970505	Preliminary specification	-	9397 750 02294	-

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Level	Data sheet status [1]	Product status [2] [3]	Definition
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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