CAT3606

6-Channel Low Noise Charge Pump White LED Driver

Description

The CAT3606 controls up to four LEDs for the main display and two LEDs for the sub-display in cellular phones. The device is capable of operating in either 1x (LDO) mode or 1.5x charge pump mode. All LED pin currents are regulated and tightly matched to achieve uniformity of brightness across the LCD backlight. An external resistor (R_{SET}) sets the nominal output current.

The device can deliver as much as 20 mA per channel during low voltage operation (3 V), and 30 mA per channel during nominal operation (3.3 V). A constant high-frequency switching scheme (1 MHz) provides low noise and allows the use of very small value ceramic capacitors.

A “zero” quiescent current mode can be achieved via the chip enable pin EN. The Main and Sub LEDs each have their own dedicated ON/OFF control pins $\overline{\text{ENM}}$, $\overline{\text{ENS}}$. Dimming can be achieved using either a DC voltage to control the R_{SET} pin current, or by applying a PWM signal on the $\overline{\text{ENM}}$ and $\overline{\text{ENS}}$ pins.

The device is available in a 16−pad TQFN package with a max height of 0.8 mm.

Features

- Drives up to 4 Main LEDs and 2 Sub LEDs
- Separate Control for Main and Sub LEDs
- Compatible with Supply Voltage of 3 V to 5.5 V
- Power Efficiency up to 90%
- Output Current up to 30 mA per LED
- High−frequency Operation at 1 MHz
- 2 Modes of Operation 1x and 1.5x
- White LED Detect Circuitry on All Channels
- Shutdown Current less than 1 μA
- Small Ceramic Capacitors
- Soft Start and Current Limiting
- Short Circuit Protection
- 16−pad TQFN Package, 0.8 mm Max Height
- These Devices are Pb−Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- Cell Phone Main and Sub−display Backlight
- Navigation
- PDAs
- Digital Cameras

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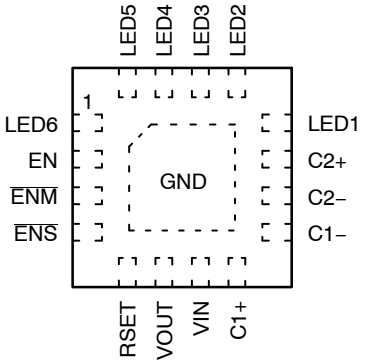
TQFN−16
HV4 SUFFIX
CASE 510AE

PIN CONNECTIONS (Note 1)



(4 x 4 mm) (Top View)

MARKING DIAGRAMS

G366 = CAT3606HV4−T2
CDBB = CAT3606HV4−GT2

ORDERING INFORMATION

Device	Package	Shipping
CAT3606HV4−T2	TQFN−16 (Note 2)	2,000/ Tape & Reel
CAT3606HV4−GT2	TQFN−16 (Note 3)	

1. The “exposed pad” under the package must be connected to the ground plane on the PCB.
2. Matte−Tin Plated Finish (RoHS−compliant).
3. NiPdAu Plated Finish (RoHS−compliant).

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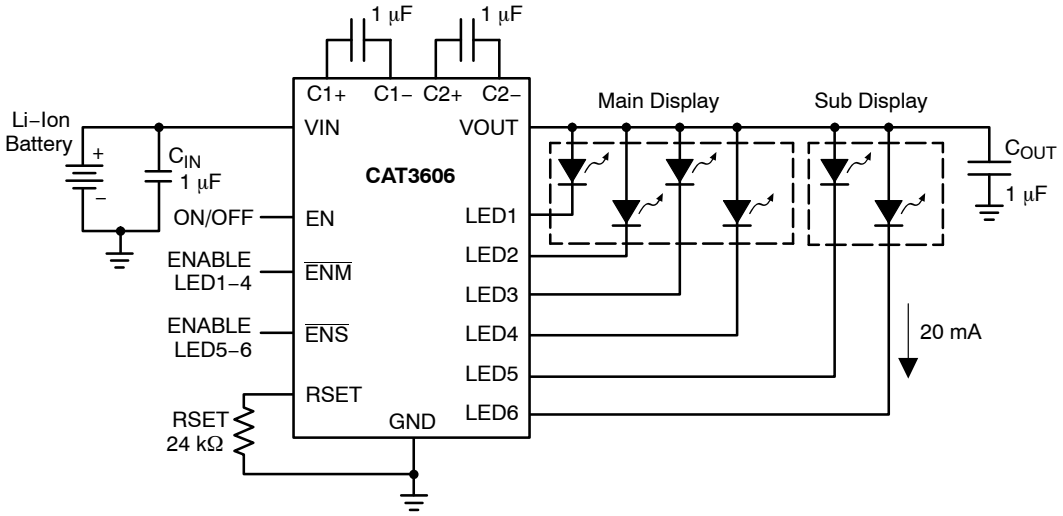


Figure 1. Typical Application Circuit

Table 1. PIN DESCRIPTION

Pin #	Name	Function
1	LED6	LED6 cathode terminal
2	EN	Enable/shutdown input, active high
3	$\overline{\text{ENM}}$	Enable “main” input for LED1 to LED4, active low
4	$\overline{\text{ENS}}$	Enable “sub” input for LED5 and LED6, active low
5	RSET	The LED output current is set by the current sourced out of the RSET pin
6	VOUT	Charge pump output connected to the LED anodes
7	VIN	Supply voltage
8	C1+	Bucket capacitor 1 terminal
9	C1	Bucket capacitor 1 terminal
10	C2	Bucket capacitor 2 terminal
11	C2+	Bucket capacitor 2 terminal
12	LED1	LED 1 cathode terminal
13	LED2	LED 2 cathode terminal
14	LED3	LED 3 cathode terminal
15	LED4	LED 4 cathode terminal
16	LED5	LED 5 cathode terminal
PAD	GND	Ground reference

Table 2. ABSOLUTE MAXIMUM RATINGS

Parameter	Rating	Unit
VIN, VOUT, LEDx voltage	−0.3 to 7.0	V
EN, $\overline{ENM}$, $\overline{ENS}$ voltage	−0.3 to VIN	V
RSET voltage	−0.3 to VIN	V
RSET current	±1	mA
Ambient Temperature Range	−40 to +85	°C
Storage Temperature Range	−65 to +160	°C
Lead Temperature	300	°C
ESD Rating HBM (Human Body Model)	2,000	V
ESD Rating MM (Machine Model) (Note 4)	200	V

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

4. Machine model is with 200 pF capacitor discharged directly into each pin.

Table 3. RECOMMENDED OPERATING CONDITIONS

Parameter	Range	Unit
VIN	3.0 to 5.5	V
Ambient Temperature Range	−40 to +85	°C
Input/Output/Bucket Capacitors	1 ±20% Typical	μF
I_{LED} per LED pin	0 to 30	mA
I_{OUT} Total Output Current	0 to 150	mA

Table 4. ELECTRICAL OPERATING CHARACTERISTICS

(Limits over recommended operating conditions unless specified otherwise. Typical values at T_A = 25°C, V_{IN} = 3.5 V, I_{RSET} = 5 μA.)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I_Q	Quiescent Current	V_{EN} = 0 V 1x Mode, No Load 1.5x Mode, No Load		0.1 0.3 2.6	1 1 5	μA mA mA
V_{RSET}	RSET Regulated Voltage		1.17	1.2	1.23	V
I_{LED}	Programmed LED Current	I_{RSET} = 5 μA I_{RSET} = 37 μA I_{RSET} = 78 μA		2.4 15.0 30.0		mA
I_{LED}	LED Current Range with 6 LEDs	3.3 ≤ VIN ≤ 4.5 V 3.0 ≤ VIN ≤ 4.5 V			30 20	mA
I_{LED}	LED Current Range with 4 LEDs	3.3 ≤ VIN ≤ 4.5 V			30	mA
$I_{LED-ACC}$	LED Current Accuracy	0.5 mA ≤ I_{LED} ≤ 3 mA 3 mA ≤ I_{LED} ≤ 30 mA		±15 ±5		%
$I_{LED-DEV}$	LED Channel Matching	(I_{LED} − I_{LEDAVG}) / I_{LEDAVG}		±3		%
R_{OUT}	Output Resistance (Open Loop)	1x Mode, 1.5x Mode, I_{OUT} = 100 mA		1.4 6.5	2.5 10	Ω
f_{OSC}	Charge Pump Frequency		0.8	1.0	1.3	MHz
$T_{DROPOUT}$	1x to 1.5x Mode Transition Dropout Delay		0.4	0.6	0.9	ms
I_{EN-CTR}	Input Leakage Current	On Inputs EN, $\overline{ENM}$, $\overline{ENS}$			1	μA
V_{EN-CTR}	High Detect Threshold Low Detect Threshold	On Inputs EN, $\overline{ENM}$, $\overline{ENS}$	 0.4	0.8 0.7	1.3	V
I_{SC}	Input Current Limit	VOUT = GND	30	45	60	mA
I_{LIM}	Maximum Input Current	VOUT > 1 V	200	400	600	mA

Block Diagram

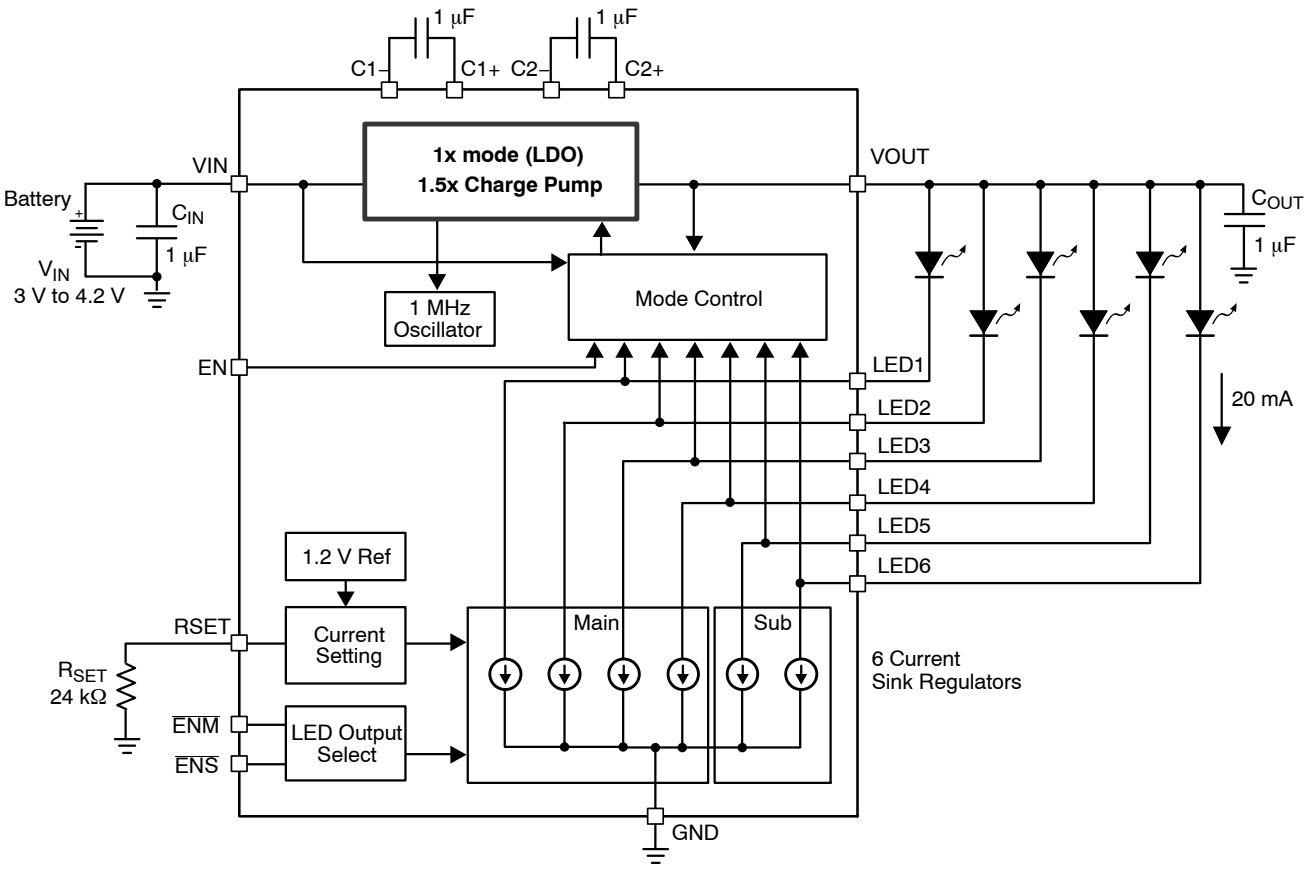


Figure 2. CAT3606 Functional Block Diagram

Basic Operation

At power-up, the CAT3606 starts operation in 1x mode. If it is able to drive the programmed LED current, it continues in 1x mode. If the battery voltage drops to a level where the LED current cannot be met, the driver automatically switches into 1.5x mode, to boost the output voltage high enough to achieve the nominal LED current.

The above sequence is reinitialized each and every time the chip is powered up or is taken out of shutdown mode (via EN pin). The use of the Main and Sub display enable pins ($\overline{ENM}$ or $\overline{ENS}$) does not affect the mode of operation.

LED Current Setting

The LED current is set by the external resistor R_{SET} connected between the RSET pin and ground. Table 5 lists various LED currents and the associated R_{SET} resistor value for standard 1% precision surface mount resistors.

Table 5. RSET Resistor Selection

LED Current (mA)	R_{SET} (kΩ)
1	649
2	287
5	102
10	49.9
15	32.4
20	23.7
30	15.4

The enable lines $\overline{ENM}$ and $\overline{ENS}$ allow to turn On or Off a group of LEDs as shown in Table 6.

Table 6. LED Selection

Control Lines			LED Outputs	
EN	$\overline{ENM}$	$\overline{ENS}$	Main LED1 – LED4	Sub LED5 – LED6
0	X	X	–	–
1	1	1	–	–
1	0	1	ON	–
1	1	0	–	ON
1	0	0	ON	ON

NOTES: 1 = logic high (or VIN)
0 = logic low (or GND)
– = LED output OFF
X = don't care

The unused LED channels can also be turned off by connecting the respective LED pins to VOUT. In which case, the corresponding LED driver is disabled and the typical LED sink current is only about 0.2 mA. When the following equation is true on any channel, the driver turns off the LED channel:

$$VOUT - V_{LED} \leq 1\ V\ (LED\ channel\ OFF)$$

Note: The CAT3606 is designed to drive LEDs with forward voltage greater than 1 V and is not compatible with resistive loads.

TYPICAL CHARACTERISTICS

(V_{IN} = 3.6 V, EN = V_{IN}, $\overline{ENM}$ = $\overline{ENS}$ = GND, C_{IN} = C_{OUT} = 1 μF, R_{SET} = 24 kΩ (20 mA per LED), T_{AMB} = 25°C, unless otherwise specified.)

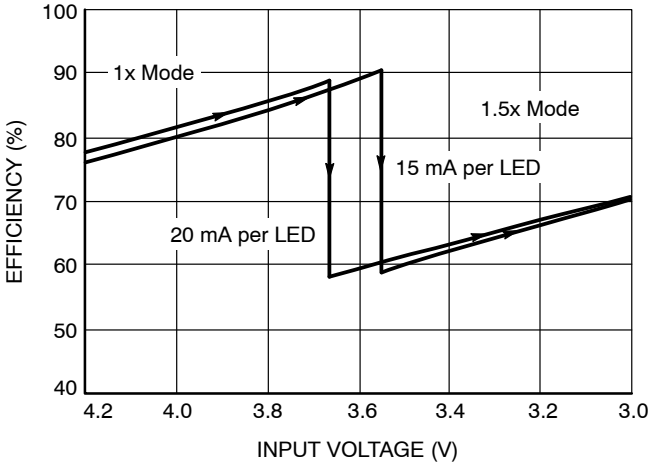


Figure 3. Efficiency vs. Input Voltage (6 LEDs)

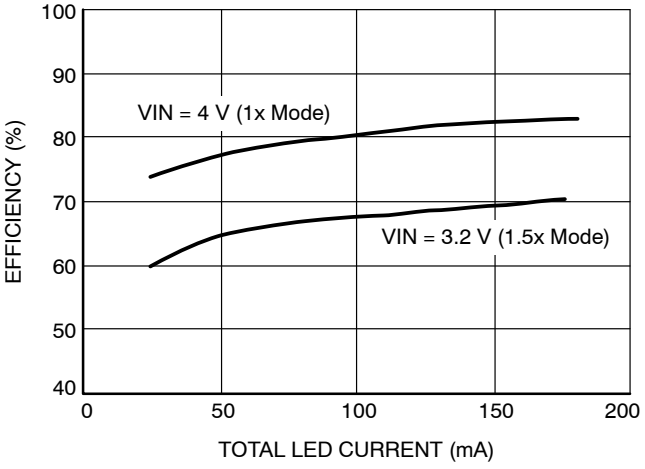


Figure 4. Efficiency vs. Total LED Current (6 LEDs)

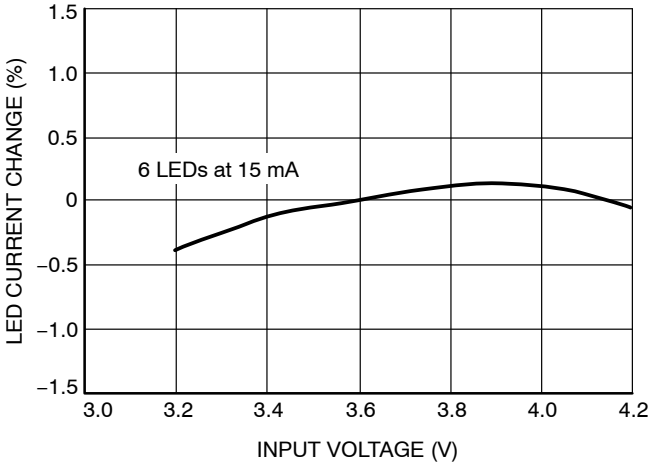


Figure 5. LED Current vs. Input Voltage

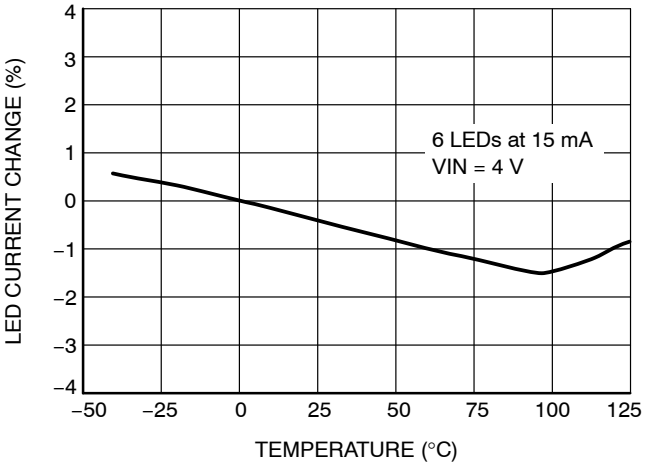


Figure 6. LED Current Change vs. Temperature

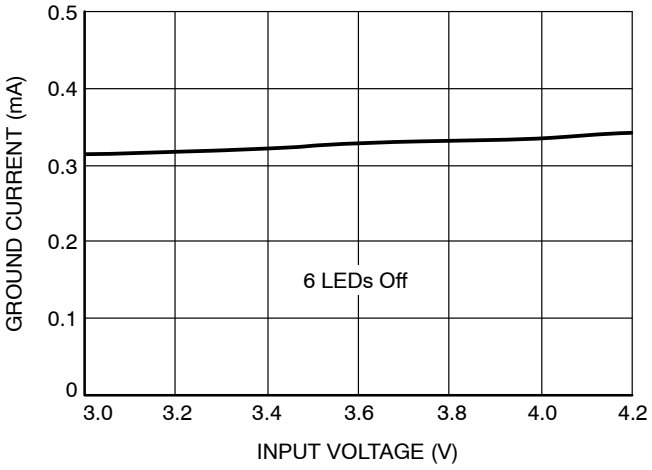


Figure 7. Ground Current vs. Input Voltage (1x Mode)

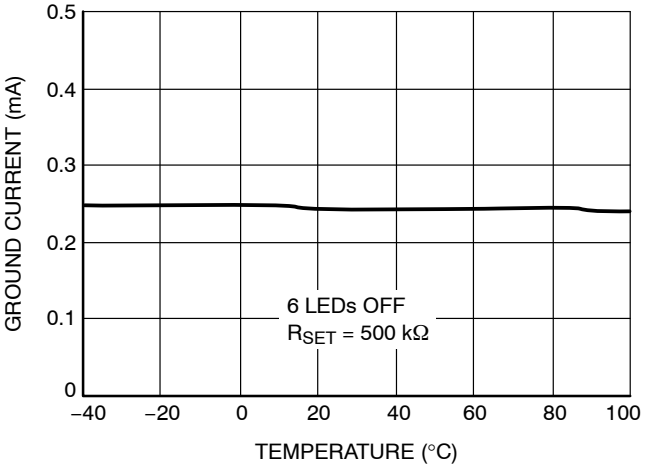


Figure 8. Ground Current vs. Temperature (1x Mode)

TYPICAL CHARACTERISTICS

(V_{IN} = 3.6 V, EN = V_{IN}, $\overline{ENM}$ = $\overline{ENS}$ = GND, C_{IN} = C_{OUT} = 1 μF, R_{SET} = 24 kΩ (20 mA per LED), T_{AMB} = 25°C, unless otherwise specified.)

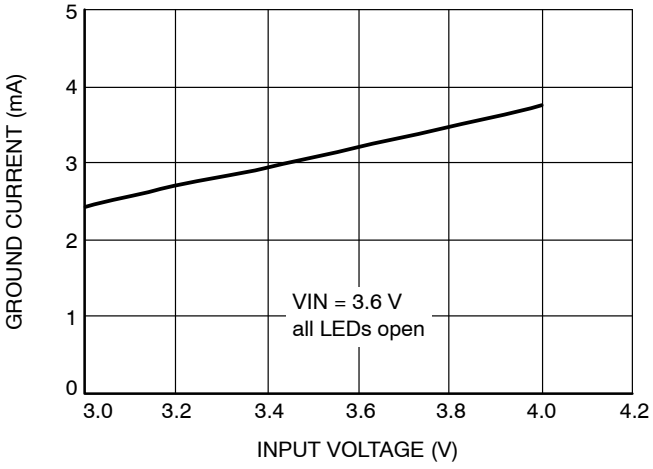


Figure 9. Ground Current vs. Input Voltage (1.5x Mode)

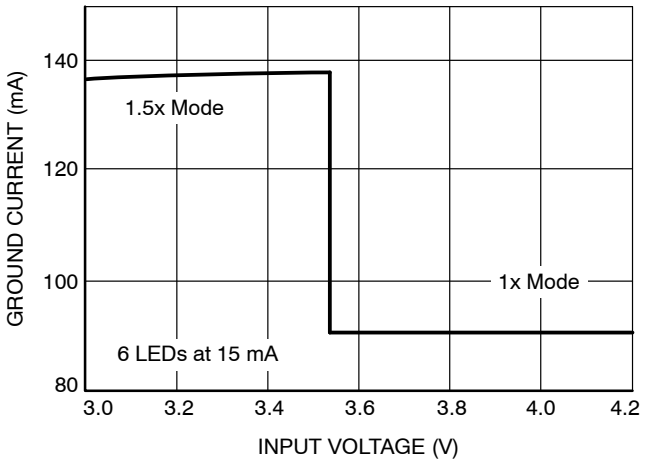


Figure 10. Supply Current vs. Input Voltage

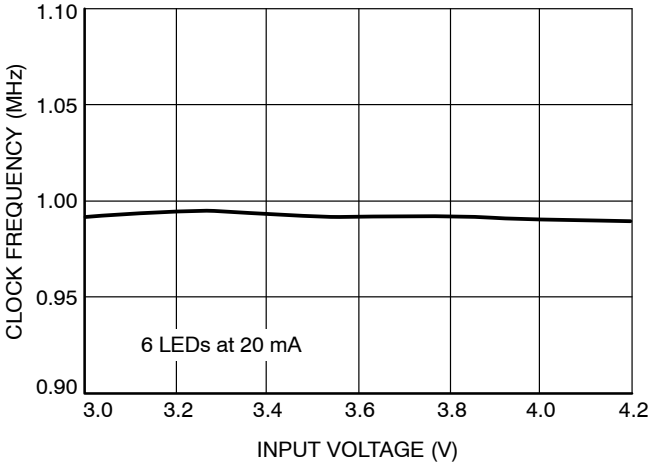


Figure 11. Oscillator Frequency vs. Input Voltage

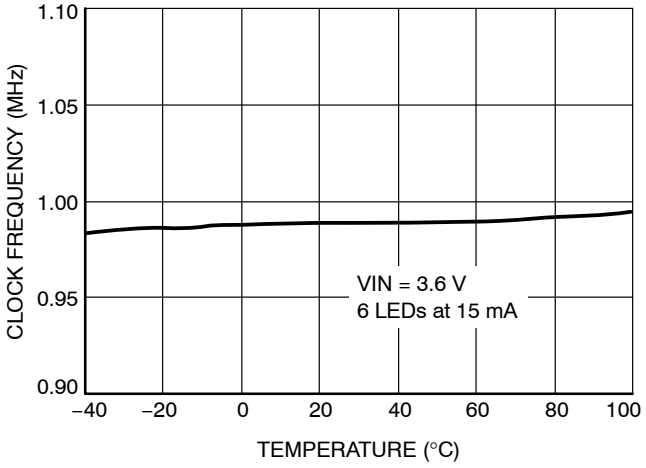


Figure 12. Oscillator Frequency vs. Temperature

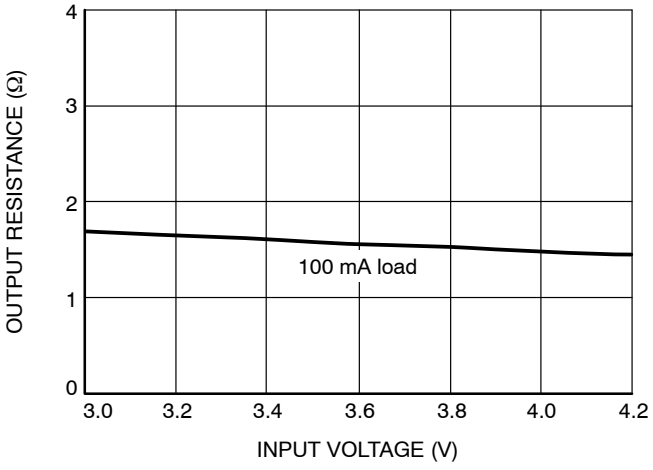


Figure 13. Output Resistance vs. Input Voltage (1x Mode)

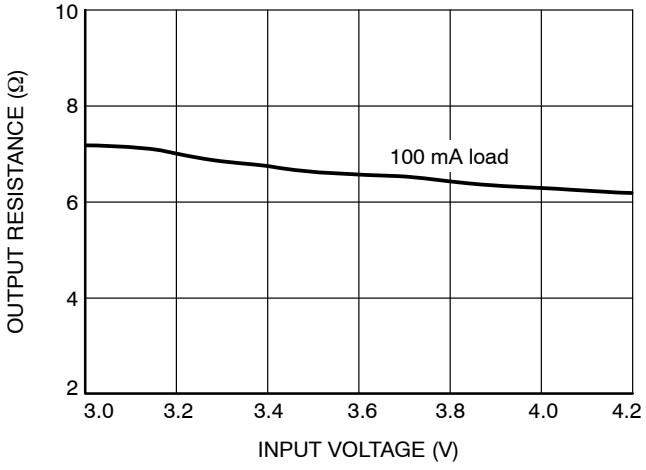


Figure 14. Output Resistance vs. Input Voltage (1.5x Mode)

TYPICAL CHARACTERISTICS

(V_{IN} = 3.6 V, EN = V_{IN}, $\overline{ENM}$ = $\overline{ENS}$ = GND, C_{IN} = C_{OUT} = 1 μF, R_{SET} = 24 kΩ (20 mA per LED), T_{AMB} = 25°C, unless otherwise specified.)

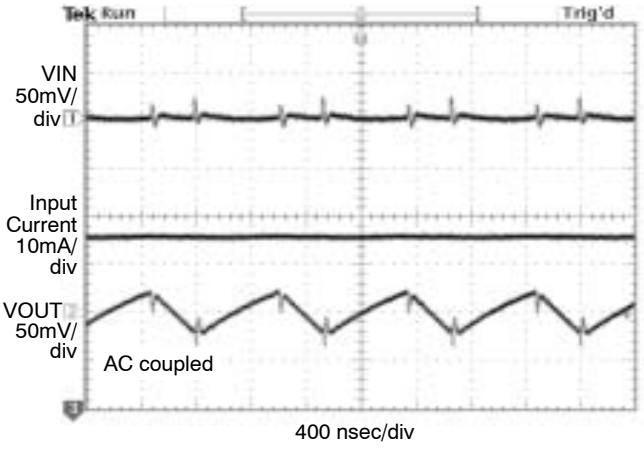


Figure 15. Switching Waveforms in 1.5x Mode

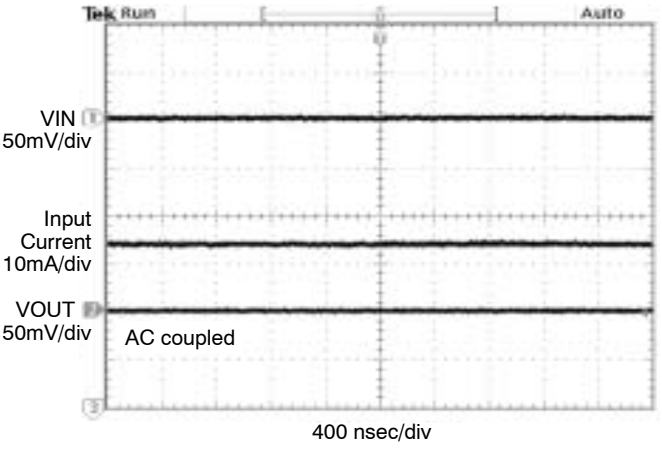


Figure 16. Operating Waveforms in 1x Mode

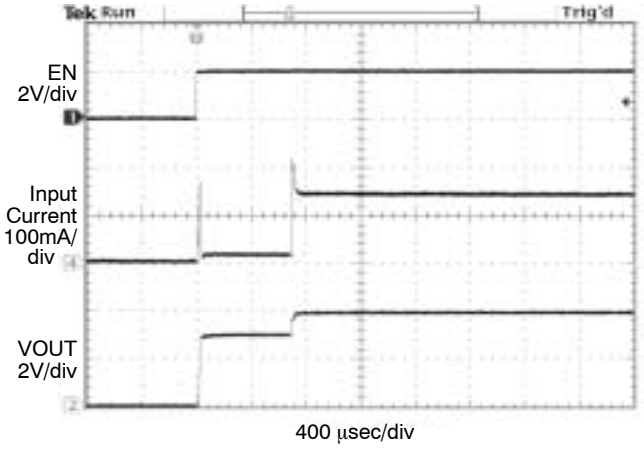


Figure 17. Power Up 6 LEDs at 15 mA, VIN = 3 V (1.5x Mode)

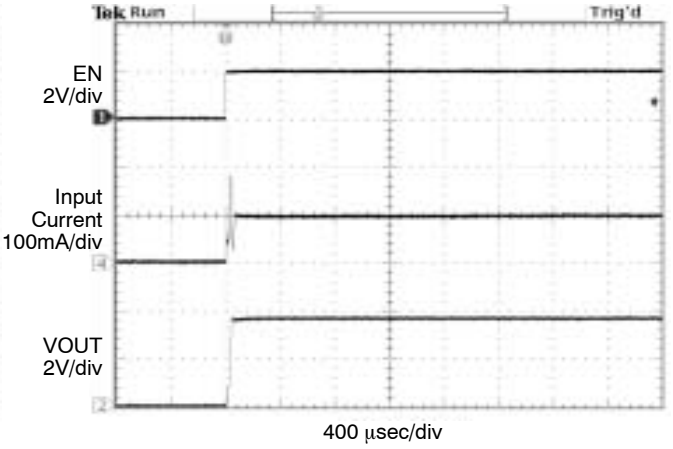


Figure 18. Power Up 6 LEDs at 15 mA, VIN = 3.6 V (1x Mode)

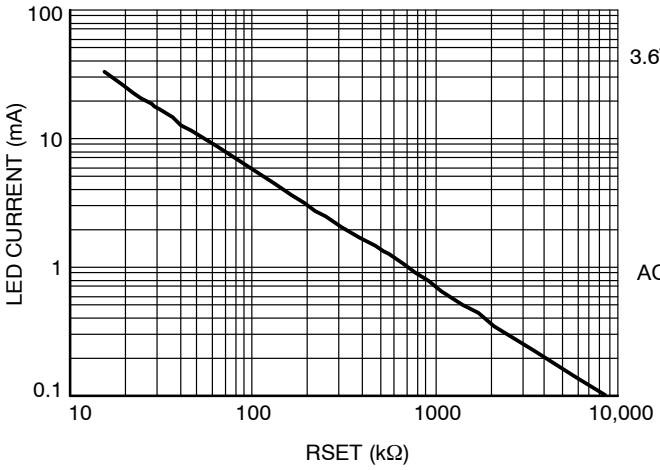


Figure 19. LED Current vs. R_{SET}

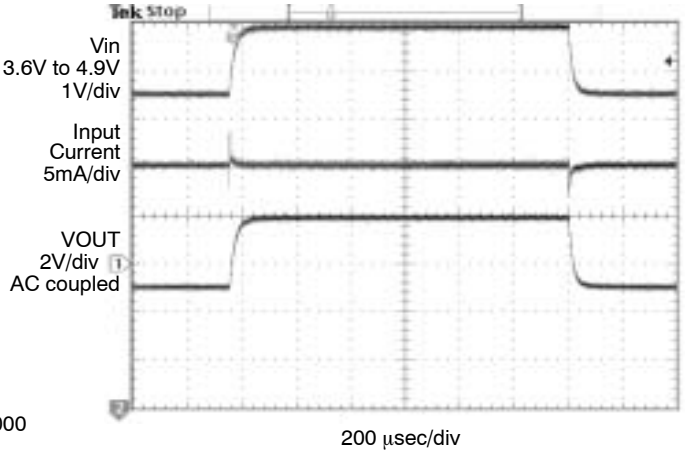


Figure 20. Line Transient Response in 1x Mode

TYPICAL CHARACTERISTICS

(V_{IN} = 3.6 V, EN = V_{IN}, $\overline{ENM}$ = $\overline{ENS}$ = GND, C_{IN} = C_{OUT} = 1 μF, T_{AMB} = 25°C, unless otherwise specified.)

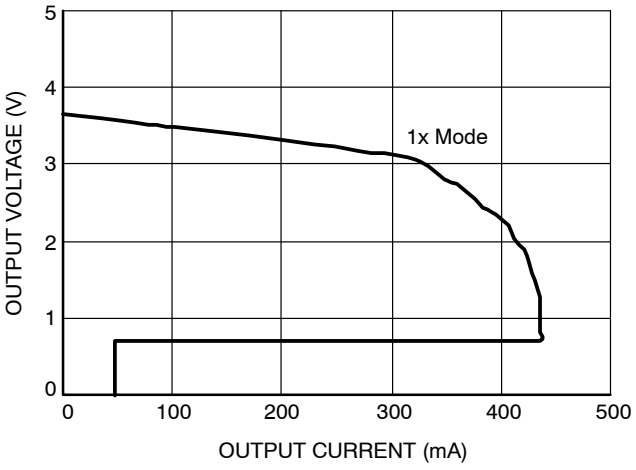


Figure 21. Foldback Current Limiting

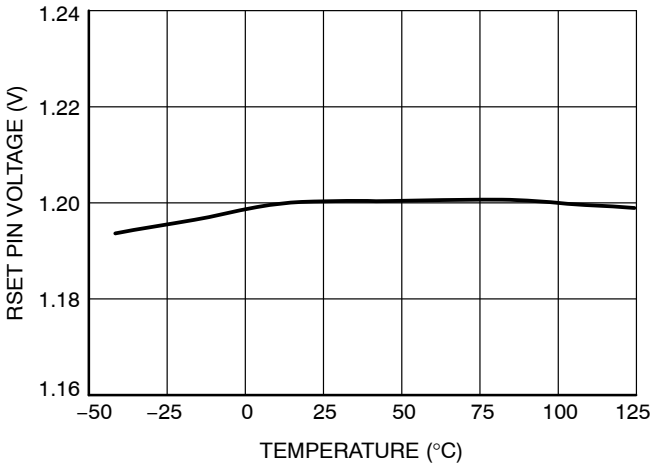


Figure 22. RSET Pin Voltage vs. Temperature

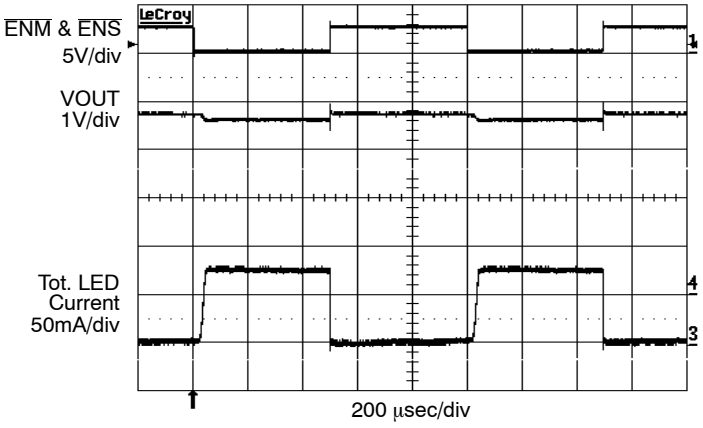


Figure 23. PWM Dimming at 1 kHz on $\overline{ENM}$ and $\overline{ENS}$

Recommended Layout

When the driver is in the 1.5x charge pump mode, the 1 MHz switching frequency operation requires to minimize trace length and impedance to ground on all 4 capacitors. A ground plane should cover the area on the bottom side of the PCB opposite to the IC and the bypass capacitors. Capacitors Cin and Cout require short connection to ground which can be done with multiple vias as shown on Figure 24. A square copper area matches the QFN16 exposed pad (GND) and must be connected to the ground plane underneath. The use of multiple via will improve the heat dissipation.

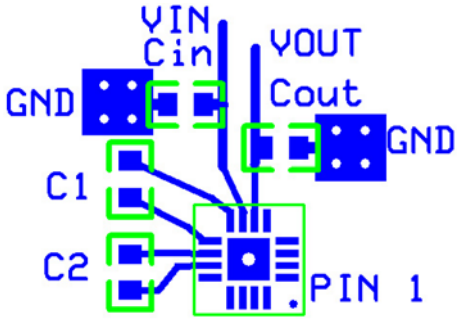
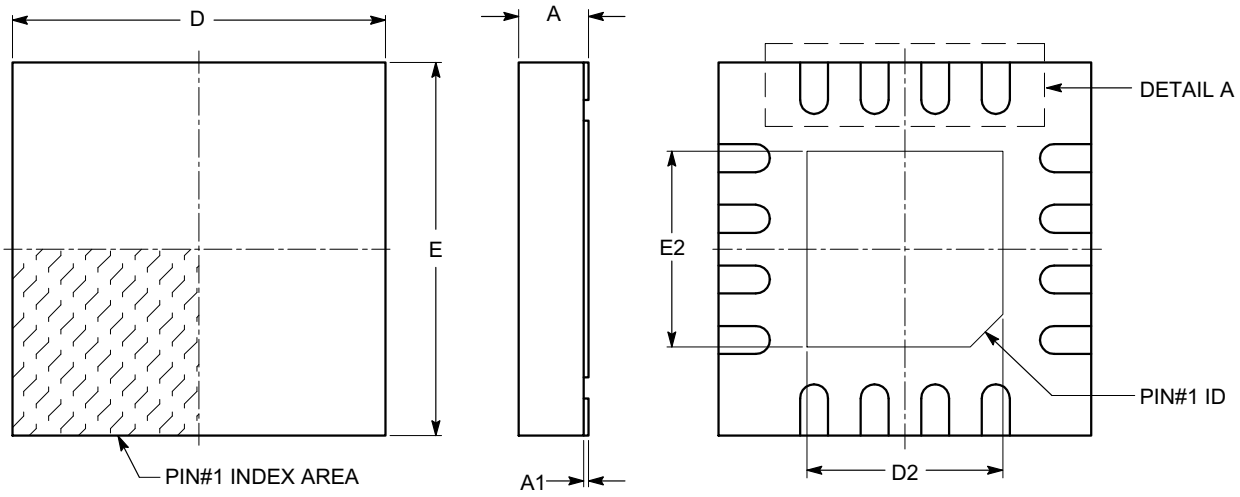


Figure 24. PCB Layout

PACKAGE DIMENSIONS

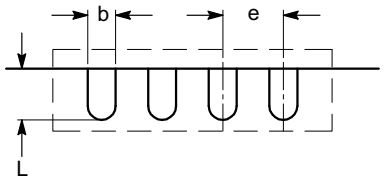
TQFN16, 4x4
CASE 510AE−01
ISSUE A



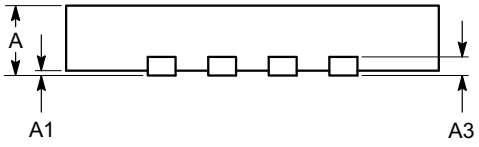
TOP VIEW **SIDE VIEW** **BOTTOM VIEW**

SYMBOL	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A3		0.20 REF	
b	0.25	0.30	0.35
D	3.90	4.00	4.10
D2	2.00	−−−	2.25
E	3.90	4.00	4.10
E2	2.00	−−−	2.25
e		0.65 BSC	
L	0.45	−−−	0.65

Notes:
(1) All dimensions are in millimeters.
(2) Complies with JEDEC MO-220.

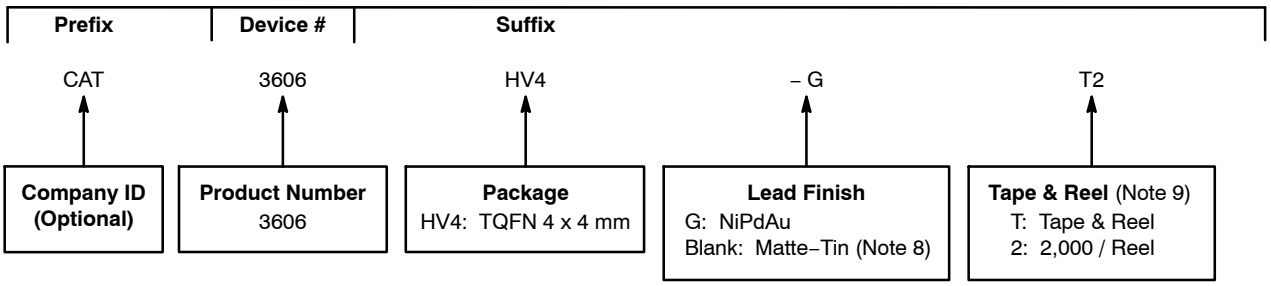


DETAIL A



FRONT VIEW

Example of Ordering Information (Note 7)



5. All packages are RoHS−compliant (Lead−free, Halogen−free).
6. The standard lead finish is NiPdAu.
7. The device used in the above example is a CAT3606HV4−GT2 (TQFN, NiPdAu Plated Finish, Tape & Reel, 2,000/Reel).
8. For Matte−Tin package option, please contact your nearest ON Semiconductor Sales office.
9. For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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