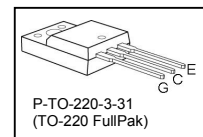
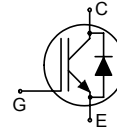


Low Loss DuoPack : IGBT in Trench and Fieldstop technology with soft, fast recovery anti-parallel EmCon HE diode

- Very low $V_{CE(sat)}$ 1.5 V (typ.)
- Maximum Junction Temperature 175°C
- Short circuit withstand time – 5μs
- Designed for :
 - Variable Speed Drive for washing machines, air conditioners and induction cooking
 - Uninterrupted Power Supply
- Trench and Fieldstop technology for 600 V applications offers :
 - very tight parameter distribution
 - high ruggedness, temperature stable behavior
 - very high switching speed
 - low $V_{CE(sat)}$
- NPT technology offers easy parallel switching capability due to positive temperature coefficient in $V_{CE(sat)}$
- Low EMI
- Low Gate Charge
- Very soft, fast recovery anti-parallel EmCon HE diode
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt>



Type	V_{CE}	I_C	$V_{CE(sat)}, T_J=25^\circ\text{C}$	$T_{j,max}$	Marking Code	Package	Ordering Code
IKA10N60T	600V	10A	1.5V	175°C	K10T60	TO-220-FP	Q67040S4683

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CE}	600	V
DC collector current, limited by $T_{j,max}$	I_C	20	A
$T_C = 25^\circ\text{C}$		10	
$T_C = 100^\circ\text{C}$		30	
Pulsed collector current, t_p limited by $T_{j,max}$	I_{Cpuls}	30	
Turn off safe operating area	-	30	
$V_{CE} \leq 400\text{V}, T_J \leq 150^\circ\text{C}$			
Diode forward current, limited by $T_{j,max}$	I_F	20	
$T_C = 25^\circ\text{C}$		10	
$T_C = 100^\circ\text{C}$		30	
Diode pulsed current, t_p limited by $T_{j,max}$	I_{Fpuls}	30	
Gate-emitter voltage	V_{GE}	± 20	V
Short circuit withstand time ¹⁾	t_{SC}	5	μs
$V_{GE} = 15\text{V}, V_{CC} \leq 400\text{V}, T_J \leq 150^\circ\text{C}$			
Power dissipation, $T_C = 25^\circ\text{C}$	P_{tot}	30	W
Operating junction temperature	T_J	-40...+175	°C
Storage temperature	T_{stg}	-55...+175	

¹⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction – case	R_{thJC}	TO-220 FullPak	5	K/W
Diode thermal resistance, junction – case	R_{thJCD}	TO-220 FullPak	5.8	
Thermal resistance, junction – ambient	R_{thJA}	TO-220 FullPak	80	

Electrical Characteristic, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0V, I_C=0.2mA$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{GE} = 15V, I_C=10A$ $T_j=25^{\circ}C$ $T_j=175^{\circ}C$	- -	1.5 1.8	2.05	
Diode forward voltage	V_F	$V_{GE}=0V, I_F=10A$ $T_j=25^{\circ}C$ $T_j=175^{\circ}C$	- -	1.6 1.6	2.05 -	
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C=0.3mA, V_{CE}=V_{GE}$	4.1	4.6	5.7	
Zero gate voltage collector current	I_{CES}	$V_{CE}=600V, V_{GE}=0V$ $T_j=25^{\circ}C$ $T_j=175^{\circ}C$	- -	- -	40 1000	μA
Gate-emitter leakage current	I_{GES}	$V_{CE}=0V, V_{GE}=20V$	-	-	100	nA
Transconductance	g_{fs}	$V_{CE}=20V, I_C=15A$	-	6	-	S
Integrated gate resistor	R_{Gint}		none			Ω

Dynamic Characteristic

Input capacitance	C_{iss}	$V_{CE}=25V,$ $V_{GE}=0V,$ $f=1MHz$	-	551	-	pF
Output capacitance	C_{oss}		-	40	-	
Reverse transfer capacitance	C_{rss}		-	17	-	
Gate charge	Q_{Gate}	$V_{CC}=480V, I_C=10A$ $V_{GE}=15V$	-	62	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E	TO-220-3-31	-	7	-	nH
Short circuit collector current ¹⁾	$I_{C(SC)}$	$V_{GE}=15V, t_{SC}\leq 5\mu s$ $V_{CC}=400V,$ $T_j=25^\circ\text{C}$	-	100	-	A

¹⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

Switching Characteristic, Inductive Load, at $T_j=25^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_J=25^{\circ}\text{C}$, $V_{CC}=400\text{V}$, $I_C=10\text{A}$, $V_{GE}=0/15\text{V}$, $R_G=23\Omega$, $L_{\sigma}^{2)}=60\text{nH}$, $C_{\sigma}^{2)}=40\text{pF}$	-	12	-	ns
Rise time	t_r		-	8	-	
Turn-off delay time	$t_{d(off)}$		-	215	-	
Fall time	t_f		-	38	-	
Turn-on energy	E_{on}	Energy losses include “tail” and diode reverse recovery.	-	0.16	-	mJ
Turn-off energy	E_{off}		-	0.27	-	
Total switching energy	E_{ts}		-	0.43	-	
Anti-Parallel Diode Characteristic						
Diode reverse recovery time	t_{rr}	$T_J=25^{\circ}\text{C}$, $V_R=400\text{V}$, $I_F=10\text{A}$, $di_F/dt=880\text{A}/\mu\text{s}$	-	115	-	ns
Diode reverse recovery charge	Q_{rr}		-	0.38	-	μC
Diode peak reverse recovery current	I_{rrm}		-	10	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	680	-	A/ μs

Switching Characteristic, Inductive Load, at $T_j=175^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_J=175^{\circ}\text{C}$, $V_{CC}=400\text{V}$, $I_C=10\text{A}$, $V_{GE}=0/15\text{V}$, $R_G=23\Omega$ $L_{\sigma}^{1)}=60\text{nH}$, $C_{\sigma}^{1)}=40\text{pF}$	-	10	-	ns
Rise time	t_r		-	11	-	
Turn-off delay time	$t_{d(off)}$		-	233	-	
Fall time	t_f		-	63	-	
Turn-on energy	E_{on}	Energy losses include “tail” and diode reverse recovery.	-	0.26	-	mJ
Turn-off energy	E_{off}		-	0.35	-	
Total switching energy	E_{ts}		-	0.61	-	
Anti-Parallel Diode Characteristic						
Diode reverse recovery time	t_{rr}	$T_J=175^{\circ}\text{C}$ $V_R=400\text{V}$, $I_F=10\text{A}$, $di_F/dt=880\text{A}/\mu\text{s}$	-	200	-	ns
Diode reverse recovery charge	Q_{rr}		-	0.92	-	μC
Diode peak reverse recovery current	I_{rrm}		-	13	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	390	-	A/ μs

²⁾ Leakage inductance L_{σ} and Stray capacity C_{σ} due to dynamic test circuit in Figure E.

¹⁾ Leakage inductance L_{σ} and Stray capacity C_{σ} due to dynamic test circuit in Figure E.

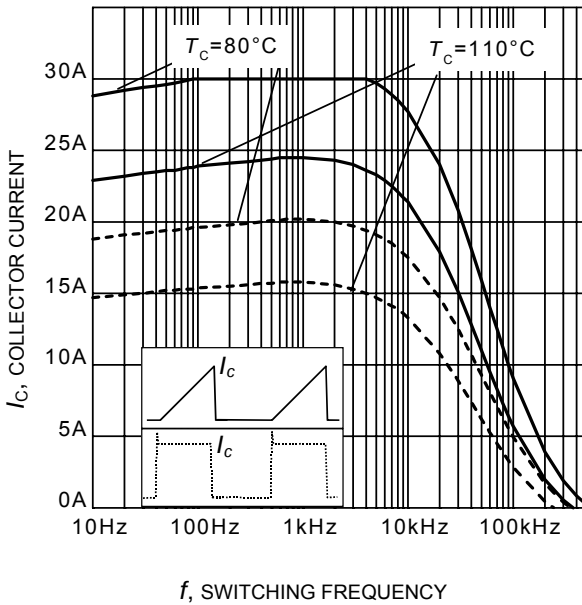


Figure 1. Collector current as a function of switching frequency
 $(T_J \leq 175^\circ\text{C}, D = 0.5, V_{CE} = 400\text{V}, V_{GE} = 0/+15\text{V}, R_G = 23\Omega)$

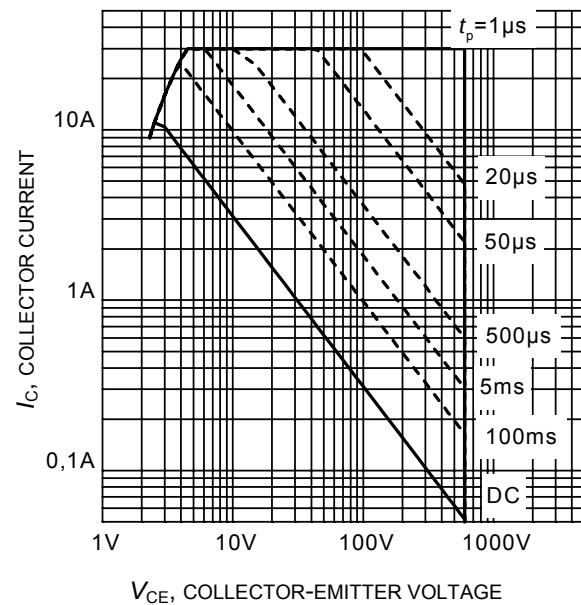


Figure 2. Safe operating area
 $(D = 0, T_C = 25^\circ\text{C}, T_J \leq 175^\circ\text{C}; V_{GE} = 15\text{V})$

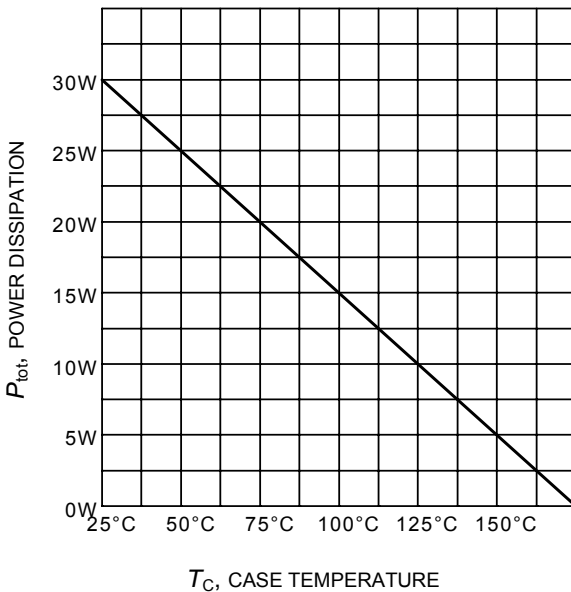


Figure 3. Power dissipation as a function of case temperature
 $(T_J \leq 175^\circ\text{C})$

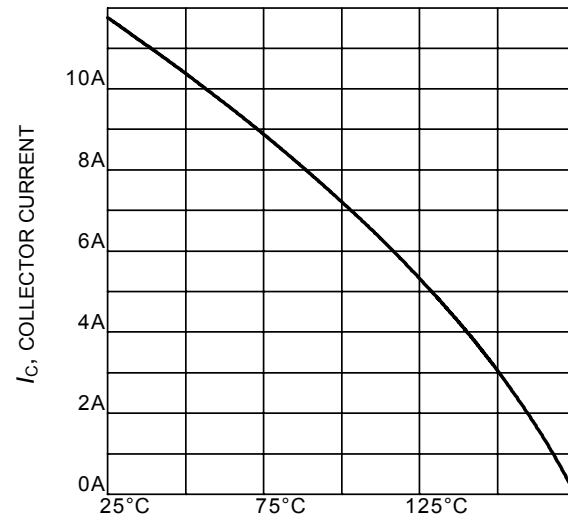


Figure 4. Collector current as a function of case temperature
 $(V_{GE} \geq 15\text{V}, T_J \leq 175^\circ\text{C})$

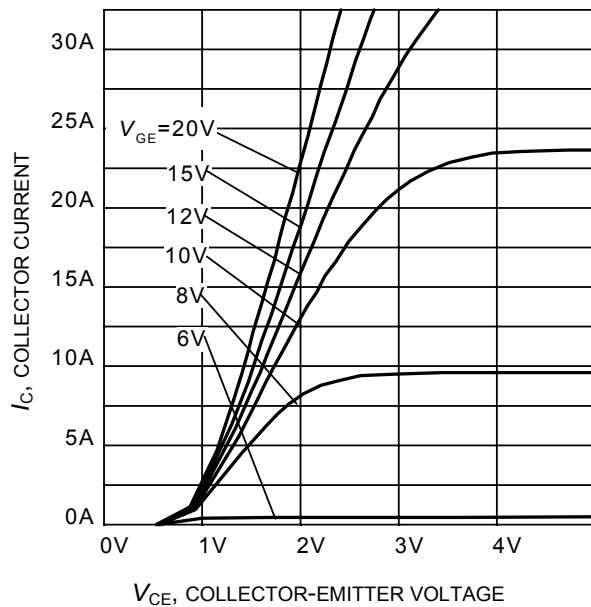


Figure 5. Typical output characteristic
($T_j = 25^\circ\text{C}$)

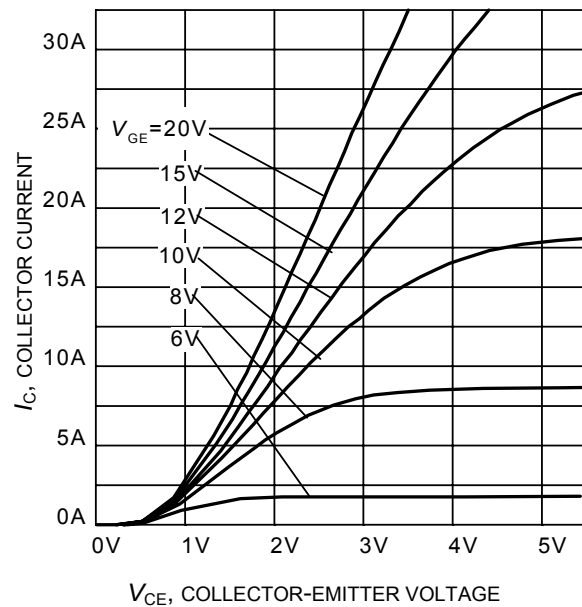


Figure 6. Typical output characteristic
($T_j = 175^\circ\text{C}$)

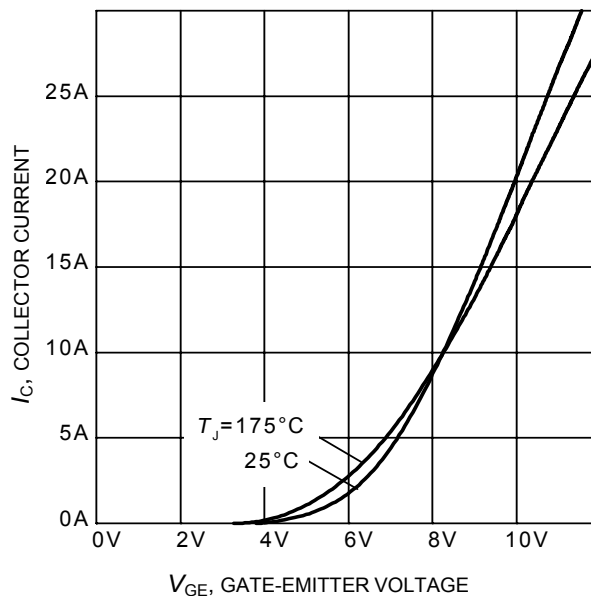


Figure 7. Typical transfer characteristic
($V_{CE}=20\text{V}$)

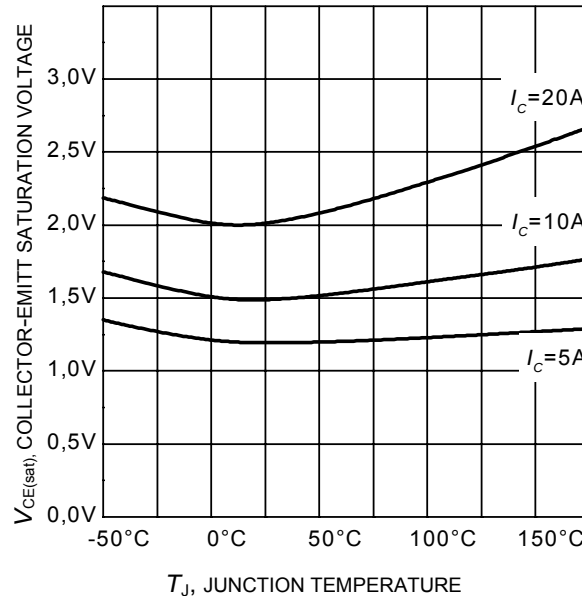


Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature
($V_{GE} = 15\text{V}$)

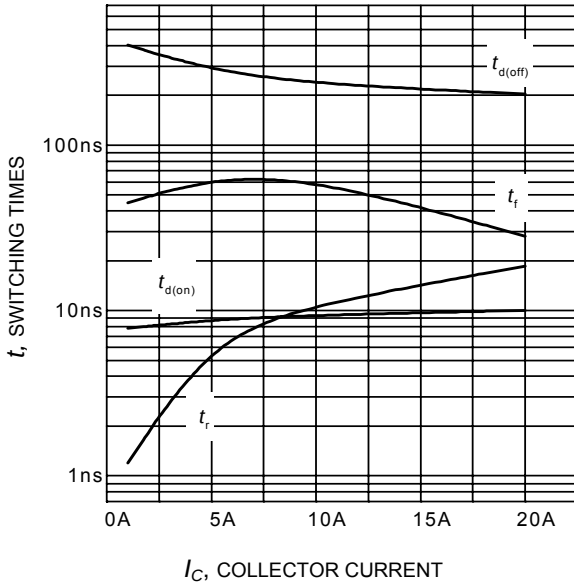


Figure 9. Typical switching times as a function of collector current
(inductive load, $T_J = 175^\circ\text{C}$,
 $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $R_G = 23\Omega$,
Dynamic test circuit in Figure E)

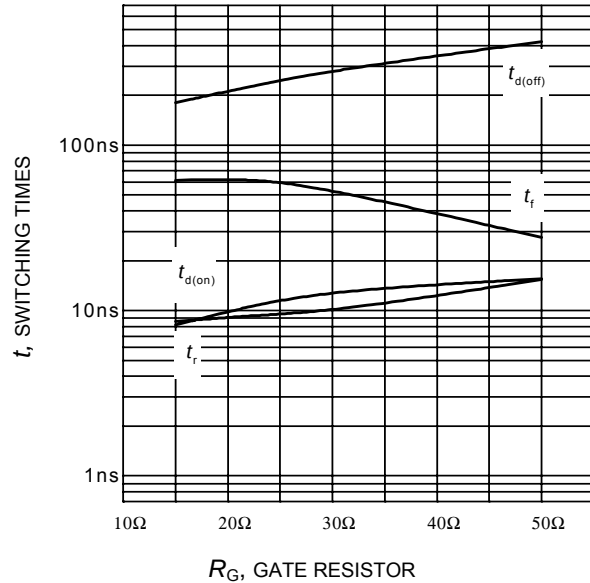


Figure 10. Typical switching times as a function of gate resistor
(inductive load, $T_J = 175^\circ\text{C}$,
 $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $I_C = 10\text{A}$,
Dynamic test circuit in Figure E)

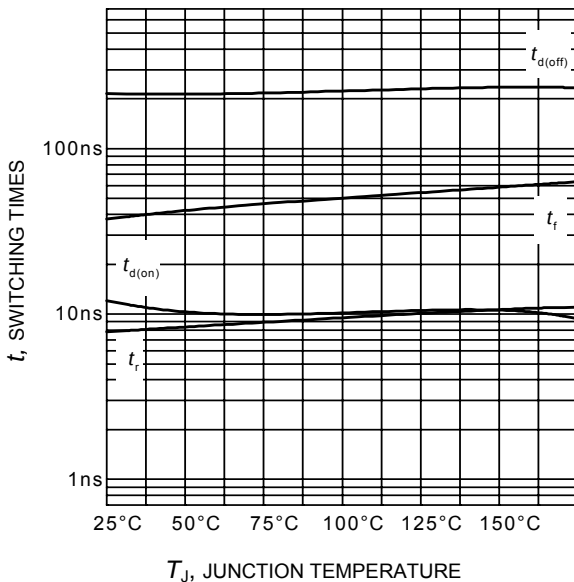


Figure 11. Typical switching times as a function of junction temperature
(inductive load, $V_{CE} = 400\text{V}$,
 $V_{GE} = 0/15\text{V}$, $I_C = 10\text{A}$, $R_G = 23\Omega$,
Dynamic test circuit in Figure E)

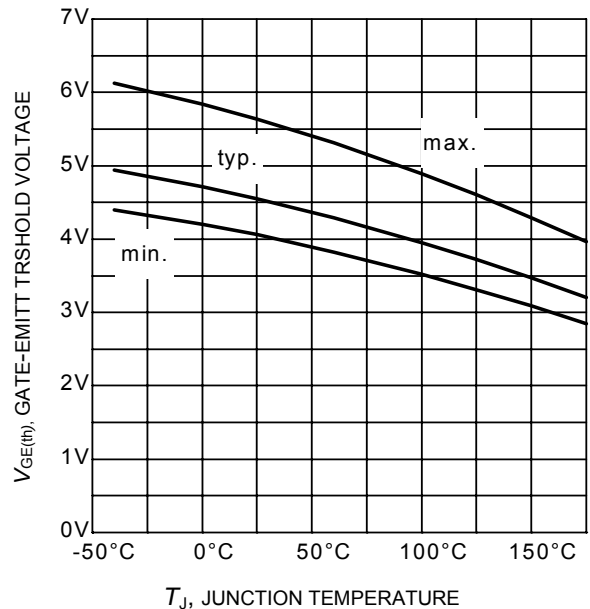


Figure 12. Gate-emitter threshold voltage as a function of junction temperature
($I_C = 0.3\text{mA}$)

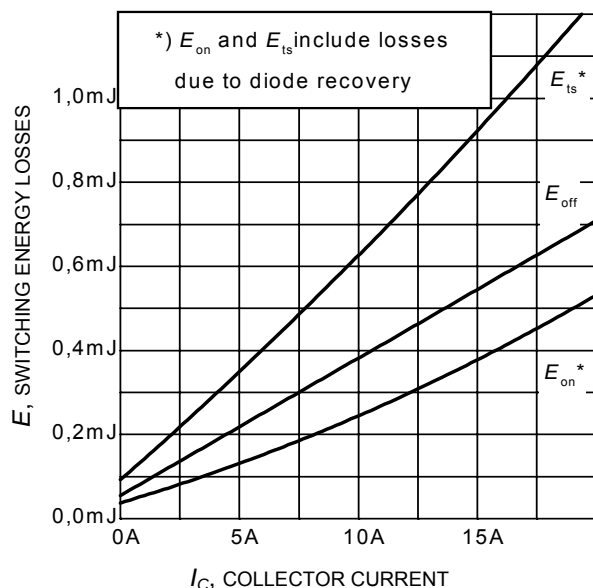


Figure 13. Typical switching energy losses as a function of collector current
(inductive load, $T_J = 175^\circ\text{C}$, $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $R_G = 23\Omega$, Dynamic test circuit in Figure E)

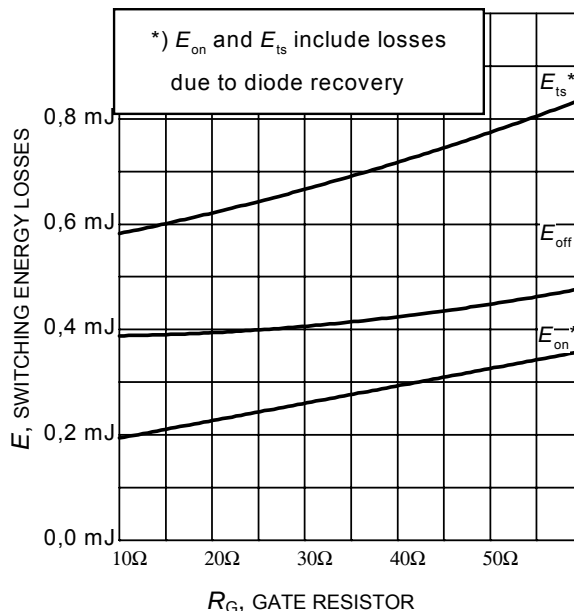


Figure 14. Typical switching energy losses as a function of gate resistor
(inductive load, $T_J = 175^\circ\text{C}$, $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $I_C = 10\text{A}$, Dynamic test circuit in Figure E)

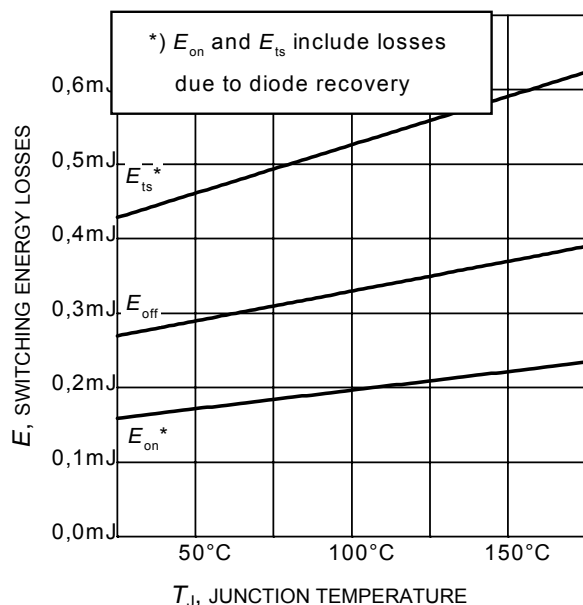


Figure 15. Typical switching energy losses as a function of junction temperature
(inductive load, $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $I_C = 10\text{A}$, $R_G = 23\Omega$, Dynamic test circuit in Figure E)

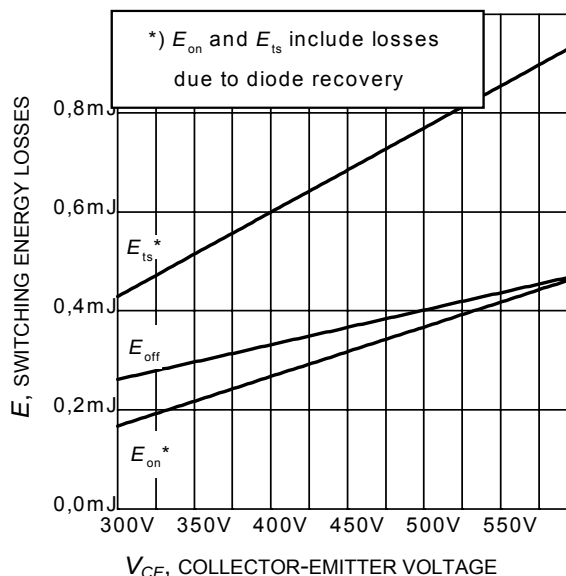


Figure 16. Typical switching energy losses as a function of collector emitter voltage
(inductive load, $T_J = 175^\circ\text{C}$, $V_{GE} = 0/15\text{V}$, $I_C = 10\text{A}$, $R_G = 23\Omega$, Dynamic test circuit in Figure E)

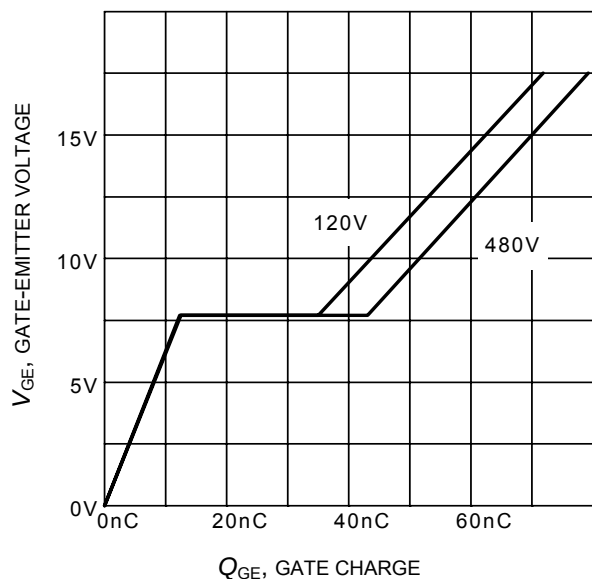


Figure 17. Typical gate charge
($I_C = 10\text{ A}$)

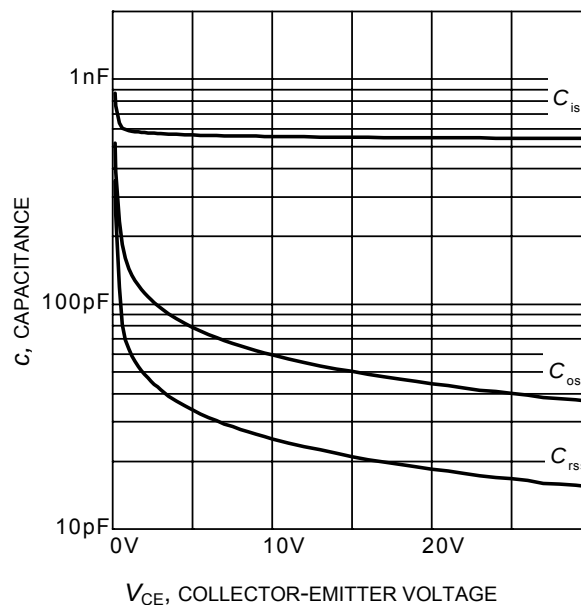


Figure 18. Typical capacitance as a function of collector-emitter voltage
($V_{GE} = 0\text{ V}$, $f = 1\text{ MHz}$)

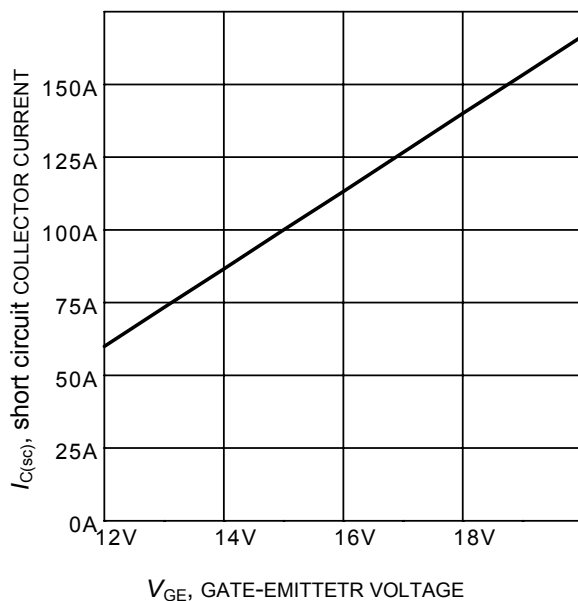


Figure 19. Typical short circuit collector current as a function of gate-emitter voltage
($V_{CE} \leq 400\text{ V}$, $T_J \leq 150^\circ\text{C}$)

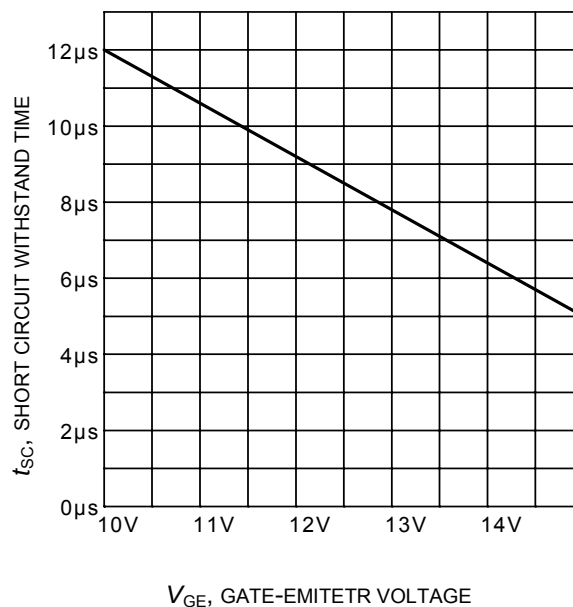


Figure 20. Short circuit withstand time as a function of gate-emitter voltage
($V_{CE} = 600\text{ V}$, start at $T_J = 25^\circ\text{C}$, $T_{Jmax} < 150^\circ\text{C}$)

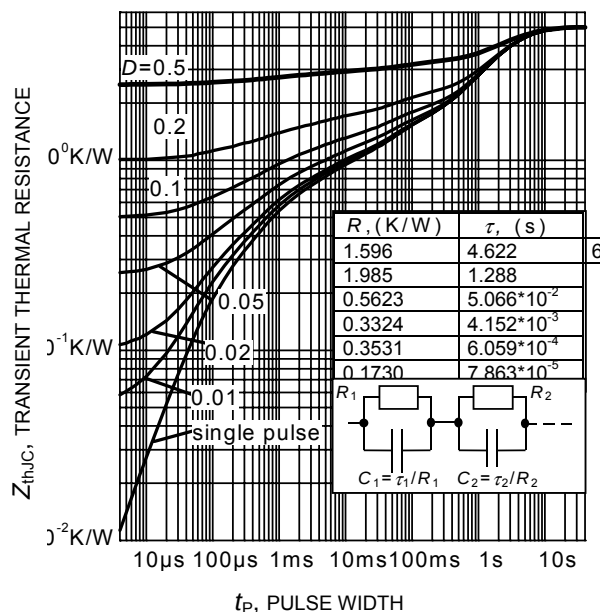


Figure 21. IGBT transient thermal resistance
($D = t_p / T$)

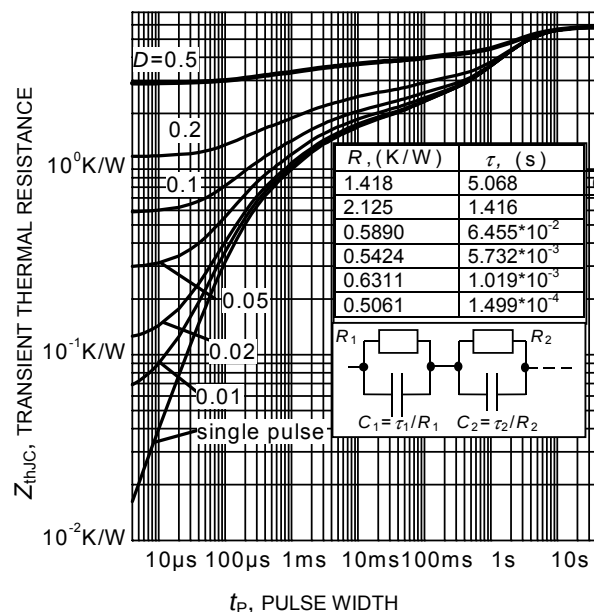


Figure 22. Diode transient thermal impedance as a function of pulse width
($D = t_p / T$)

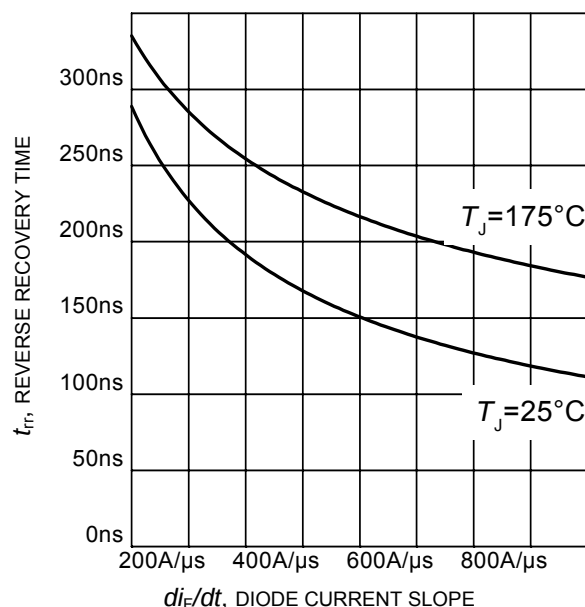


Figure 23. Typical reverse recovery time as a function of diode current slope
($V_R = 400V$, $I_F = 10A$,
Dynamic test circuit in Figure E)

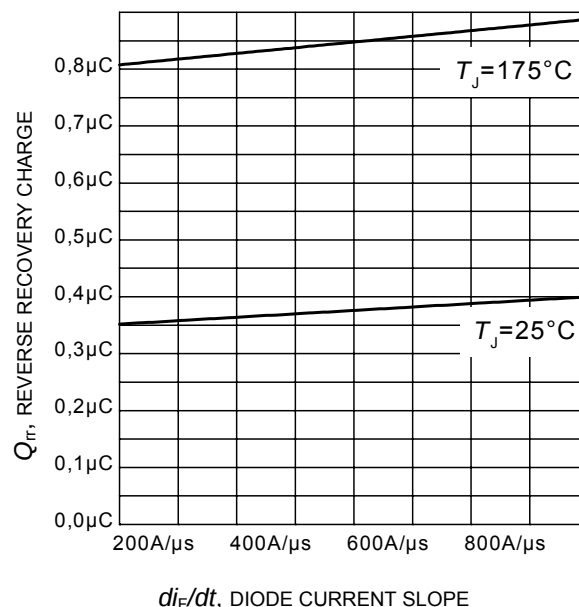
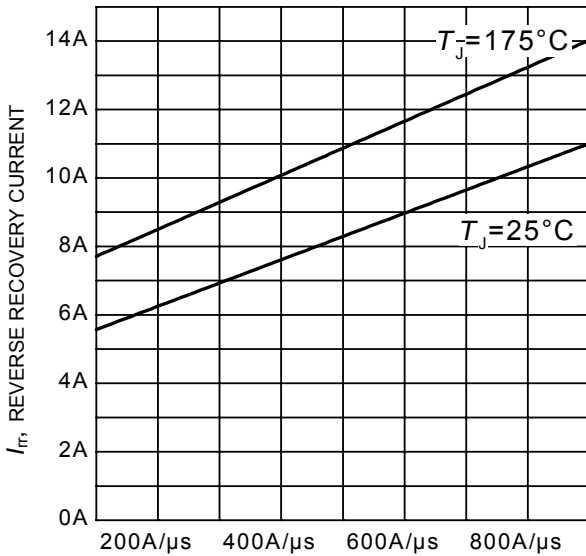


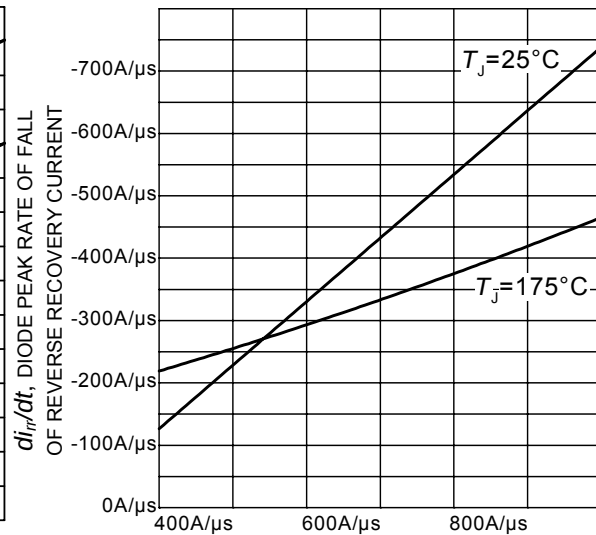
Figure 24. Typical reverse recovery charge as a function of diode current slope
($V_R = 400V$, $I_F = 10A$,
Dynamic test circuit in Figure E)



di_F/dt , DIODE CURRENT SLOPE

Figure 25. Typical reverse recovery current as a function of diode current slope

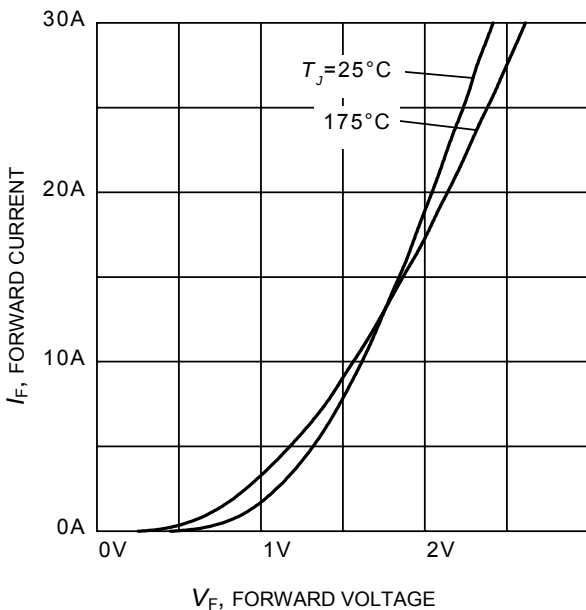
($V_R = 400V$, $I_F = 10A$,
Dynamic test circuit in Figure E)



di_F/dt , DIODE CURRENT SLOPE

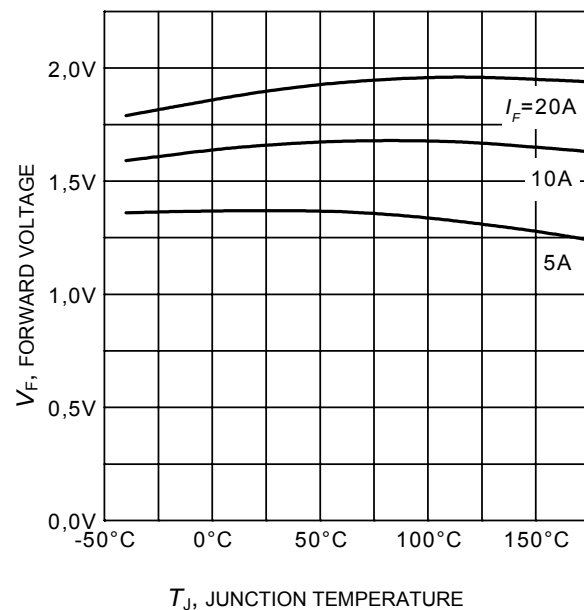
Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope

($V_R = 400V$, $I_F = 10A$,
Dynamic test circuit in Figure E)



V_F , FORWARD VOLTAGE

Figure 27. Typical diode forward current as a function of forward voltage



T_J , JUNCTION TEMPERATURE

Figure 28. Typical diode forward voltage as a function of junction temperature

P-TO220-3-31

The image shows a mechanical drawing of a P-TO220-3-31 package. The top view (left) shows a rectangular package with a central circular feature. Dimensions A, B, C, D, E, H, L, M, and T are indicated. The side view (right) shows the package profile with dimensions N, P, F, and K. The package is labeled with '1 2 3' on the top surface and 'H' on the side.

symbol	dimensions			
	[mm]		[inch]	
	min	max	min	max
A	10.37	10.63	0.4084	0.4184
B	15.86	16.12	0.6245	0.6345
C	0.65	0.78	0.0256	0.0306
D	2.95 typ.			
E	3.15	3.25	0.124	0.128
F	6.05	6.56	0.2384	0.2584
G	13.47	13.73	0.5304	0.5404
H	3.18	3.43	0.125	0.135
K	0.45	0.63	0.0177	0.0247
L	1.23	1.36	0.0484	0.0534
M	2.54 typ.			
N	4.57	4.83	0.1800	0.1900
P	2.57	2.83	0.1013	0.1113
T	2.51	2.62	0.0990	0.1030

Please refer to mounting instructions (application note AN-TO220-3-31-01)

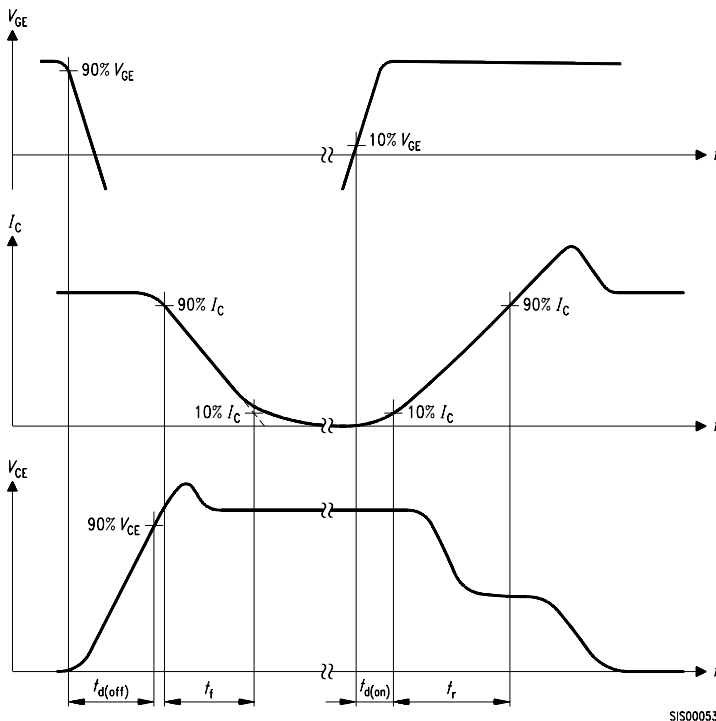


Figure A. Definition of switching times

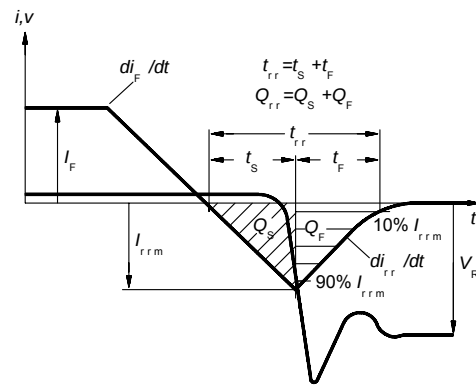


Figure C. Definition of diodes switching characteristics

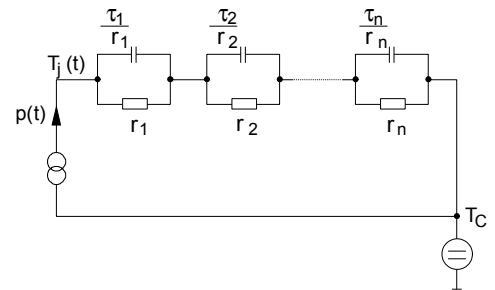


Figure D. Thermal equivalent circuit

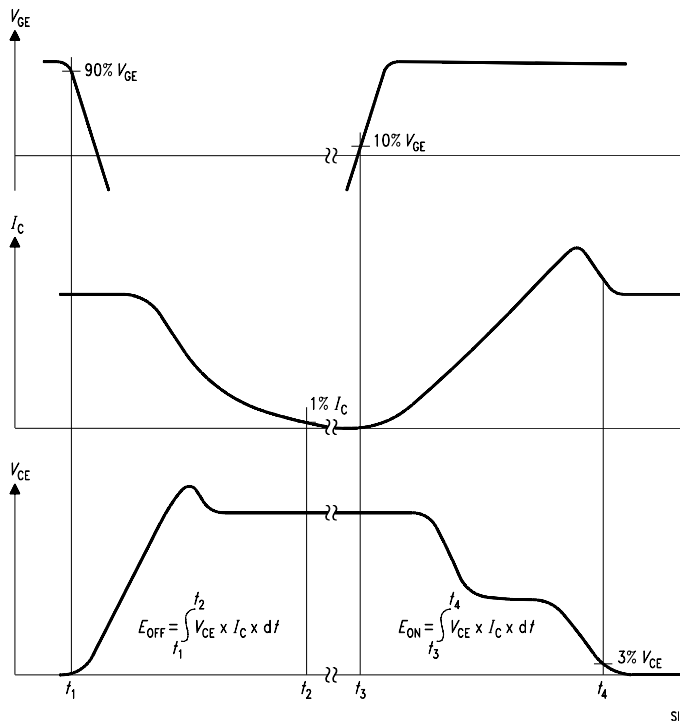


Figure B. Definition of switching losses

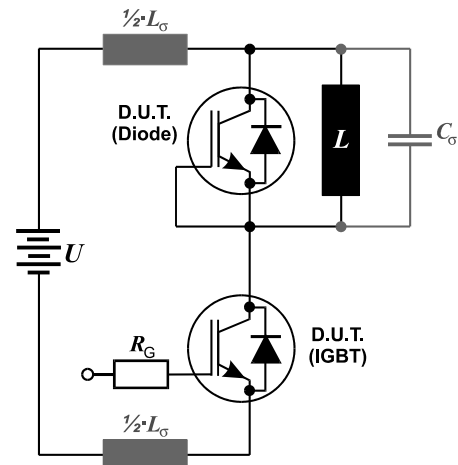


Figure E. Dynamic test circuit
Leakage inductance $L_\sigma = 60\text{nH}$
and Stray capacity $C_\sigma = 40\text{pF}$.

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