

DM74ALS540A

Octal Inverting Buffer and Line Driver with 3-STATE Outputs

General Description

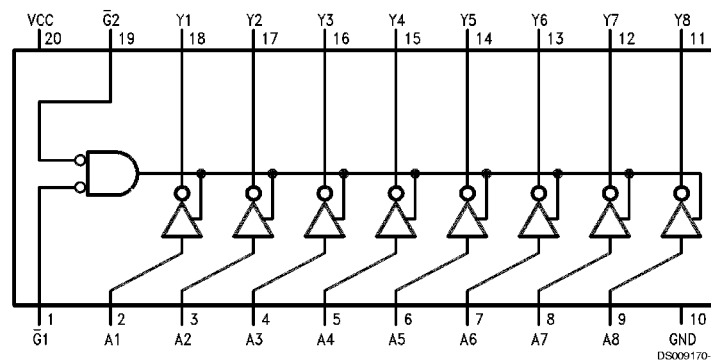
This octal buffer and line driver is designed to have the performance of the 'ALS240 series and, at the same time, offer a pinout with inputs and outputs on opposite sides of the package. This arrangement greatly enhances printed circuit board layout. The 3-STATE control gate is a 2-input NOR such that if either $\overline{G}1$ or $\overline{G}2$ is high, all eight outputs are in the high impedance state.

- Switching performance is guaranteed over full temperature and V_{CC} supply range
- Data flow-thru pinout (All inputs on opposite side from outputs)
- P-N-P inputs reduce DC loading

Features

- Advanced oxide-isolated, ion-implanted Schottky TTL process

Connection Diagram



Order Number DM74ALS540AWM, DM74ALS540ASJ or DM74ALS540AN
See Package Number M20B, M20D or N20A

Function Table

Inputs			Output Y
$\overline{G}1$	$\overline{G}2$	A	
H	X	X	Hi-Z
X	H	X	Hi-Z
L	L	L	H
L	L	H	L

H = High Logic Level, L = Low Logic Level
X = Don't Care (Either High or Low Logic Level)
Hi-Z = High Impedance (Off) State

Absolute Maximum Ratings (Note 1)

Supply Voltage	7V	Range	DM74ALS	0°C to +70°C
Input Voltage	7V	Storage Temperature Range		-65°C to +150°C
Voltage Applied to a Disabled 3-STATE Output	5.5V	Typical θ_{JA}	N Package	58.5°C/W
Operating Free-Air Temperature			M Package	77.5°C/W

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.5	5	5.5	V
V _{IH}	High Level Input Voltage	2			V
V _{IL}	Low Level Input Voltage			0.7	V
I _{OH}	High Level Output Current			-15	mA
I _{OL}	Low Level Output Current			24	mA
T _A	Free Air Operating Temperature	0		70	°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Electrical Characteristics

over recommended free air temperature range

Symbol	Parameter	Test Conditions		Min	Typ	Max	Units
V _{IK}	Input Clamp Voltage	V _{CC} = Min, I _I = -18 mA				-1.5	V
V _{OH}	High Level Output Voltage	V _{CC} = 4.5V to 5.5V	I _{OH} = -0.4 mA	V _{CC} - 2			V
		V _{CC} = Min	I _{OH} = -3 mA	2.4	3.2		
			I _{OH} = Max	2			
V _{OL}	Low Level Output Voltage	V _{CC} = Min	I _{OL} = 12 mA		0.25	0.4	mA
			I _{OL} = 24 mA		0.35	0.5	
I _I	Input Current @ Max Input Voltage	V _{CC} = Max, V _I = 7V				100	μA
I _{IH}	High Level Input Current	V _{CC} = Max, V _I = 2.7V				20	μA
I _{IL}	Low Level Input Current	V _{CC} = Max, V _I = 0.4V				-100	μA
I _{OZH}	High Level 3-STATE Output Current	V _{CC} = Max, V _O = 2.7V				20	μA
I _{OZL}	Low Level 3-STATE Output Current	V _{CC} = Max, V _O = 0.4V				-20	μA
I _O	Output Drive Current	V _{CC} = Max, V _O = 2.25V		-30		-112	mA
I _{CC}	Supply Current	V _{CC} = Max	Outputs High		5	10	mA
			Outputs Low		13	22	
			Outputs Disabled		11	19	

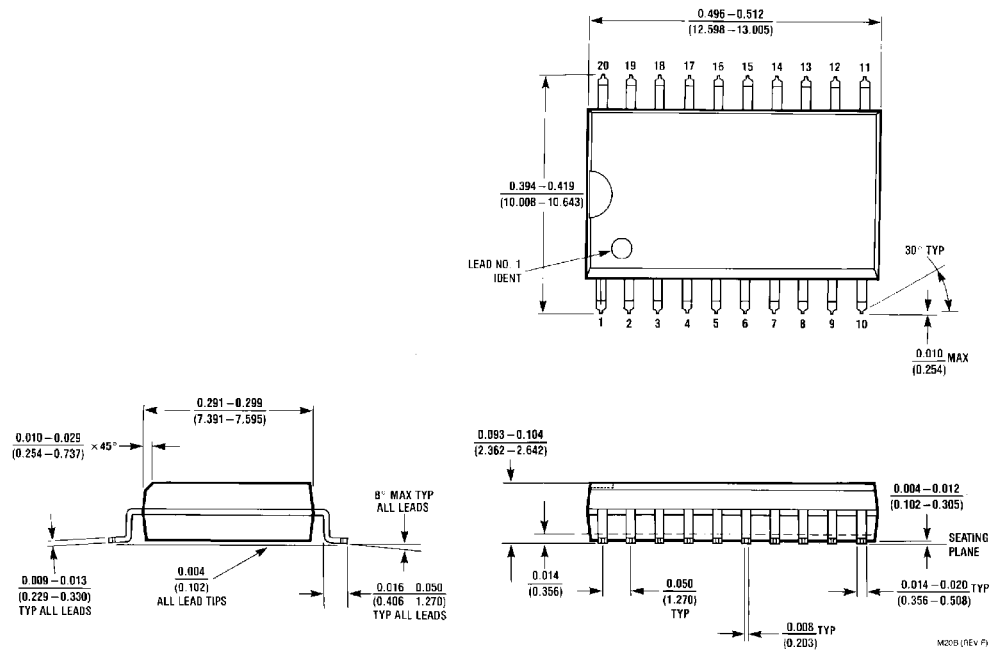
Switching Characteristics

over recommended free air operating temperature range (Note 2)

Symbol	Parameter	Conditions	From (Input) To (Output)	Min	Max	Units
t_{PLH}	Propagation Delay Time Low to High Level Output	$V_{CC} = 4.5V$ to $5.5V$, $R_1 = R_2 = 500\Omega$, $C_L = 50$ pF	A or B to Y	2	12	ns
t_{PHL}	Propagation Delay Time High to Low Level Output		A or B to Y	2	9	ns
t_{PZH}	Output Enable Time to High Level Output		$\overline{G}$ to Y	5	15	ns
t_{PZL}	Output Enable Time to Low Level Output		$\overline{G}$ to Y	8	20	ns
t_{PHZ}	Output Disable Time from High Level Output		$\overline{G}$ to Y	1	10	ns
t_{PLZ}	Output Disable Time from Low Level Output		$\overline{G}$ to Y	2	12	ns

Note 2: See Section 5 for test waveforms and output load.

Physical Dimensions inches (millimeters) unless otherwise noted



S.O. Package (M)
Order Number DM74ALS540AWM
Package Number M20B

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)

