



MSL1

* Pb Free Part

| | | | |
|---------------|-------------------------|--|----------------|
| Customer Name | Standard specifications | TAIYO YUDEN Mobile Technology Co.,Ltd. | |
| System | GPS | Date | March 31, 2010 |
| Part Number | FAR-F6QA-1G5754-H2JC | Version 2.0b | |

Table 1 Electrical Specification

| Passband : 1574.42 ~ 1576.42 MHz | | | | | | |
|----------------------------------|-------------------|------------------------------|------|------|--------|--------|
| Parameter | Frequency | Value | | | Unit | Remark |
| | | Min. | Typ. | Max. | | |
| Insertion loss | 1574.42 ~ 1576.42 | - | 1.11 | 1.35 | dB(*1) | |
| Ripple in passband | 1574.42 ~ 1576.42 | - | 0.05 | 0.5 | dB | |
| Absolute attenuation | DC ~ 1476 | 40 | 43 | - | dB | |
| | 1476 ~ 1526 | 33 | 36 | - | dB | |
| | 1625~1676 | 48 | 55 | - | dB | |
| | 1710 ~ 1785 | 45 | 50 | - | dB | |
| | 1850 ~ 1910 | 45 | 50 | - | dB | |
| | 1910 ~ 1980 | 45 | 50 | - | dB | |
| | 1980 ~ 2500 | 37 | 44 | - | dB | |
| | 2500 ~ 3000 | 30 | 37 | - | dB | |
| | 3000 ~ 6000 | 15 | 22 | - | dB | |
| VSWR Input | 1574.42 ~ 1576.42 | - | 1.2 | 1.7 | - | |
| VSWR Outout | 1574.42 ~ 1576.42 | - | 1.1 | 1.7 | - | |
| Input Impedance | | 50 | | | Ohm | |
| Output Impedance | | 50 | | | ohm | |
| Operating Temperature | | -30 to +85 | | | °C | |
| Device size (L x W x H) | | 1.1typ. x 0.9typ. x 0.5 max. | | | mm | |

(*1) Specification of insertion loss includes loss that comes from the test board.



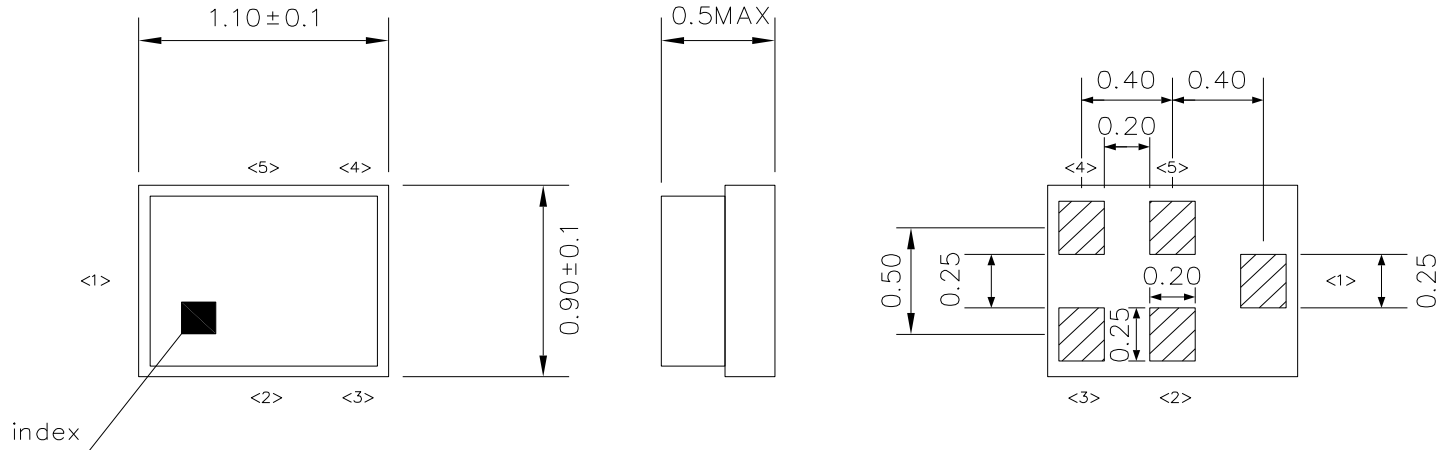
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Dimension

Device size: 1.1typ. x 0.9typ. x 0.5max.

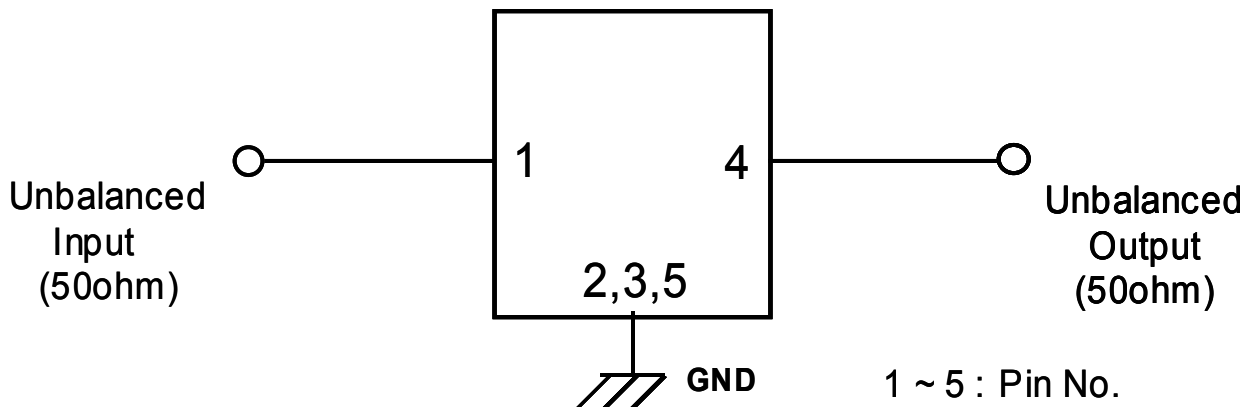


Unit: mm

Pin Configuration

| Pin No. | Symbol | Function |
|---------|--------|----------------|
| 1 | IN | Unbalanced pin |
| 2 | GND | Ground |
| 3 | GND | Ground |
| 4 | OUT | Unbalanced pin |
| 5 | GND | Ground |

Evaluation Circuit





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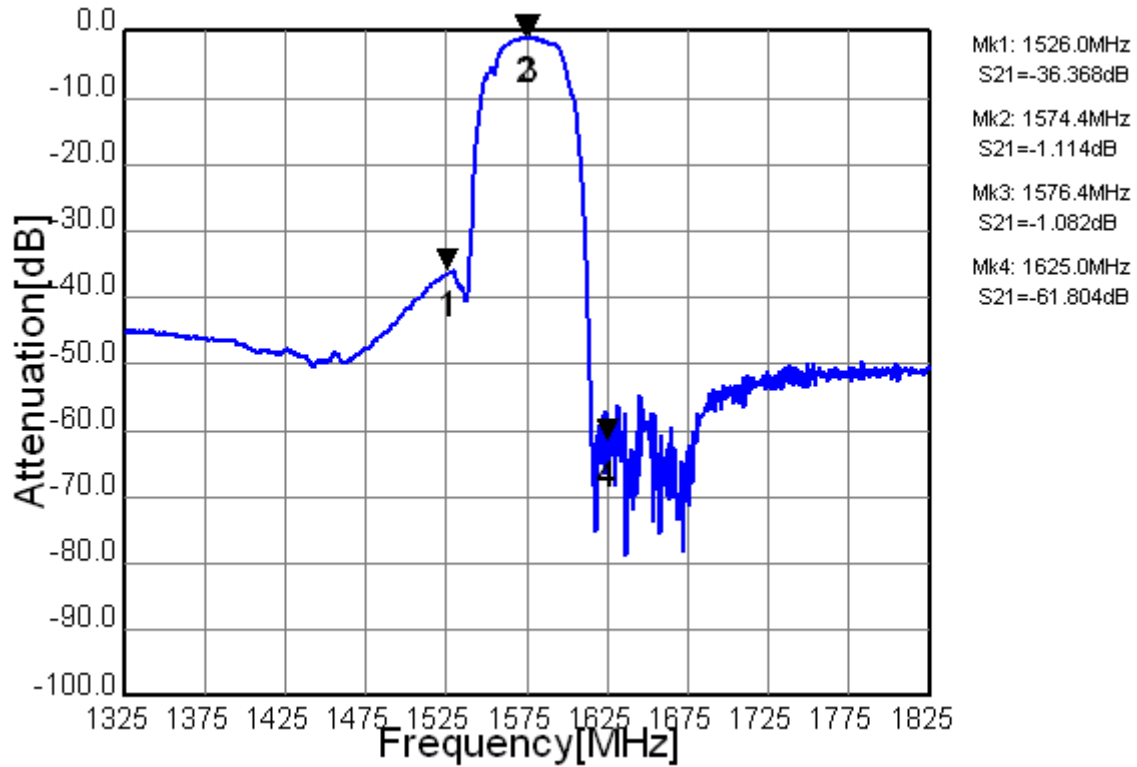


Fig.1 Pass-band Characteristics

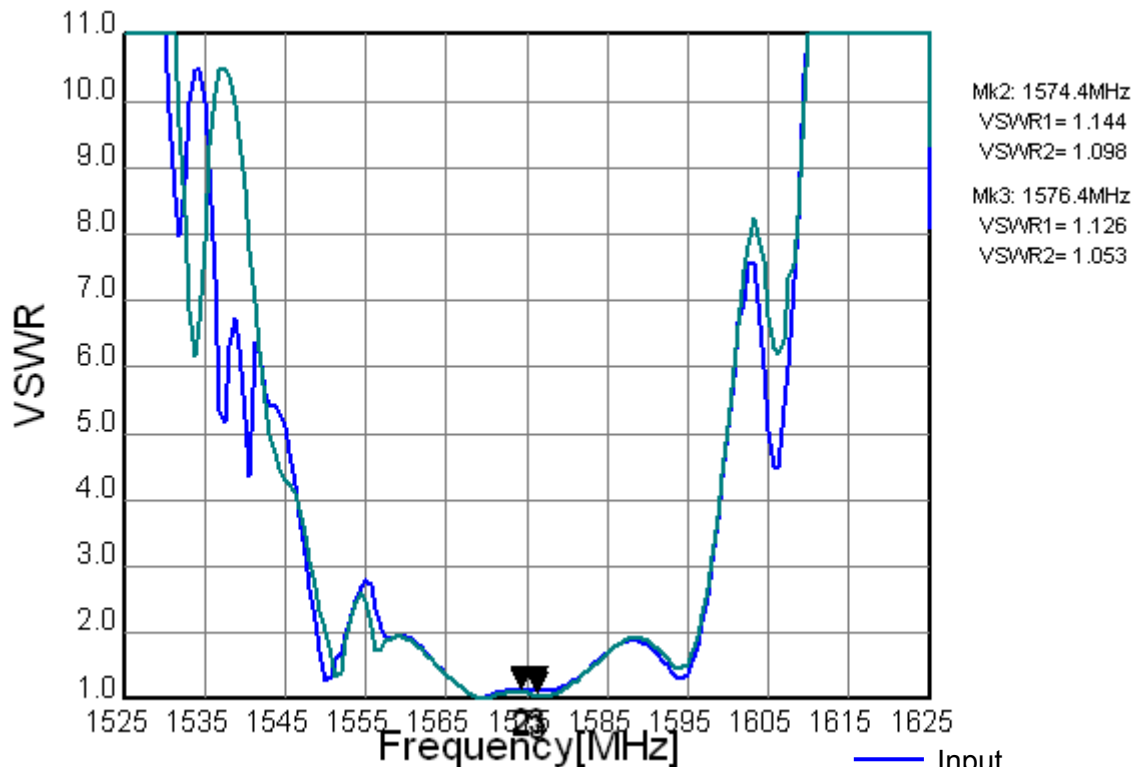


Fig.2 VSWR



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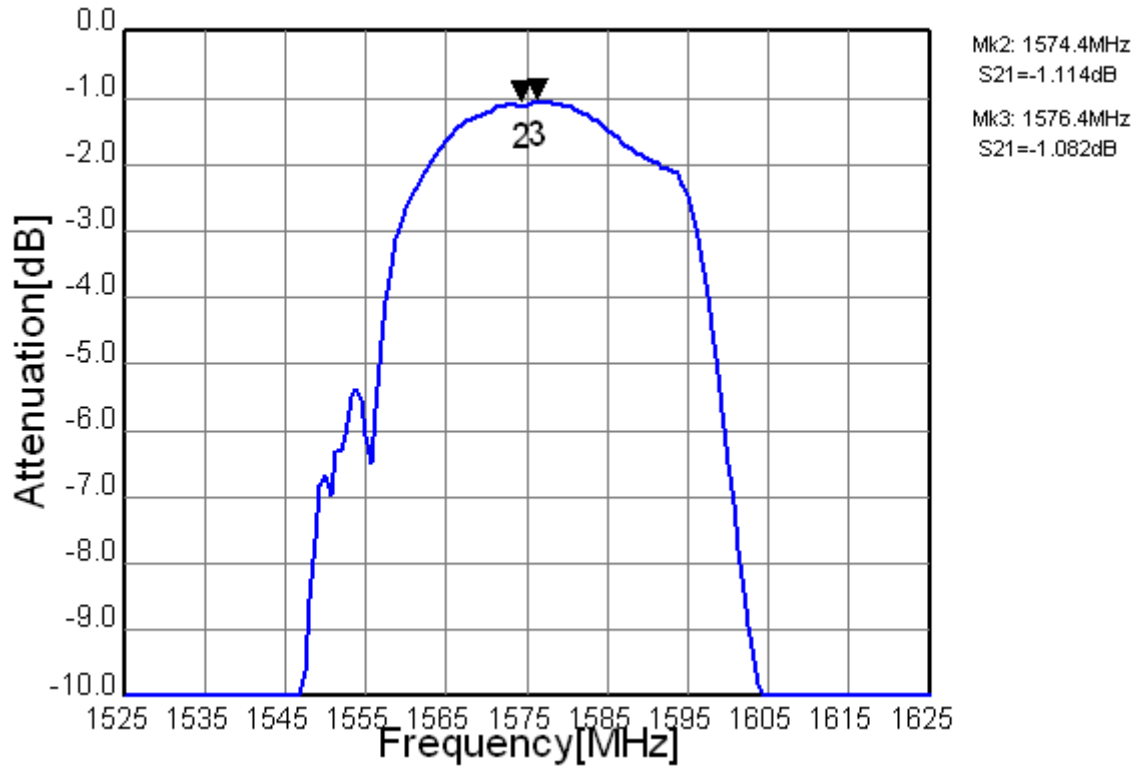


Fig.3 In-band Characteristics

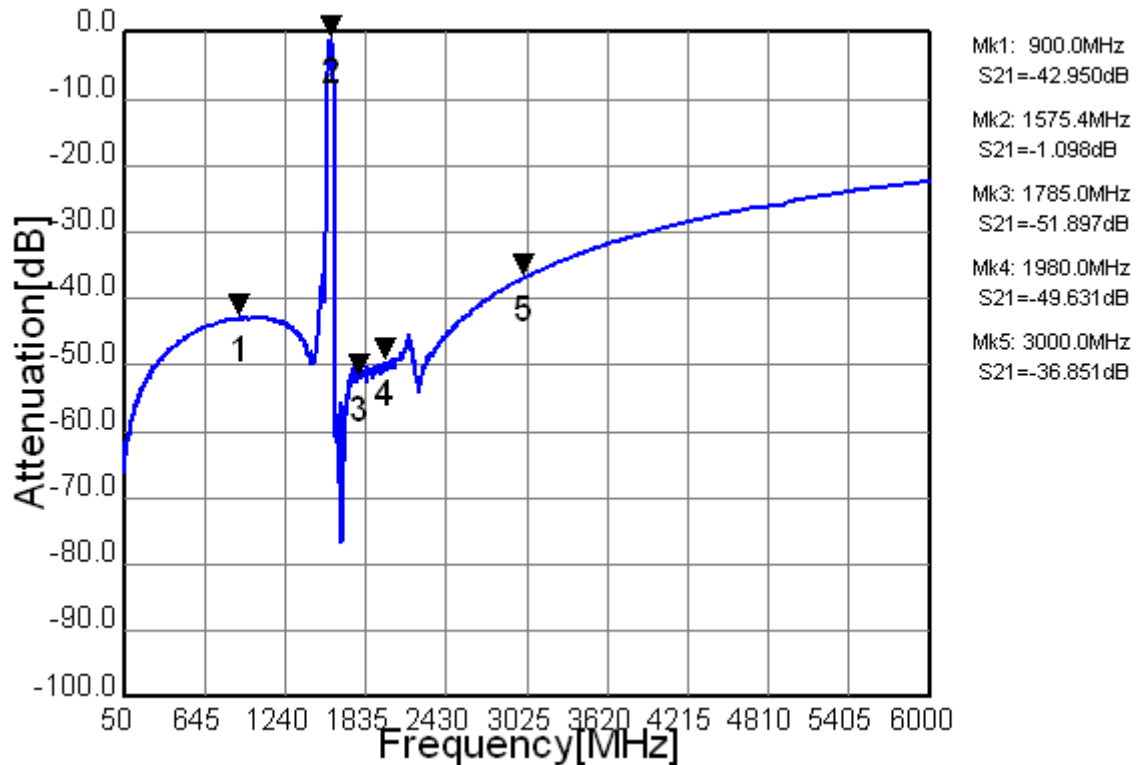


Fig.4 Wide-band Characteristics



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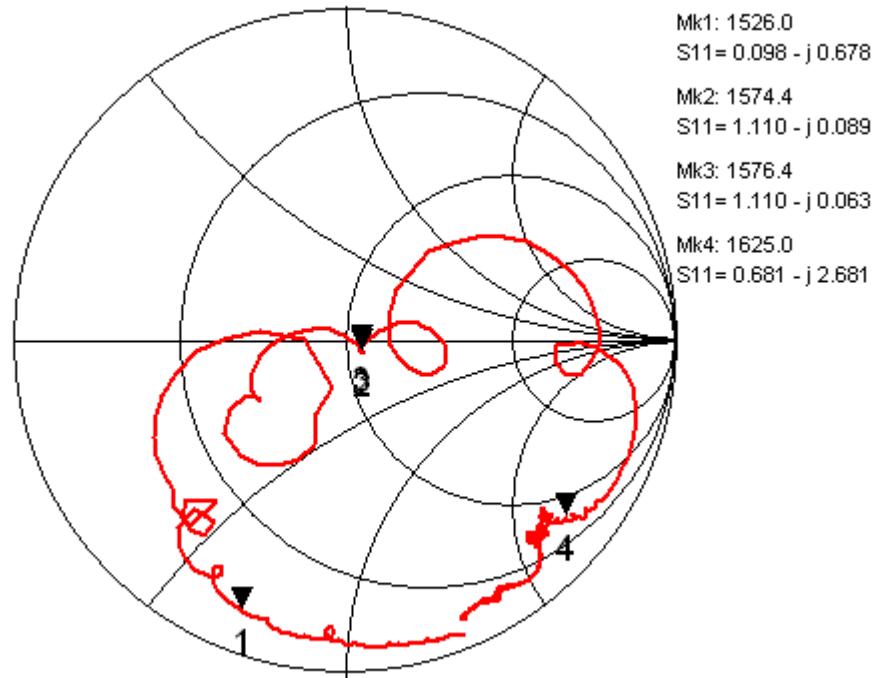


Fig.5 Input impedance

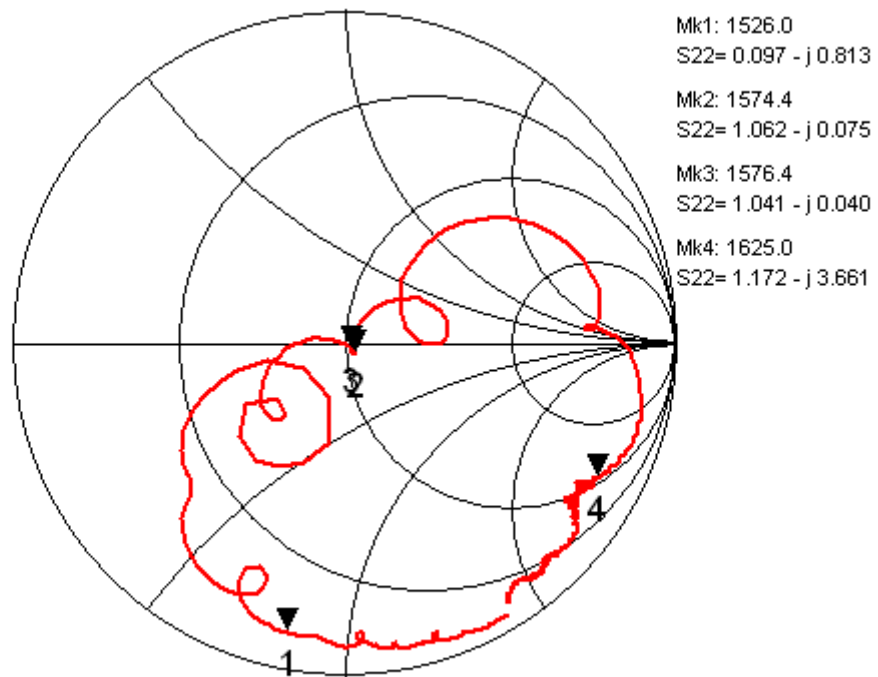


Fig.6 Output impedance