

L-Band Medium & High Power GaAs FET

FEATURES

• High Output Power: P_{1dB} = 40.0dBm (Typ.)

• High Gain: $G_{1dB} = 10.0dB$ (Typ.)

• High PAE: $\eta_{add} = 40\%$ (Typ.)

Proven Reliability

• Hermetically Sealed Package

DESCRIPTION

The FLL120MK is a Power GaAs FET that is specifically designed to provide high power at L-Band frequencies with gain, linearity and efficiency superior to that of silicon devices. The performance in multitone environments for Class AB operation make them ideally suited for base station applications.

Fujitsu's stringent Quality Assurance Program assures the highest reliability and consistent performance.



ABSOLUTE MAXIMUM RATING (Ambient Temperature Ta=25°C)

Item	Symbol	Condition	Rating	Unit
Drain-Source Voltage	VDS		15	V
Gate-Source Voltage	VGS		-5	V
Total Power Dissipation	PT	T _C = 25°C	37.5	W
Storage Temperature	T _{stg}		-65 to +175	°C
Channel Temperature	T _{ch}		175	°C

Fujitsu recommends the following conditions for the reliable operation of GaAs FETs:

1. The drain-source operating voltage (V_{DS}) should not exceed 10 volts.

ELECTRICAL CHARACTERISTICS (Ambient Temperature Ta=25°C)

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Item	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Saturated Drain Current	IDSS	V _{DS} = 5V, V _{GS} = 0V	-	4000	6000	mA	
Transconductance	9m	V _{DS} = 5V, I _{DS} = 2400mA	-	2000	-	mS	
Pinch-off Voltage	Vp	V _{DS} = 5V, I _{DS} =240mA	-1.0	-2.0	-3.5	V	
Gate Source Breakdown Voltage	V _{GSO}	IGS = -240μA	-5	-	-	V	
Output Power at 1dB G.C.P.	P _{1dB}	V 40V	39.5	40.0	-	dBm	
Power Gain at 1dB G.C.P.	G _{1dB}	VDS = 10V IDS = 0.55 IDSS (Typ.), f = 2.3GHz	9.0	10.0	-	dB	
Power-added Efficiency	ηadd		-	40	-	%	
Thermal Resistance	R _{th}	Channel to Case	-	3.3	4.0	°C/W	

CASE STYLE: MK G.C.P.: Gain Compression Point



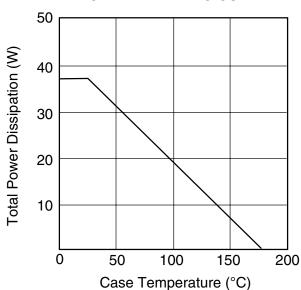
^{2.} The forward and reverse gate currents should not exceed 26.8 and -5.8 mA respectively with gate resistance of 50Ω .

^{3.} The operating channel temperature (T_{ch}) should not exceed 145°C.

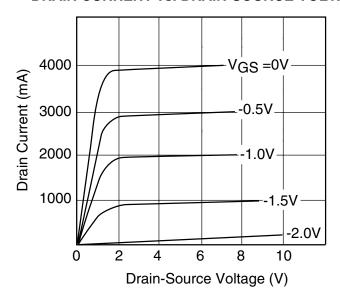
FLL120MK

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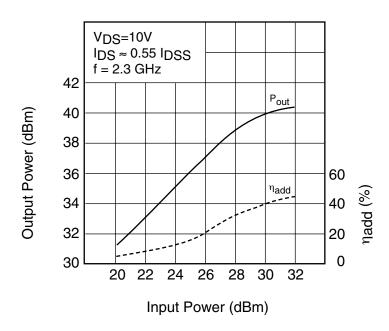
POWER DERATING CURVE



DRAIN CURRENT vs. DRAIN-SOURCE VOLTAGE



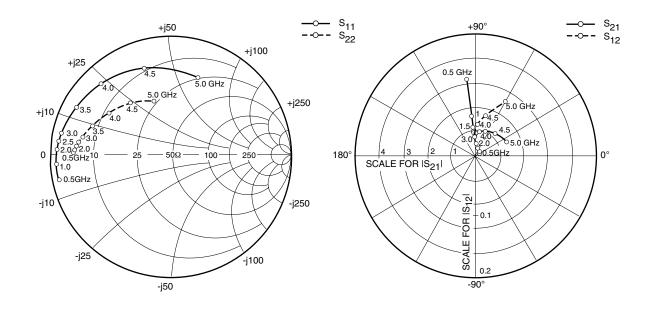
OUTPUT POWER vs. INPUT POWER





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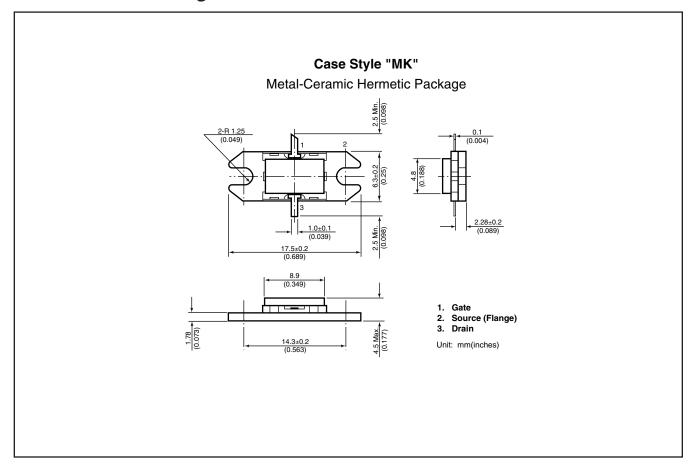
S-PARAMETERS

 $V_{DS} = 10V, I_{DS} = 2200 \text{mA}$

FREQUENCY	FREQUENCY S11		S21		S1	S12		S22	
(MHZ)	MAG	ANG	MAG	ANG	MAG	ANG	MAG	ANG	
500	.959	-168.4	3.136	95.8	.008	31.0	.824	179.4	
1000	.953	-176.0	1.617	94.1	.010	45.7	.813	178.8	
1500	.953	-179.6	1.170	93.8	.011	64.3	.810	177.7	
2000	.951	177.0	.978	92.3	.014	82.4	.792	176.5	
2500	.939	172.6	.927	91.4	.021	89.1	.778	174.0	
3000	.914	165.1	.936	88.0	.024	93.2	.739	168.3	
3500	.885	152.7	.990	80.6	.033	94.6	.695	158.9	
4000	.836	134.0	1.106	67.1	.051	88.1	.633	145.1	
4500	.766	107.3	1.239	48.2	.067	77.3	.559	128.0	
5000	.690	71.6	1.415	23.9	.103	60.5	.477	107.3	



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CAUTION

Fujitsu Compound Semiconductor Products contain **gallium arsenide (GaAs)** which can be hazardous to the human body and the environment. For safety, observe the following procedures:

- Do not put these products into the mouth.
- Do not alter the form of this product into a gas, powder, or liquid through burning, crushing, or chemical processing as these by-products are dangerous to the human body if inhaled, ingested, or swallowed.
- Observe government laws and company regulations when discarding this product. This product must be discarded in accordance with methods specified by applicable hazardous waste procedures.

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