



PESD36VS1UJ

Unidirectional ESD protection diode

17 April 2023

Product data sheet

1. General description

Unidirectional ElectroStatic Discharge (ESD) protection diode in a flat lead very small SOD323F Surface-Mounted Device (SMD) plastic package designed to protect one signal line from the damage caused by ESD and other transients.

2. Features and benefits

- ESD protection of one line
- ESD protection up to 30 kV
- IEC 61000-4-5; (surge); $I_{PPM} = 2.5\text{ A}$
- Rated peak pulse power: $P_{PPM} = 150\text{ W}$
- Ultra low leakage current: $I_{RM} < 1\text{ nA}$

3. Applications

- computers and peripherals
- audio and video equipment
- cellular handsets and accessories
- portable electronics
- communication systems

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{RWM}	reverse standoff voltage	$T_{amb} = 25\text{ °C}$	-	-	36	V
C_d	diode capacitance	$f = 1\text{ MHz}; V_R = 0\text{ V}; T_{amb} = 25\text{ °C}$	-	18	30	pF

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]	 SC-90 (SOD323F)	 sym035
2	A	anode		

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PESD36VS1UJ	SC-90	plastic, surface-mounted package; 2 leads; 1.7 mm x 1.25 mm x 0.7 mm body	SOD323F

7. Marking

Table 4. Marking codes

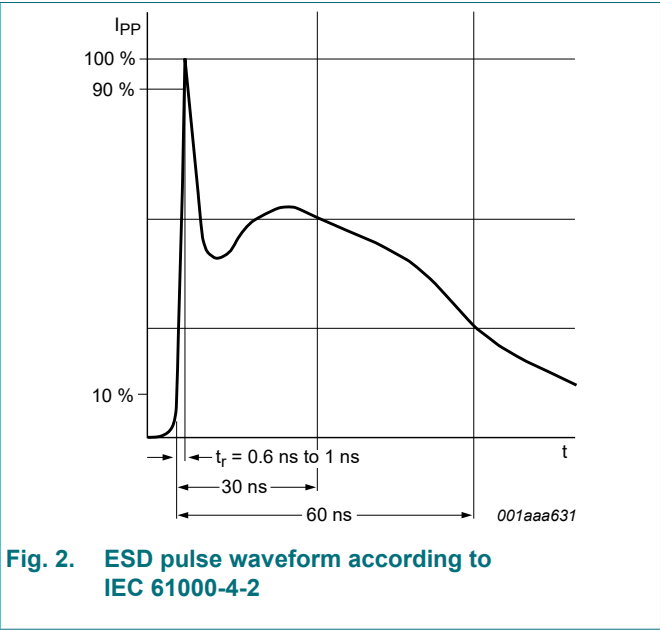
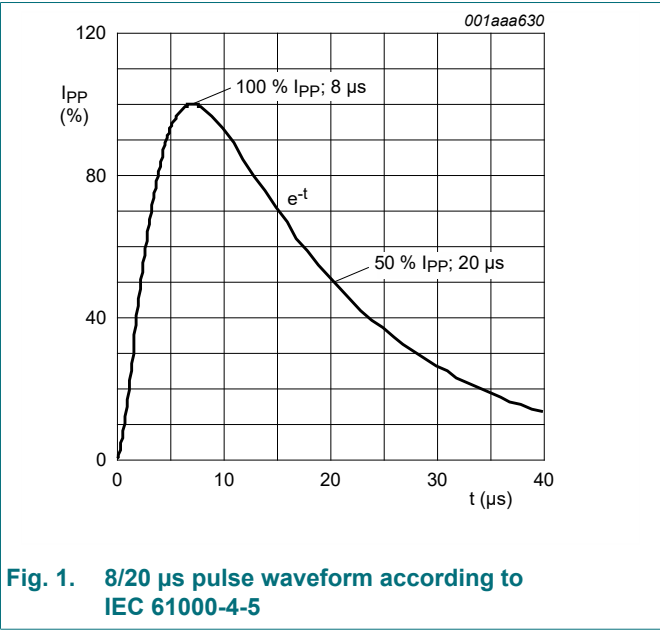
Type number	Marking code
PESD36VS1UJ	EA

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P _{PPM}	rated peak pulse power	t _p = 8/20 μs	[1] [2]	-	150	W
I _{PPM}	rated peak pulse current		[1] [2]	-	2.5	A
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximum ratings						
V _{ESD}	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[2] [3]	-	30	kV
		IEC 61000-4-2; air discharge	[2]	-	30	kV

- [1] Device stressed with ten non-repetitive ESD pulses (8/20 μs exponential decay waveform according to IEC 61000-4-5).
[2] Measured from pin 1 to pin 2.
[3] Device stressed with ten non-repetitive ESD pulses.

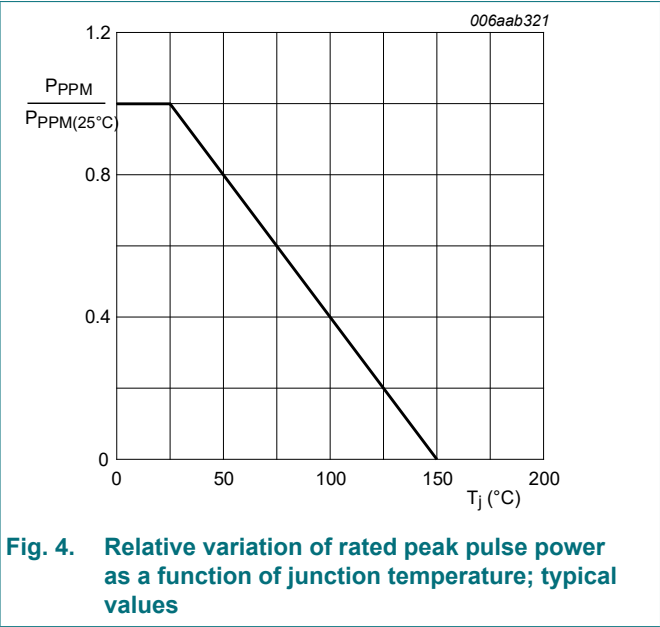
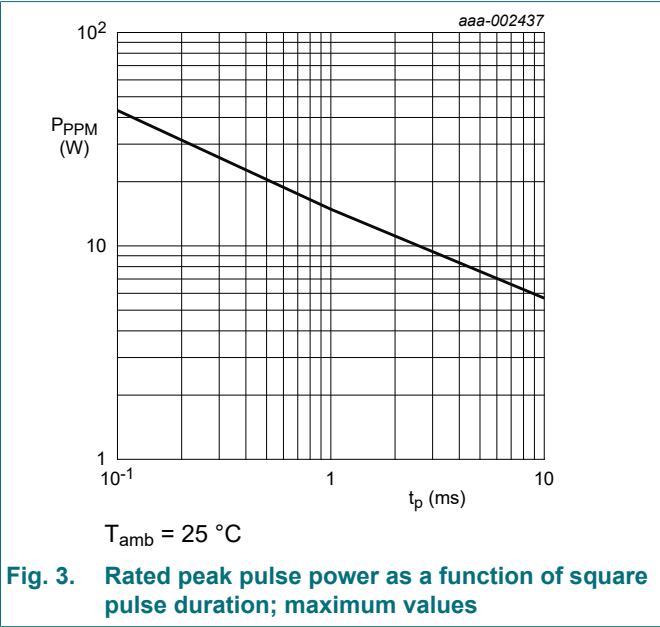


9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	36	V
V _{BR}	breakdown voltage	I _R = 2 mA; T _{amb} = 25 °C		38.2	39	39.8	V
I _{RM}	reverse leakage current	V _R = 36 V; T _{amb} = 25 °C		-	1	10	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	18	30	pF
V _{CL}	clamping voltage	I _{PP} = 1 A; T _{amb} = 25 °C	[1] [2]	-	-	58	V
		I _{PP} = 2.5 A; T _{amb} = 25 °C	[1] [2]	-	-	80	V
R _{dyn}	dynamic resistance	T _{amb} = 25 °C	[3]	-	9.5	-	Ω

- [1] Device stressed with 8/20 μs exponential decay waveform according to IEC 61000-4-5.
[2] Measured from pin 1 to pin 2.
[3] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008.



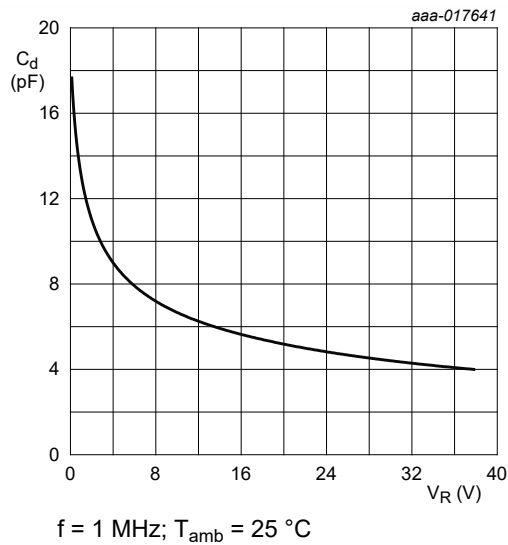


Fig. 5. Diode capacitance as a function of reverse voltage; typical values

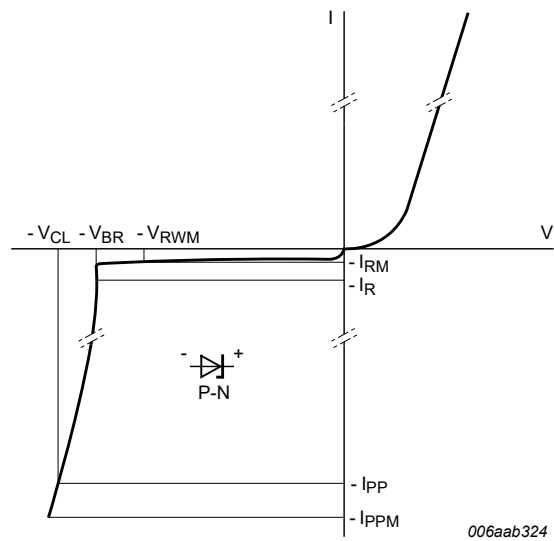


Fig. 6. V-I characteristics for a unidirectional ESD protection diode

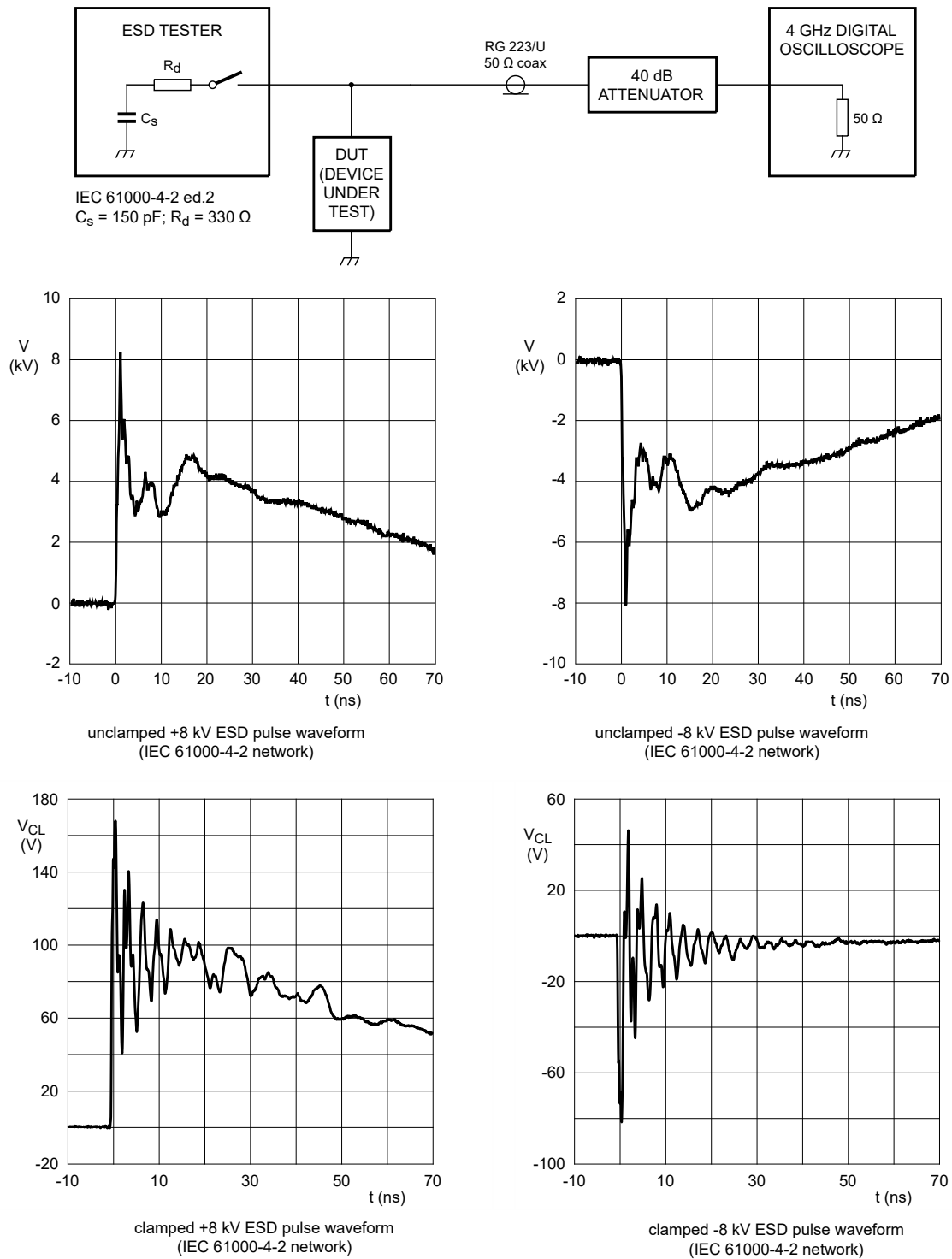


Fig. 7. ESD clamping test setup and waveforms

10. Application information

The device is designed for protection of one unidirectional data or signal line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are either positive or negative with respect to ground.

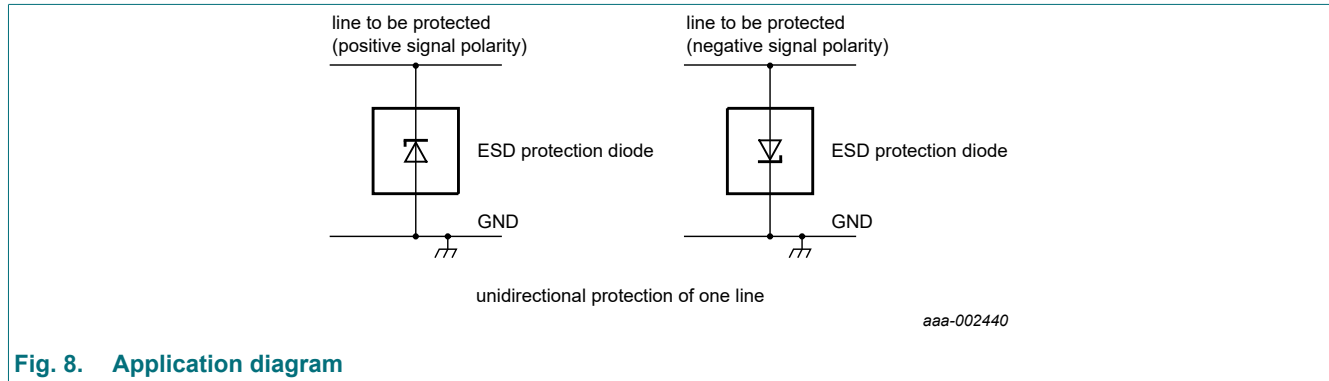


Fig. 8. Application diagram

Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Package outline

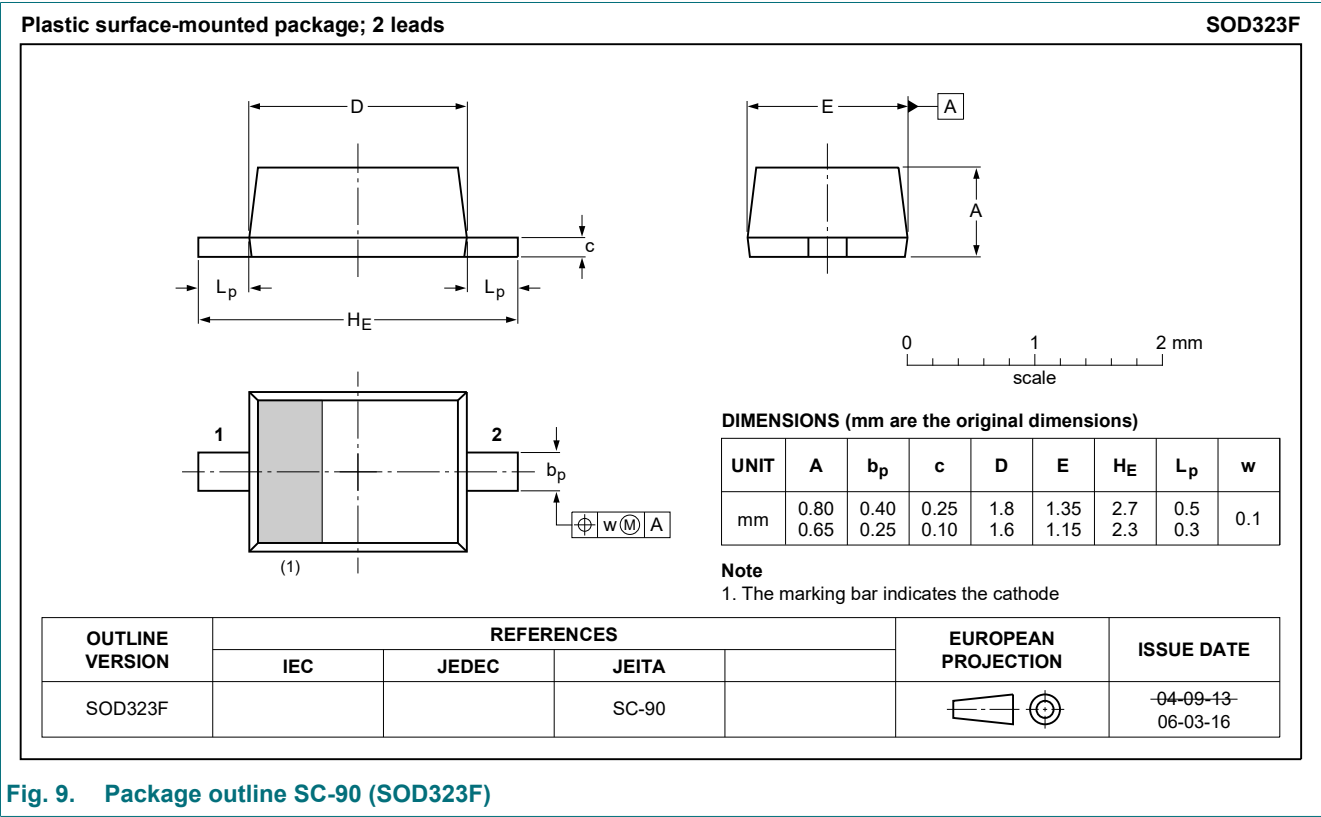


Fig. 9. Package outline SC-90 (SOD323F)

12. Soldering

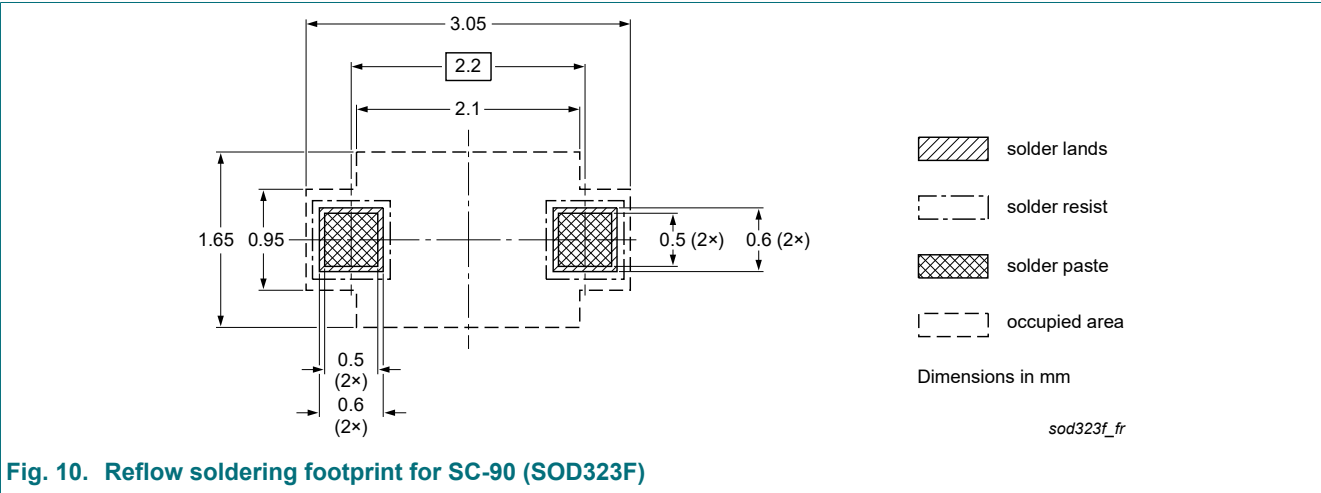


Fig. 10. Reflow soldering footprint for SC-90 (SOD323F)

13. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD36VS1UJ v.3	20230417	Product data sheet	-	PESD36VS1UJ v.2
Modifications:	Product changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s)			
PESD36VS1UJ v.2	20180830	Product data sheet	-	PESD36VS1UJ v.1
PESD36VS1UJ v.1	20150916	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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