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# ADC1213D080W1

## ADC1213D080W1 Demo board With FPGA

IDT's ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration. The FPGA eases the evaluation and analysis of the ADC dynamic and enables use of the full JESD204A feature set.



### Features

- Single power supply (on-board regulators)
- On-board Virtex 5 FPGA for data-acquisition
- SMA connector for clock and analog input signals
- On-board Data clock generator for ADC and FPGA

## Product Options

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ADC1213D080W1-DB	Active New	BD0	BOARD	0	C	N	<a href="#">Check Inventory</a>

## Documents

There are no public documents available for this product. [Click here to request documentation.](#)

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PRODUCT ID	TITLE
<a href="#">ADC1213D080HN</a>	Dual 12-bit ADC; 80 Msps

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