

HN2E05J

Super High Speed Switching Application

Interface Circuit

Driver Circuit Applications

Q1

Since bias resistor is built in the transistor, the miniaturization of the apparatus by curtailment of the number of parts and laborsaving of an assembly are possible.

Q2

Low Forward Voltage Drop : $V_{F(3)}=0.98V(\text{typ.})$

Fast Reverse Recovery Time : $t_{rr}=1.6ns(\text{typ.})$

Low Total Capacitance : $C_T=0.5pF(\text{typ.})$

Q1(Transistor) : RN2104F equivalent

Q2(Transistor) : 1SS352 equivalent

Q1(Transistor) Maximum Ratings ($T_a = 25^\circ\text{C}$)

| Characteristic | Symbol | Rating | Unit |
|---------------------------|-----------|--------|------|
| Collector-base voltage | V_{CBO} | -50 | V |
| Collector-emitter voltage | V_{CEO} | -50 | V |
| Emitter-base voltage | V_{EBO} | -10 | V |
| Collector current | I_C | -100 | mA |

Q2(Diode) Maximum Ratings ($T_a = 25^\circ\text{C}$)

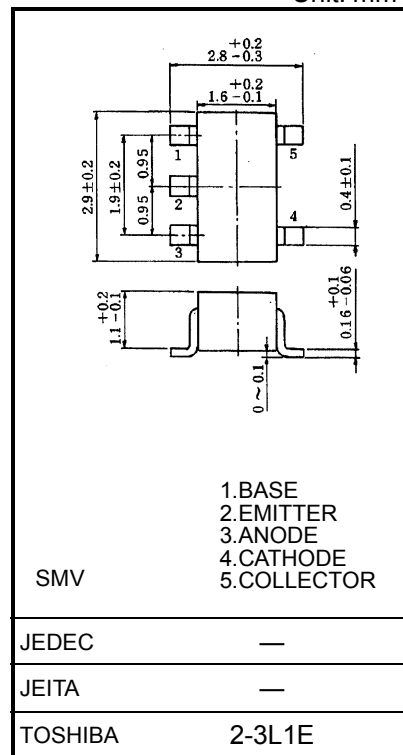
| Characteristic | Symbol | Rating | Unit |
|--------------------------------|-----------|--------|------|
| Maximum (peak) reverse voltage | V_{RM} | 85 | V |
| Reverse voltage | V_R | 80 | V |
| Maximum (peak) forward current | I_{FM} | 200 | mA |
| Average forward current | I_O | 100 | mA |
| Surge current (10ms) | I_{FSM} | 1 | A |

Maximum Ratings ($T_a = 25^\circ\text{C}$) (Q1, Q2 Common)

| Characteristic | Symbol | Rating | Unit |
|-----------------------------|-----------|---------|------------------|
| Collector power dissipation | P_C^* | 300 | mW |
| Junction temperature | T_j | 150 | $^\circ\text{C}$ |
| Storage temperature range | T_{stg} | -55~150 | $^\circ\text{C}$ |

* Total rating. 200mW per 1 element must not be exceeded.

Unit: mm



Weight:0.014g (typ.)

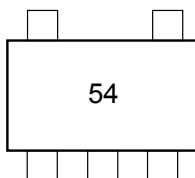
Q1(Transistor) Electrical Characteristics (Ta = 25°C)

| Characteristics | Symbol | Test Condition | Min | Typ. | Max | Unit |
|--------------------------------------|---------------|--|--------|------|-------|------|
| Collector cut-off current | I_{CBO} | $V_{CB} = -50\text{ V}, I_E = 0$ | — | — | -100 | nA |
| | I_{CEO} | $V_{CE} = -50\text{ V}, I_B = 0$ | — | — | -500 | |
| Emitter cut-off current | I_{EBO} | $V_{EB} = -10\text{ V}, I_C = 0$ | -0.082 | — | -0.15 | mA |
| DC current gain | h_{FE} | $V_{CE} = -5\text{ V}, I_C = -10\text{ mA}$ | 80 | — | — | |
| Collector-emitter saturation voltage | $V_{CE(sat)}$ | $I_C = -5\text{ mA}, I_B = -0.25\text{ mA}$ | — | -0.1 | -0.3 | V |
| Input voltage (ON) | $V_{I(ON)}$ | $V_{CE} = -0.2\text{ V}, I_C = -5\text{ mA}$ | -1.5 | — | -5.0 | V |
| Input voltage (OFF) | $V_{I(OFF)}$ | $V_{CE} = -5\text{ V}, I_C = -0.1\text{ mA}$ | -1.0 | — | -1.5 | V |
| Transition frequency | f_T | $V_{CE} = -10\text{ V}, I_C = -5\text{ mA}$ | — | 200 | — | MHz |
| Collector output capacitance | C_{ob} | $V_{CB} = -10\text{ V}, I_E = 0, f = 1\text{ MHz}$ | — | 3 | 6 | pF |
| Input resistor | R1 | — | 32.9 | 47 | 61.1 | kΩ |
| Resistor ratio | R1/R2 | — | 0.9 | 1.0 | 1.1 | |

Q2(Diode) Electrical Characteristics (Ta = 25°C)

| Characteristic | Symbol | Test Circuit | Test Condition | Min | Typ. | Max | Unit |
|-----------------------|----------|--------------|------------------------------|-----|------|------|------|
| Forward voltage | $V_F(1)$ | — | $I_F = 1\text{ mA}$ | — | 0.62 | — | V |
| | $V_F(2)$ | — | $I_F = 10\text{ mA}$ | — | 0.75 | — | |
| | $V_F(3)$ | — | $I_F = 100\text{ mA}$ | — | 0.98 | 1.20 | |
| Reverse current | $I_R(1)$ | — | $V_R = 30\text{ V}$ | — | — | 0.1 | μA |
| | $I_R(2)$ | — | $V_R = 80\text{ V}$ | — | — | 0.5 | |
| Total capacitance | C_T | — | $V_R = 0, f = 1\text{ MHz}$ | — | 0.5 | — | pF |
| Reverse recovery time | t_{rr} | — | $I_F = 10\text{ mA (fig.1)}$ | — | 1.6 | — | ns |

Marking



Equivalent Circuit (Top View)

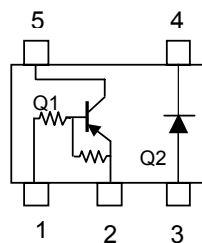
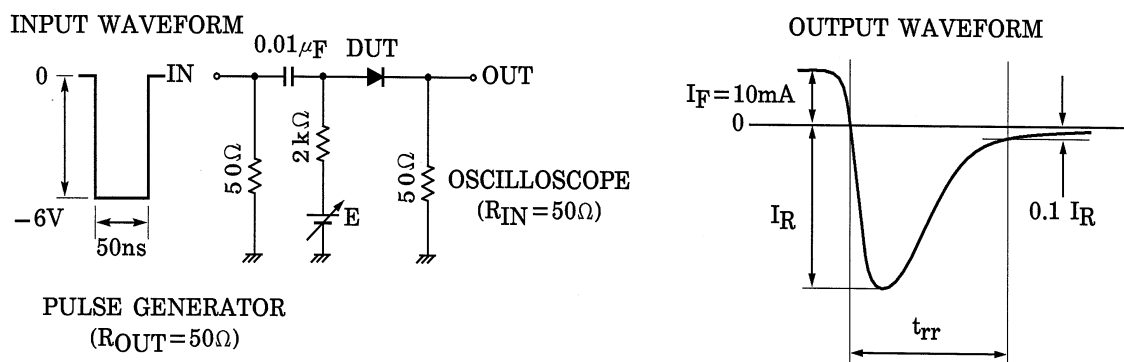
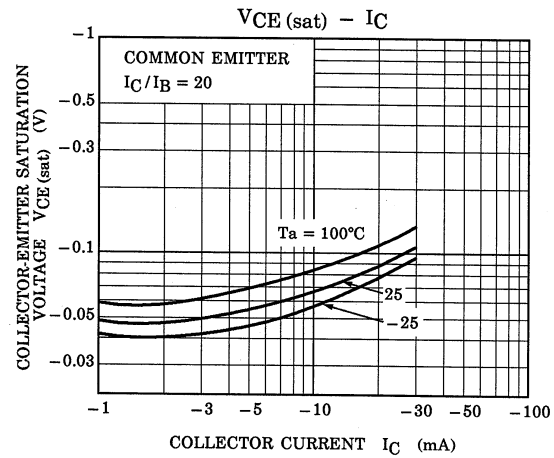
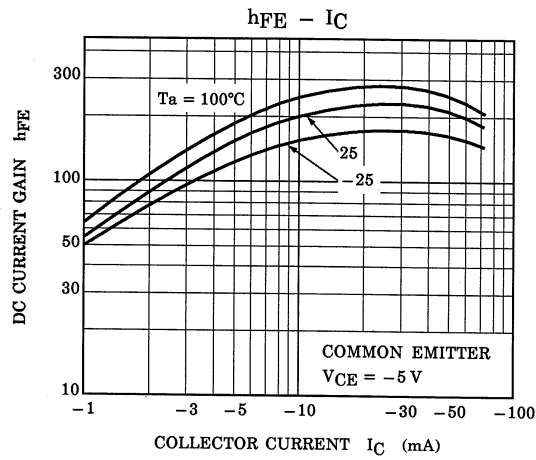
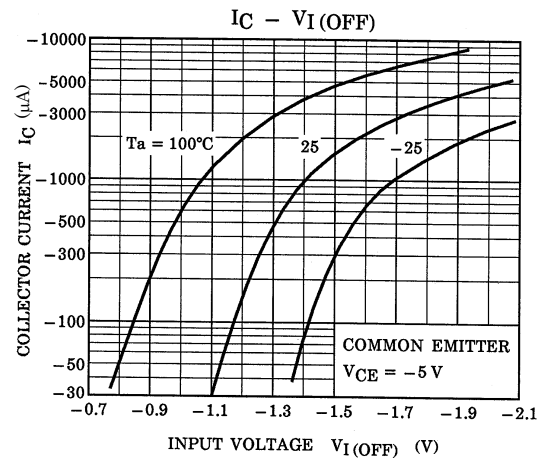
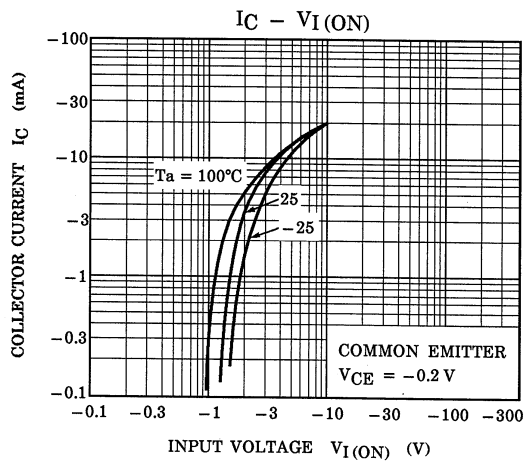


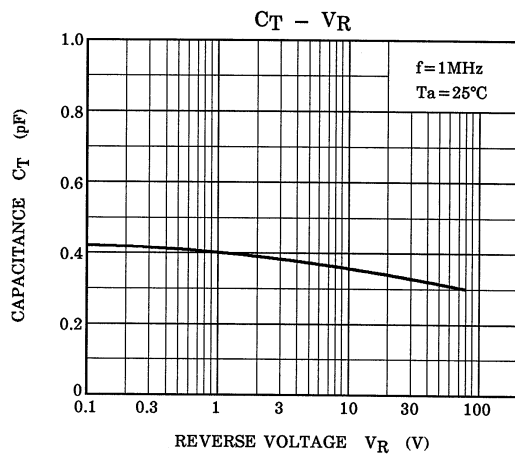
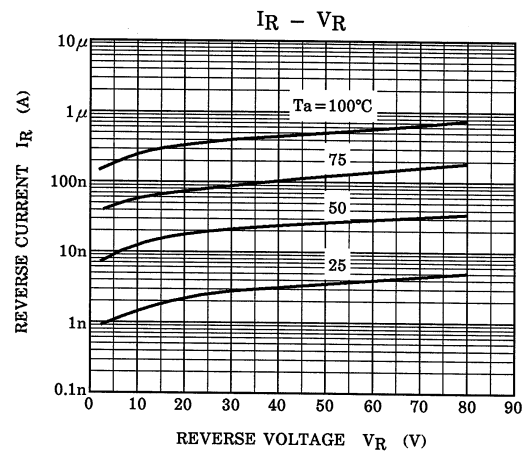
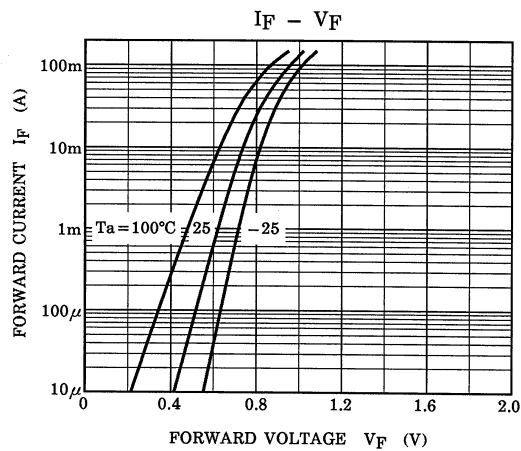
Fig.1 : Reverse Recovery Time (t_{rr}) Test Circuit



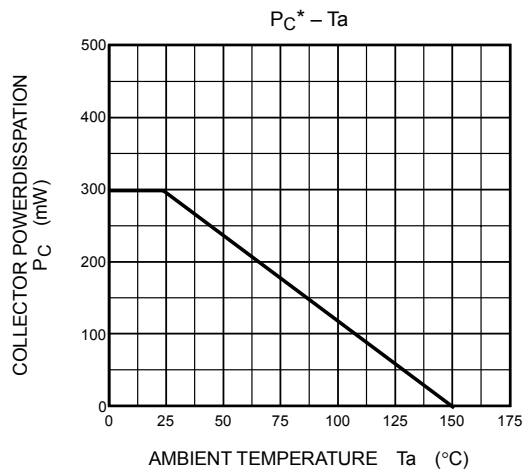
Q1



Q2



Q1,Q2 Common



*Total Rating.

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