TOSHIBA Intelligent Power Device Silicon Monolithic Power MOS IC

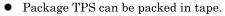
# **TPD1028AS**

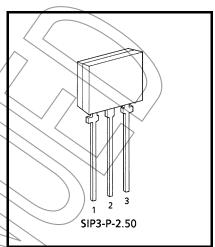
Low-side Switch for Motor, Solenoid and Lamp Drive

TPD1028AS is a monolithic power IC for low–side switch. The IC has a vertical MOSFET output which can be directly driven from a CMOS or TTL logic circuit (e.g., an MPU). The IC offers intelligent self–protection functions.

#### **Features**

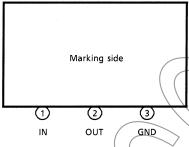
- A monolithic power IC with a new structure combining a control block and a vertical power MOSFET (π–MOS) on a single chip.
- Can directly drive a power load from a CMOS logic etc.
- Built-in protection circuits against overvoltage, load short circuit, and thermal shutdown.
- Low on resistance. RDS (ON) =  $0.25\Omega$  (max) (@VIN = 5V,  $T_{j_i} = 25$ °C)



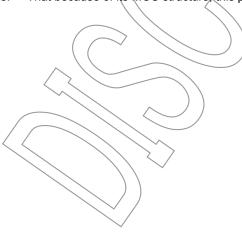


Weight: 0.54g (typ.)

### **Pin Assignment**

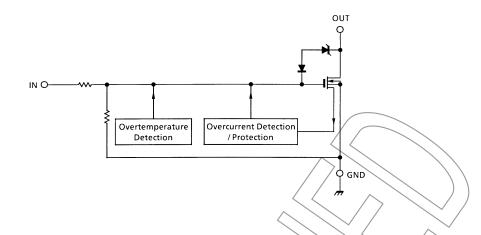


Note: That because of its MOS structure, this product is sensitive to static electricity.



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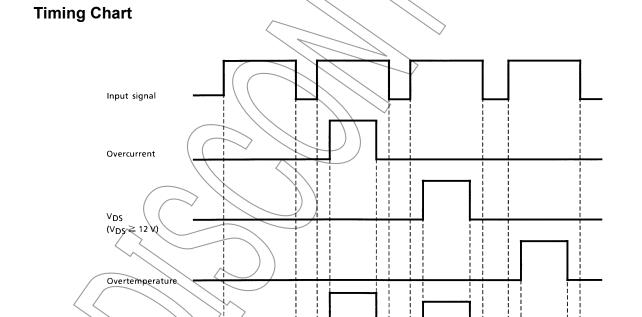
## **Block Diagram**



## **Pin Description**

Output current

Pin No.	Symbol	Pin Description
1	IN	Input pin. This pin is connected to a pull-down resistor internally, so that even when input wiring is open-circuited, output can never be turned on inadvertently.
2	OUT	Output pin. If an inrush current flows (e.g., from a lamp), the current is clamped at 10A (typ.) by an overcurrent protective circuit. Also, a 150 $\mu$ s (typ.) mask circuit is included internally, so that if $V_{DS} \ge 12V$ (typ.) after this mask time, the current is clamped at 3A (typ.).
3	GND	Ground pin.



Current limiting

Current limiting

## **Truth Table**

IN	VOUT	State		
L	Н	Normal		
Н	L	Nomia		
L	Н	Overcurrent		
Н	L	(during inrush)		
L	Н	Overcurrent		
Н	L	(shorted load)		
L	Н	Overtemperature		
Н	Н	Overtemperature		

## **Maximum Rating (Ta = 25°C)**

		^
Characteristics	Symbol	Rating Unit
Drain-source Voltage	V <sub>DS (DC)</sub>	40 V
Output Current	I <sub>D</sub>	7.5 A
Input Voltage	V <sub>IN</sub>	70.5~6
Power Dissipation	PD	1.2 W
Energy Tolerance	ES/B	200 mJ
Operating Temperature	T <sub>opr</sub> <	-40~85 °C
Junction Temperature	T <sub>j</sub>	150 °C

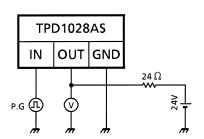
# Electrical Characteristics $(T_j = 25^{\circ}C)$

Test											
Characteristics	Symbol	Circuit	Test Condition	Min	Тур.	Max	Unit				
Drain-source Breakdown Voltage	V (BR) DSS	/	$V_{1N} = 0 \text{ V, I}_D = 10\text{mA}$	40	_	_	V				
Operating Supply Voltage	V <sub>DD</sub>	7/	_	_	_	38	V				
High Level Input Voltage	) VH	)	$V_{DS} = 24 \text{ V}, I_D = 1A$	4.5	5	5.5	٧				
Low Level Input Voltage	VĮŁ		$V_{DS} = 24 \text{ V}, I_D = 10 \mu \text{A}$	_	_	0.8	٧				
Current at Output Off	I <sub>DSS</sub> (1)	_	V <sub>IN</sub> = 0V, V <sub>DS</sub> = 40V	_	_	100	ПΔ				
Current at Output Off	IDSS (2)		V <sub>IN</sub> = 0V, V <sub>DS</sub> = 24V	_	_	10 µA					
Input Current	<u></u>	_	V <sub>IN</sub> = 5V, at normal operation	_	_	300	μΑ				
On Resistance	RDS (ON)	_	V <sub>IN</sub> = 5V, I <sub>D</sub> = 1A	_	_	0.25	Ω				
Thermal Shutdown Temperature	) T <sub>S</sub>	_	V <sub>IN</sub> = 5V	_	160	_	°C				
Oversurrent Protection	I <sub>S (1)</sub>	_	V <sub>DS</sub> = 24V, V <sub>IN</sub> = 5V, during inrush	_	10	-	Α				
Overconent Protection	I <sub>S (2)</sub>	_	V <sub>DS</sub> = 24V, V <sub>IN</sub> = 5V, when shorted load	_	3	-	A				
Shorted Load Detection Voltage	V <sub>DS</sub>	_	When shorted load		12	ı	>				
Switching Time	t <sub>ON</sub>	1	$V_{DS}$ = 24V, $V_{IN}$ = 5V, $R_L$ = 24 $\Omega$	_	70	_	μs				
	toff	'		_	120	_					
Diode Forward Voltage Between Drain and Source	V <sub>DSF</sub>	_	I <sub>F</sub> = 1.5A	_	0.9	1.8	٧				

### **Test Circuit 1**

## Switching time measuring circuit

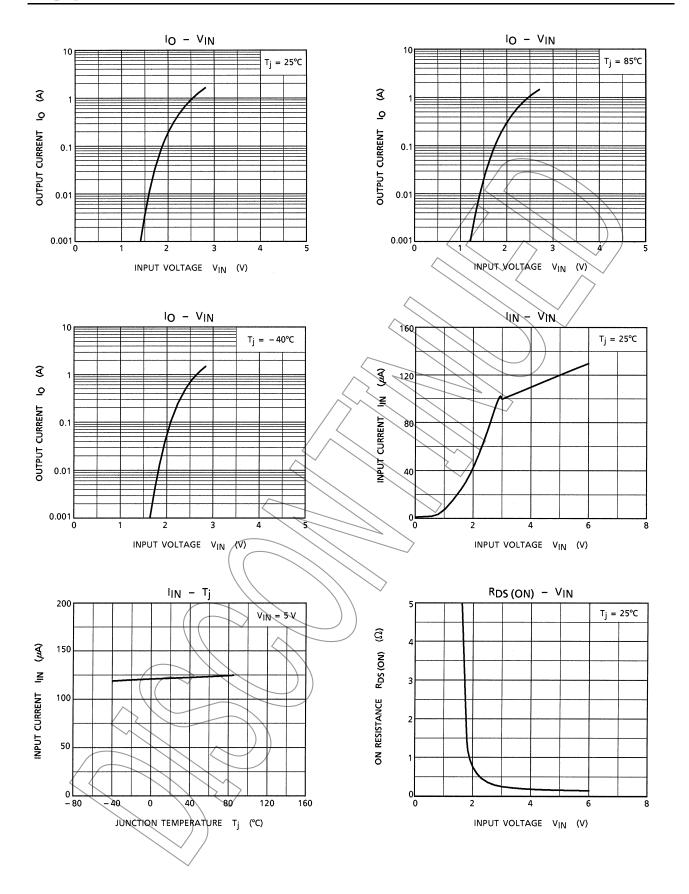
Test circuit

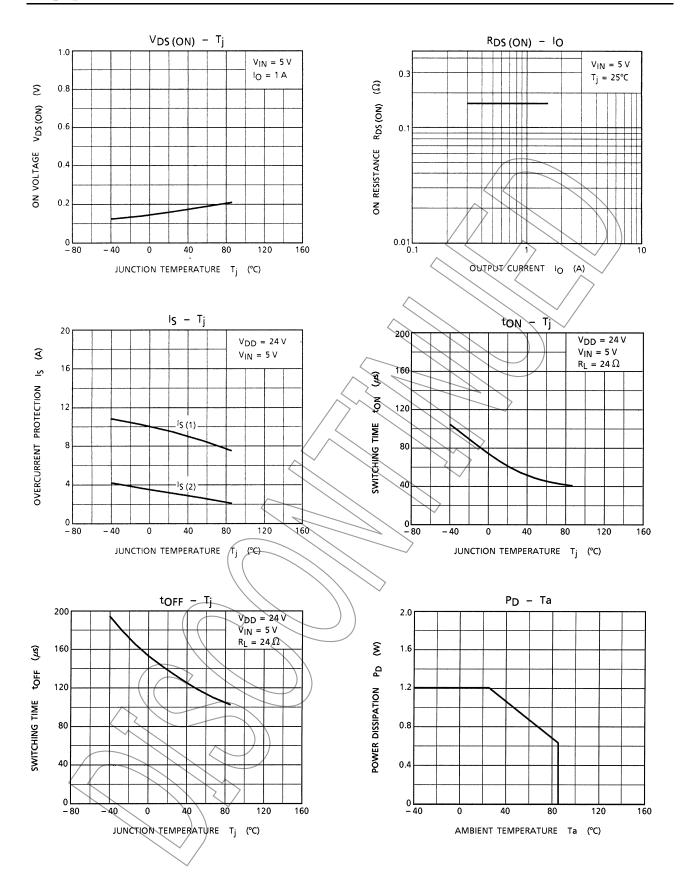


### Measured waveforms



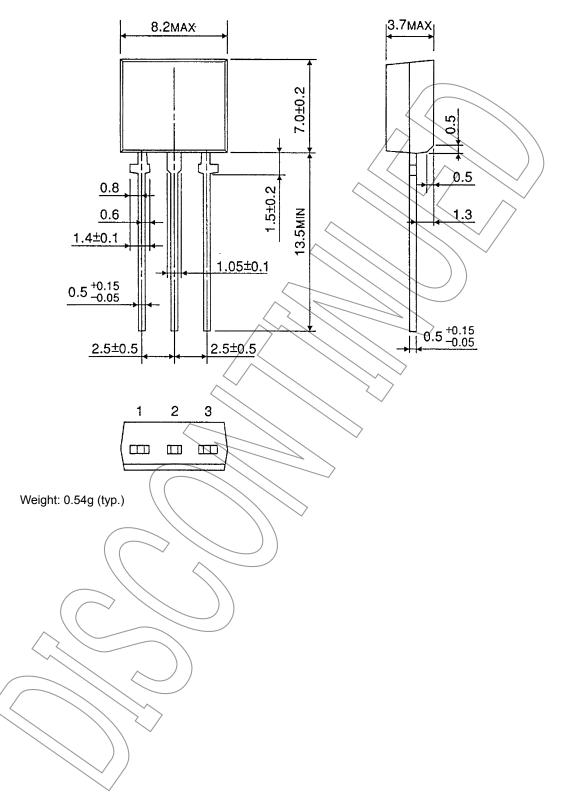
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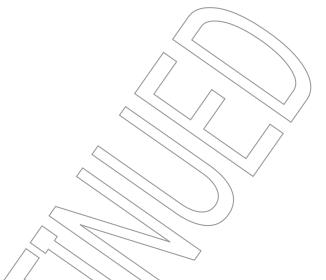


## **Package Dimensions**

SIP3-P-2.50 Unit: mm



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