

<IGBT Modules>

APPLICATION

CM100RX-12A

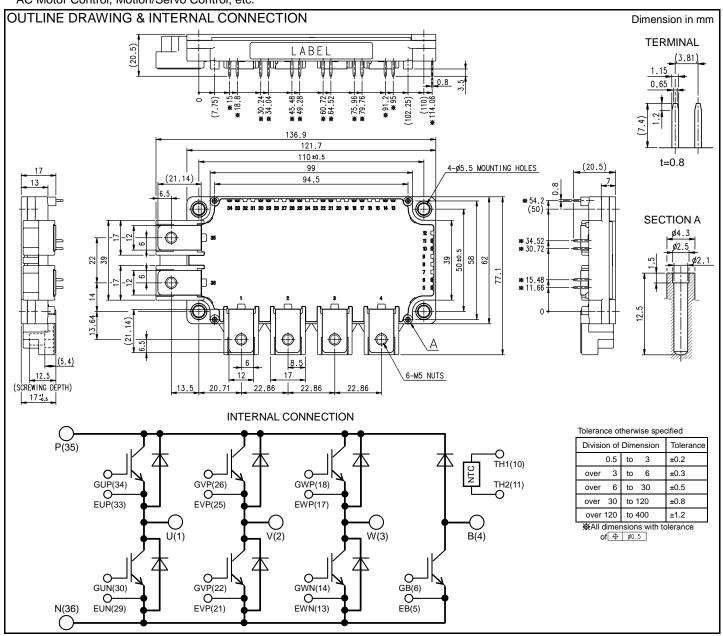
HIGH POWER SWITCHING USE INSULATED TYPE



- Flat base Type
- Copper base plate (non-plating)
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

sevenpack (3φ Inverter + Brake Chopper)

AC Motor Control, Motion/Servo Control, etc.



<IGBT Modules>

CM100RX-12A

HIGH POWER SWITCHING USE

INSULATED TYPE

MAXIMUM RATINGS (T_j=25 °C, unless otherwise specified)

INVERTER PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	600	V
V _{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Callantan aumant	DC, T _C =75 °C (Note2, 4)	100	^
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	200	A
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	400	W
I _E (Note1)	Conitton ourrent	DC (Note2)	100	^
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	200	A

BRAKE PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	600	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector overent	DC, T _C =97 °C (Note2, 4)	50	_
I _{CRM}	- Collector current	Pulse, Repetitive (Note3)	100	A
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	280	W
V_{RRM}	Repetitive peak reverse voltage	G-E short-circuited	600	V
I _F	Forward current	DC (Note2)	50	_
I _{FRM}	- Forward current	Pulse, Repetitive (Note3)	100	A

MODULE

Symbol	Item	Conditions	Rating	Unit
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
Tj	Junction temperature	-	-40 ~ + 150	°C
T _{stg}	Storage temperature	-	-40 ~ +125	C
T _{Cmax}	Maximum case temperature	(Note4)	125	°C

ELECTRICAL CHARACTERISTICS ($T_{j=}25~^{\circ}\text{C}$, unless otherwise specified)

INVERTER PART IGBT/DIODE

Company and	lto	Conditions		Limits			Linia
Symbol	Item Conditions		Min.	Тур.	Max.	Unit	
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =10 mA, V _{CE} =10 V		5	6	7	V
		I _C =100 A, V _{GE} =15 V (Note5)	T _j =25 °C	-	1.7	2.1	
V_{CEsat}	Collector-emitter saturation voltage	Refer to the figure of test circuit	T _j =125 °C	-	1.9	-	V
		I _C =100 A, V _{GE} =15 V, chip (Note5)		-	1.6	-	
Cies	Input capacitance			-	-	13.3	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	1.4	nF
Cres	Reverse transfer capacitance		-	-	-	0.45	
Q _G	Gate charge	V _{CC} =300 V, I _C =100 A, V _{GE} =15 V		-	270	-	nC
t _{d(on)}	Turn-on delay time	V 000 V I 400 A V 45 V		-	-	100	
tr	Rise time	V_{CC} =300 V, I_{C} =100 A, V_{GE} =±15 V,	-	-	-	100	
t _{d(off)}	Turn-off delay time	B. C.O.O. Individual land		-	-	300	ns
t _f	Fall time	R_G =6.2 Ω, Inductive load		-	-	600	
r _g	Internal gate resistance	Per switch		-	0	-	Ω

<IGBT Modules>

CM100RX-12A

HIGH POWER SWITCHING USE

INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont.; T_j=25 °C, unless otherwise specified)

INVERTER PART IGBT/DIODE

Command and	ltom Conditions			Limits			I Imit	
Symbol	Item	Conditions		Min.	Тур.	Max.	Unit	
		I _E =100 A, G-E short-circuited (Note5)	T _j =25 °C	-	2.0	2.8		
V _{EC} (Note1)	Emitter-collector voltage	Refer to the figure of test circuit	T _j =125 °C	-	1.95	-	V	
		I _E =100 A, G-E short-circuited, chip (Note5)		-	1.9	-		
t _{rr} (Note1)	Reverse recovery time	V _{CC} =300 V, I _E =100 A, V _{GE} =±15 V,		=	-	200	ns	
Q _{rr} (Note1)	Reverse recovery charge	R _G =6.2 Ω, Inductive load		-	3.6	-	μC	
Eon	Turn-on switching energy per pulse	V _{CC} =300 V, I _C =I _E =100 A,		-	1.6	-	I	
E _{off}	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=6.2 \Omega, T_{j}=125 \text{ °C},$		-	5.2	-	mJ	
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	1.1	-	mJ	

BRAKE PART IGBT/DIODE

Cymhal		Conditions		Limits			l lait
Symbol	Item	Conditions	Conditions		Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		=	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I_C =5 mA, V_{CE} =10 V		5	6	7	V
		I _C =50 A, V _{GE} =15 V (Note5)	T _j =25 °C	-	1.7	2.1	
V_{CEsat}	Collector-emitter saturation voltage	Refer to the figure of test circuit	T _j =125 °C	-	1.9	-	V
		I _C =50 A, V _{GE} =15 V, chip (Note5)		-	1.6	-	
Cies	Input capacitance			-	-	9.3	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	1.0	nF
Cres	Reverse transfer capacitance			-	-	0.3	
Q _G	Gate charge	V _{CC} =300 V, I _C =50 A, V _{GE} =15 V		-	200	-	nC
I _{RRM}	Repetitive peak reverse current	V _R =V _{RRM} , G-E short-circuited		-	-	1.0	mA
		I _F =50 A, G-E short-circuited (Note5)	T _j =25 °C	-	2.0	2.8	
V_{F}	Forward voltage	Refer to the figure of test circuit	T _j =125 °C	-	1.95	-	V
		I _F =50 A, G-E short-circuited, chip (N	ote5)	-	1.9	-	1
r _g	Internal gate resistance	-		-	0	-	Ω

NTC THERMISTOR PART

Symbol	Item	Conditions		Unit		
			Min.	Тур.	Max.	Offic
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note4)	-7.3	-	+7.8	%
B _(25/50)	B-constant	Approximate by equation (Note6)	-	3375	-	K
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
		Conditions	Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	=	0.31	K/W
$R_{th(j-c)D}$		Junction to case, per Inverter DIODE (Note4)	-	i	0.59	FX/VV
$R_{th(j-c)Q}$		Junction to case, Brake IGBT (Note4)	-	i	0.44	K/W
$R_{th(j-c)D}$		Junction to case, Brake DIODE (Note4)	-	i	0.85	IN/VV
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, per 1 module, Thermal grease applied (Note4, 7)	-	15	-	K/kW

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HIGH POWER SWITCHING USE

INSULATED TYPE

MECHANICAL CHARACTERISTICS

Cymphol	lane Condition			Limits			l lait	
Symbol	Item	Conditions		Min.	Тур.	Max.	Unit	
Mt	Mounting torque	Main terminals	M 5 screw	2.5	3.0	3.5	N∙m	
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N∙m	
٦	Creepage distance	Terminal to terminal		10.28	-	-		
ds		Terminal to base plate		12.46	-	-	mm	
d	Clearance	Terminal to terminal		9.88	-	-	mm	
da	Clearance	Terminal to base plate		10.12	-	-	mm	
m	mass	-		-	350	-	g	
ec	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+100	μm	

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (DIODE).

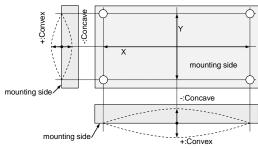
- 2. Junction temperature (T_j) should not increase beyond $T_{j\,m\,a\,x}$ rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_i) dose not exceed T_{jmax} rating.
- 4. Case temperature (T_c) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise.

6.
$$B_{(25/50)} = In(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$
,

 $R_{25}\!:$ resistance at absolute temperature T $_{25}$ [K]; T $_{25}\!=\!25$ [°C]+273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K]; $T_{50}\!=\!50$ [°C]+273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K).
- 8. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



9. Use the following screws when mounting the printed circuit board (PCB) on the standoffs.

"φ2.3×10 or φ2.3×12, B1 tapping screw"

The length of the screw depends on the thickness (t1.6~t2.0) of the PCB.

RECOMMENDED OPERATING CONDITIONS

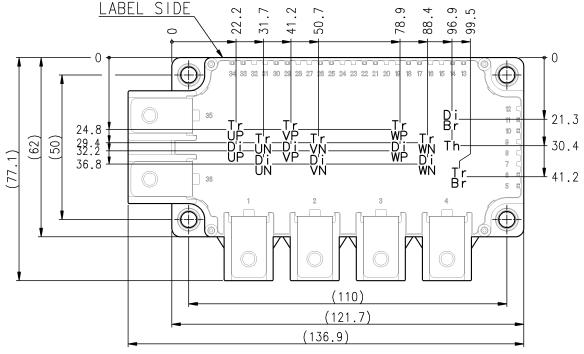
Symbol	Item	Conditions		Limits			Unit
Symbol	item			Min.	Тур.	Max.	Offit
V _{cc}	(DC) Supply voltage	Applied across P-N terminals		-	300	400	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across GB-EB / G*P-E*P / G*N-E*N (*=U, V, W) terminals		13.5	15.0	16.5	V
R_G	External gate resistance	Per switch	Inverter IGBT	6.0	i	62	Ω
K _G	Literrial gate resistance	rei switch	Brake IGBT	13	-	125	77

HIGH POWER SWITCHING USE

INSULATED TYPE

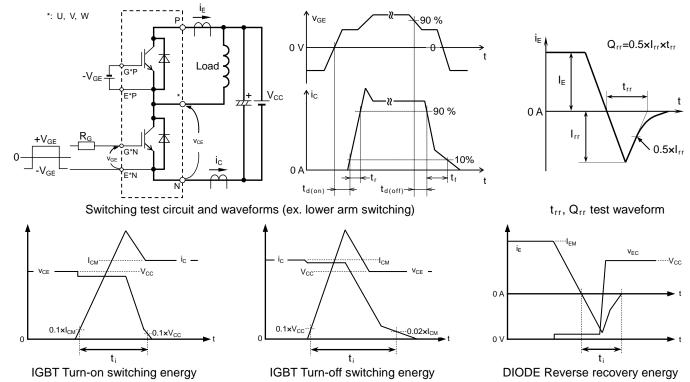
CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm



Tr*P/Tr*N/TrBr: IGBT, Di*P/Di*N: DIODE (*=U/V/W), DiBr: BRAKE DIODE, Th: NTC thermistor

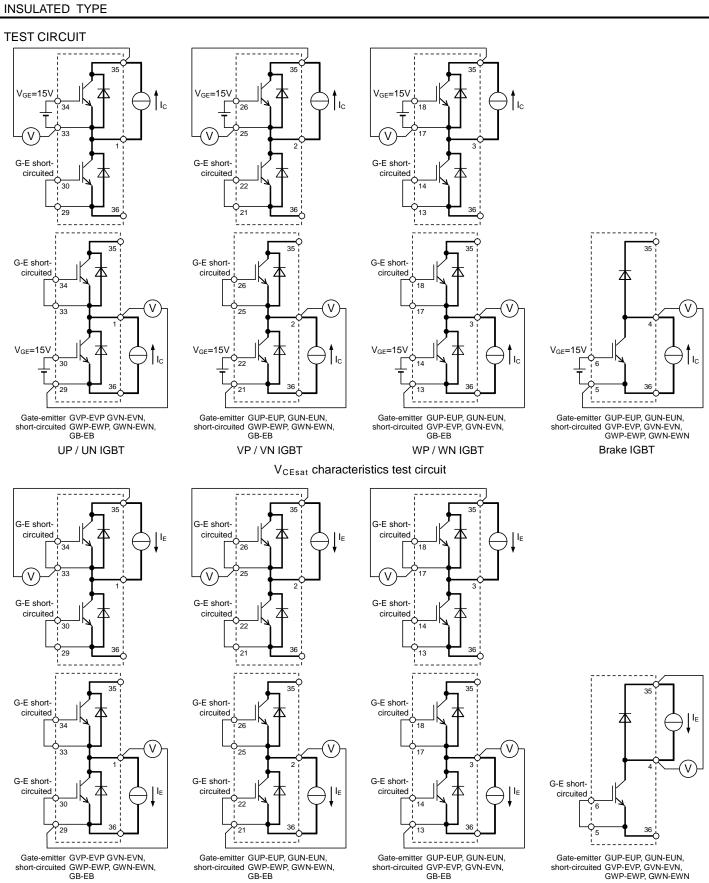
TEST CIRCUIT AND WAVEFORMS



Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

Ver.2.0

HIGH POWER SWITCHING USE



Ver.2.0

UP / UN DIODE

V_{EC} / Brake diode V_F characteristics test circuit

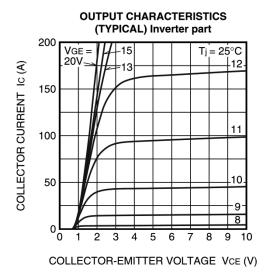
short-circuited GVP-EVP, GVN-EVN, GB-EB

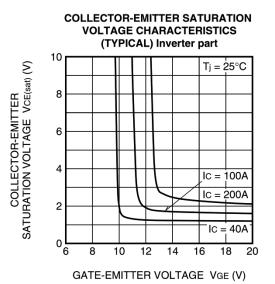
Brake DIODE

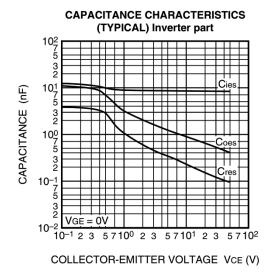
HIGH POWER SWITCHING USE INSULATED TYPE

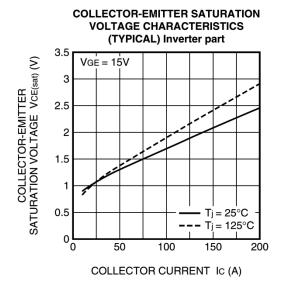
PERFORMANCE CURVES

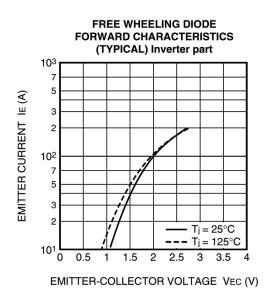
INVERTER PART

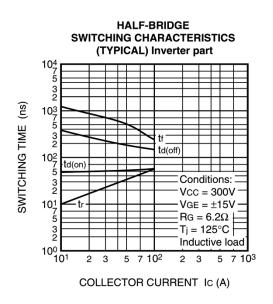








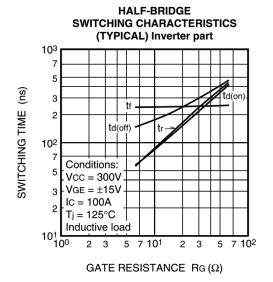




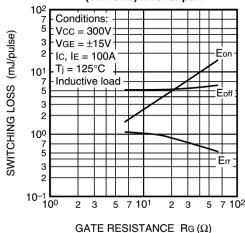
HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

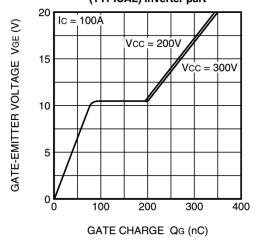
INVERTER PART



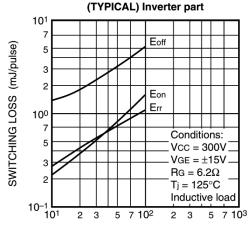
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL) Inverter part



GATE CHARGE CHARACTERISTICS (TYPICAL) Inverter part

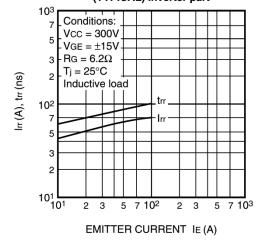


HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL) Inverter part

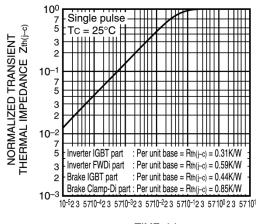


COLLECTOR CURRENT IC (A) EMITTER CURRENT IE (A)

REVERSE RECOVERY CHARACTERISTICS OF FREE WHEELING DIODE (TYPICAL) Inverter part



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS



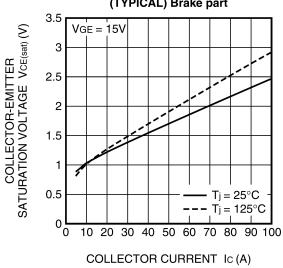
TIME (s)

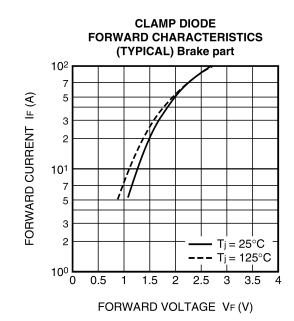
HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

BRAKE PART

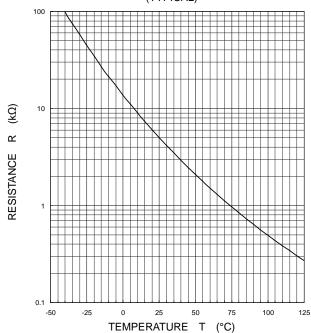
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL) Brake part





NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



HIGH POWER SWITCHING USE INSULATED TYPE

Keep safety first in your circuit designs!

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