

Silicon Diffused Power Transistor

BUJ105A

GENERAL DESCRIPTION

High-voltage, high-speed planar-passivated npn power switching transistor in TO220AB envelope intended for use in high frequency electronic lighting ballast applications, converters, inverters, switching regulators, motor control systems, etc.

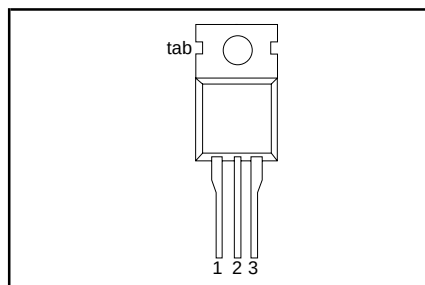
QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V_{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0\text{ V}$	-	700	V
V_{CBO}	Collector-Base voltage (open emitter)		-	700	V
V_{CEO}	Collector-emitter voltage (open base)		-	400	V
I_C	Collector current (DC)		-	8	A
I_{CM}	Collector current peak value		-	16	A
P_{tot}	Total power dissipation	$T_{mb} \leq 25\text{ }^{\circ}\text{C}$	-	80	W
V_{CESat}	Collector-emitter saturation voltage	$I_C = 4.0\text{ A}; I_B = 0.8\text{ A}$	0.3	1.0	V
h_{FEsat}		$I_C = 4.0\text{ A}; V_{CE} = 5\text{ V}$	11	15	
t_f	Fall time	$I_C = 5\text{ A}; I_{B1} = 1\text{ A}$	20	50	ns

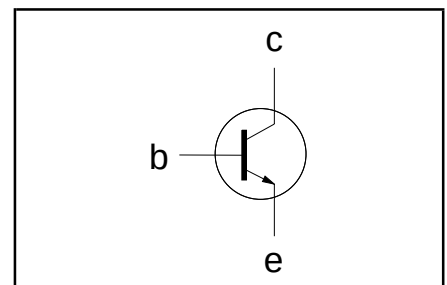
PINNING - TO220AB

PIN	DESCRIPTION
1	base
2	collector
3	emitter
tab	collector

PIN CONFIGURATION



SYMBOL

LIMITING VALUES⁸

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CESM}	Collector to emitter voltage	$V_{BE} = 0\text{ V}$	-	700	V
V_{CEO}	Collector to emitter voltage (open base)		-	400	V
V_{CBO}	Collector to base voltage (open emitter)		-	700	V
I_C	Collector current (DC)		-	8	A
I_{CM}	Collector current peak value		-	16	A
I_B	Base current (DC)		-	4	A
I_{BM}	Base current peak value		-	8	A
P_{tot}	Total power dissipation	$T_{mb} \leq 25\text{ }^{\circ}\text{C}$	-	80	W
T_{stg}	Storage temperature		-65	150	$^{\circ}\text{C}$
T_j	Junction temperature		-	150	$^{\circ}\text{C}$

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
$R_{th\ j-mb}$	Junction to mounting base		-	1.56	K/W
$R_{th\ j-a}$	Junction to ambient	in free air	60	-	K/W

Silicon Diffused Power Transistor

BUJ105A

STATIC CHARACTERISTICS

 $T_{mb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_{CES}, I_{CBO} I_{CES}	Collector cut-off current ¹	$V_{BE} = 0\text{ V}; V_{CE} = V_{CESMmax}$ $V_{BE} = 0\text{ V}; V_{CE} = V_{CESMmax}$ $T_j = 125\text{ }^{\circ}\text{C}$	- -	- -	0.2 0.5	mA mA
I_{CEO} I_{EBO} $V_{CEOsust}$	Collector cut-off current Emitter cut-off current Collector-emitter sustaining voltage	$V_{CEO} = V_{CEOMmax} (400\text{V})$ $V_{EB} = 9\text{ V}; I_C = 0\text{ A}$ $I_B = 0\text{ A}; I_C = 10\text{ mA};$ $L = 25\text{ mH}$	- - 400	- - -	0.1 1 -	mA mA V
V_{CESat} V_{BESat}	Collector-emitter saturation voltage Base-emitter saturation voltage	$I_C = 4.0\text{ A}; I_B = 0.8\text{ A}$ $I_C = 4.0\text{ A}; I_B = 0.8\text{ A}$	- -	0.3 1.0	1.0 1.5	V V
h_{FE} h_{FE} h_{FEsat}	DC current gain	$I_C = 1\text{ mA}; V_{CE} = 5\text{ V}$ $I_C = 500\text{ mA}; V_{CE} = 5\text{ V}$ $I_C = 4.0\text{ A}; V_{CE} = 5\text{ V}$	10 13 8	14 23 11	34 36 15	

DYNAMIC CHARACTERISTICS

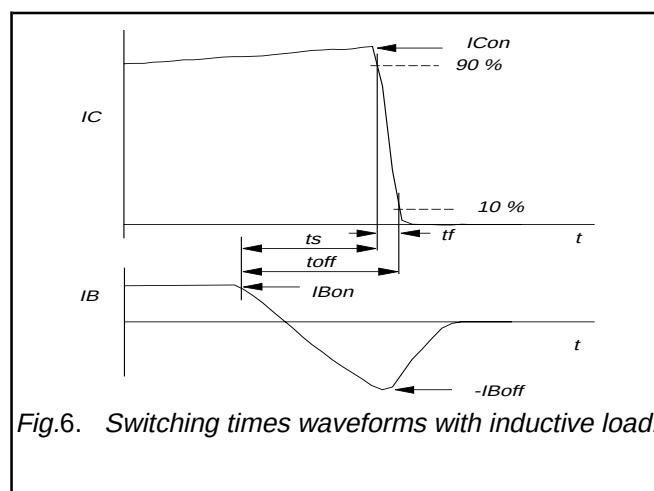
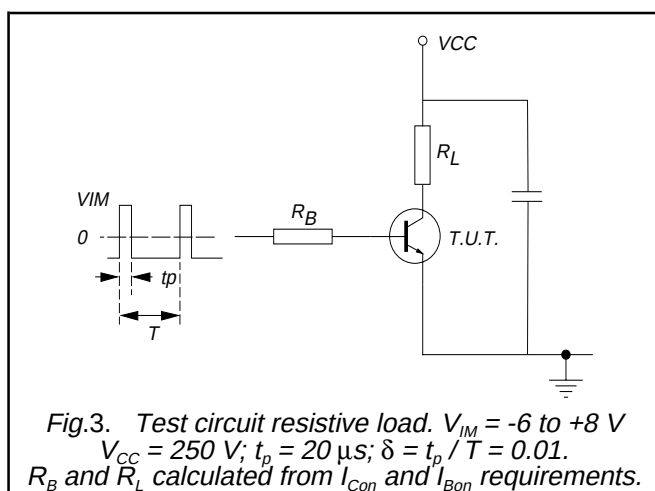
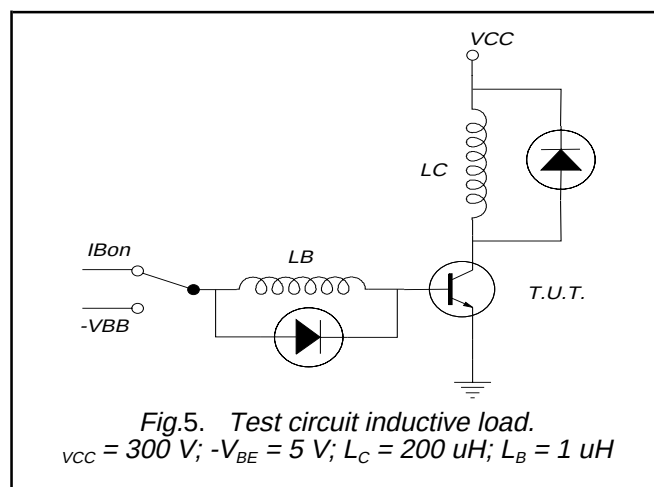
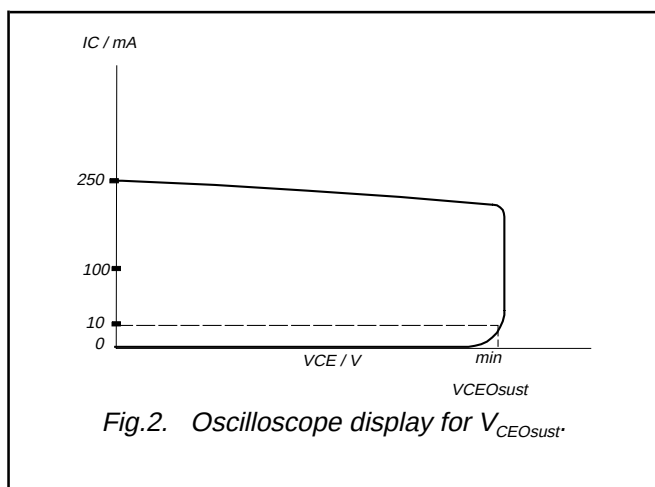
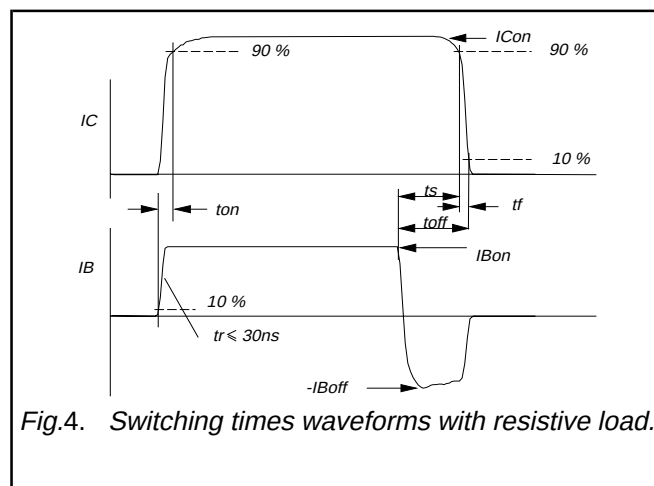
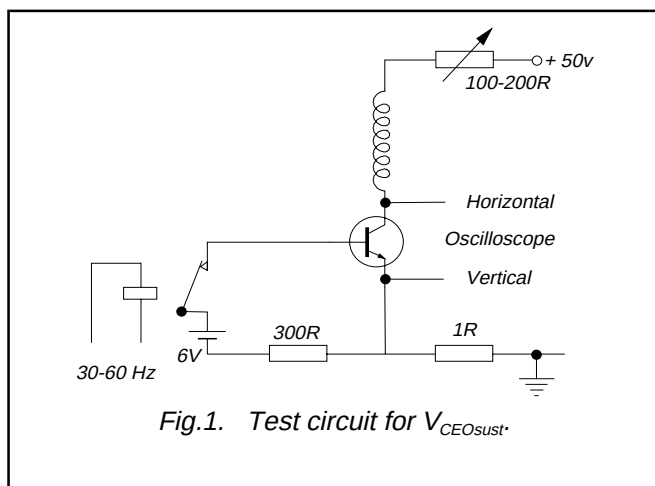
 $T_{mb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
t_{on} t_s t_f	Switching times (resistive load) Turn-on time Turn-off storage time Turn-off fall time	$I_{Con} = 5\text{ A}; I_{Bon} = -I_{Boff} = 1\text{ A};$ $R_L = 75\text{ ohms}; V_{BB2} = 4\text{ V};$	0.65 1.8 0.3	1 2.5 0.5	μs μs μs
t_s t_f	Switching times (inductive load) Turn-off storage time Turn-off fall time	$I_{Con} = 5\text{ A}; I_{Bon} = 1\text{ A}; L_B = 1\text{ }\mu\text{H};$ $-V_{BB} = 5\text{ V}$	1.2 20	1.7 50	μs ns
t_s t_f	Switching times (inductive load) Turn-off storage time Turn-off fall time	$I_{Con} = 5\text{ A}; I_{Bon} = 1\text{ A}; L_B = 1\text{ }\mu\text{H};$ $-V_{BB} = 5\text{ V}; T_j = 100\text{ }^{\circ}\text{C}$	1.4 25	1.9 100	μs ns

¹ Measured with half sine-wave voltage (curve tracer).

Silicon Diffused Power Transistor

BUJ105A



Silicon Diffused Power Transistor

BUJ105A

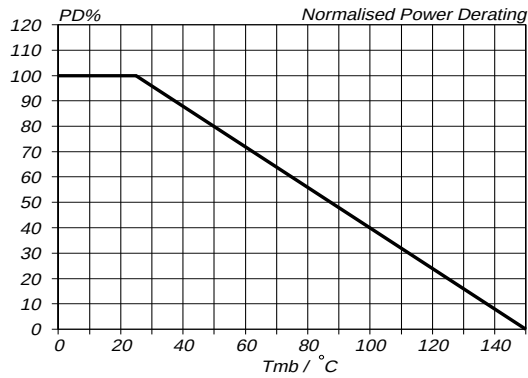


Fig. 7. Normalised power dissipation.
 $PD\% = 100 \cdot PD / PD_{25^\circ C} = f(T_{mb})$

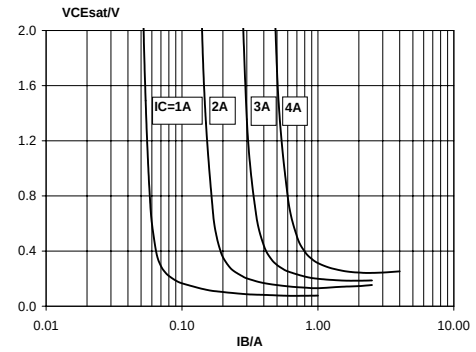


Fig. 10. Collector-Emitter saturation voltage.
 Solid lines = typ values, $V_{CEsat} = f(I_B)$; $T_J = 25^\circ C$.

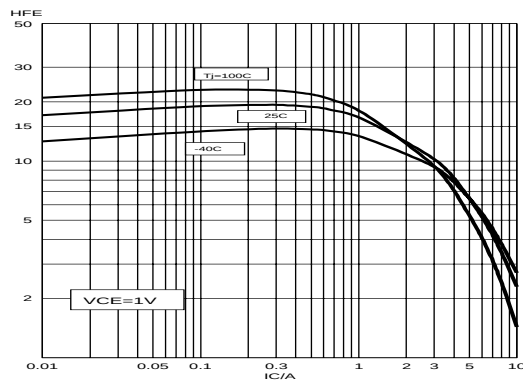


Fig. 8. Typical DC current gain. $h_{FE} = f(I_C)$
 parameter V_{CE}

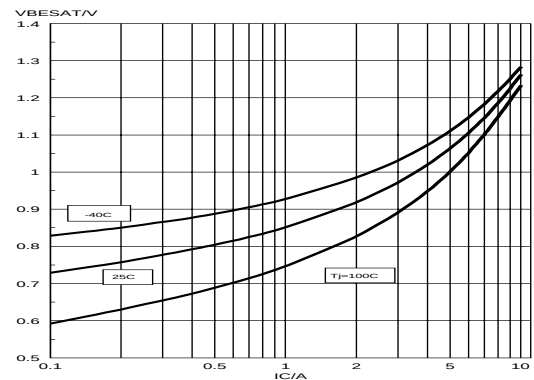


Fig. 11. Base-Emitter saturation voltage.
 Solid lines = typ values, $V_{BEsat} = f(I_C)$; at $I_C/I_B = 4$.

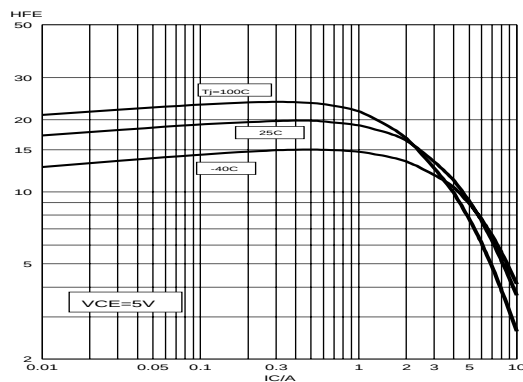


Fig. 9. Typical DC current gain. $h_{FE} = f(I_C)$
 parameter V_{CE}

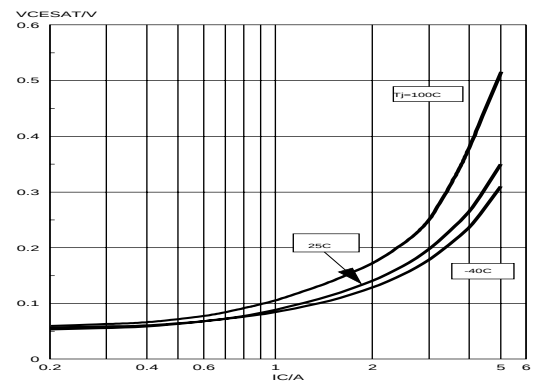


Fig. 12. Collector-Emitter saturation voltage.
 Solid lines = typ values, $V_{CEsat} = f(I_C)$; at $I_C/I_B = 4$.

Silicon Diffused Power Transistor

BUJ105A

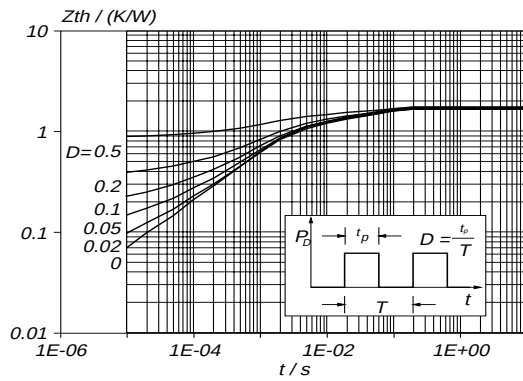


Fig.13. Transient thermal impedance.
 $Z_{th\ j-mb} = f(t)$; parameter $D = t_p/T$

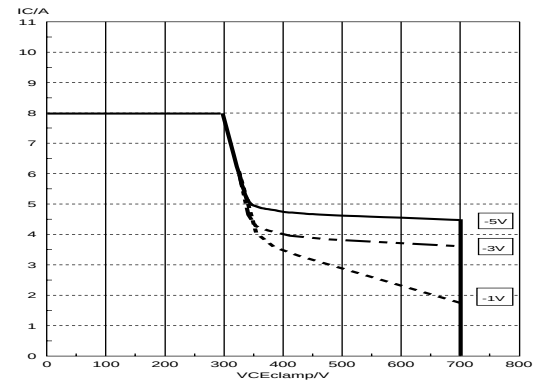


Fig.15. Reverse bias safe operating area ($T_j < T_{jmax}$)
 for $-V_{BE} = 5V, 3V \text{ \& } 1V$.

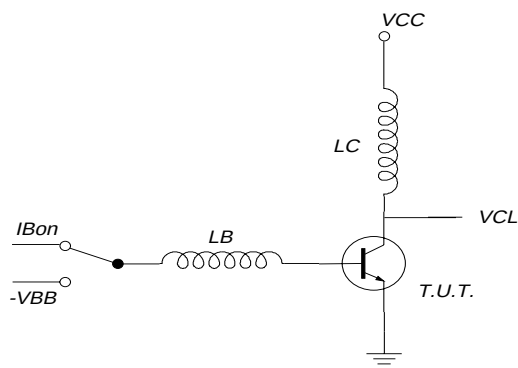


Fig.14. Test circuit for reverse bias safe operating area.

$V_{clamp} < 700V$; $V_{CC} = 150V$; $-V_{BE} = 5V, 3V \text{ \& } 1V$;
 $L_B = 1\mu H$; $L_C = 200\mu H$.

Silicon Diffused Power Transistor

BUJ105A

MECHANICAL DATA

Dimensions in mm

Net Mass: 2 g

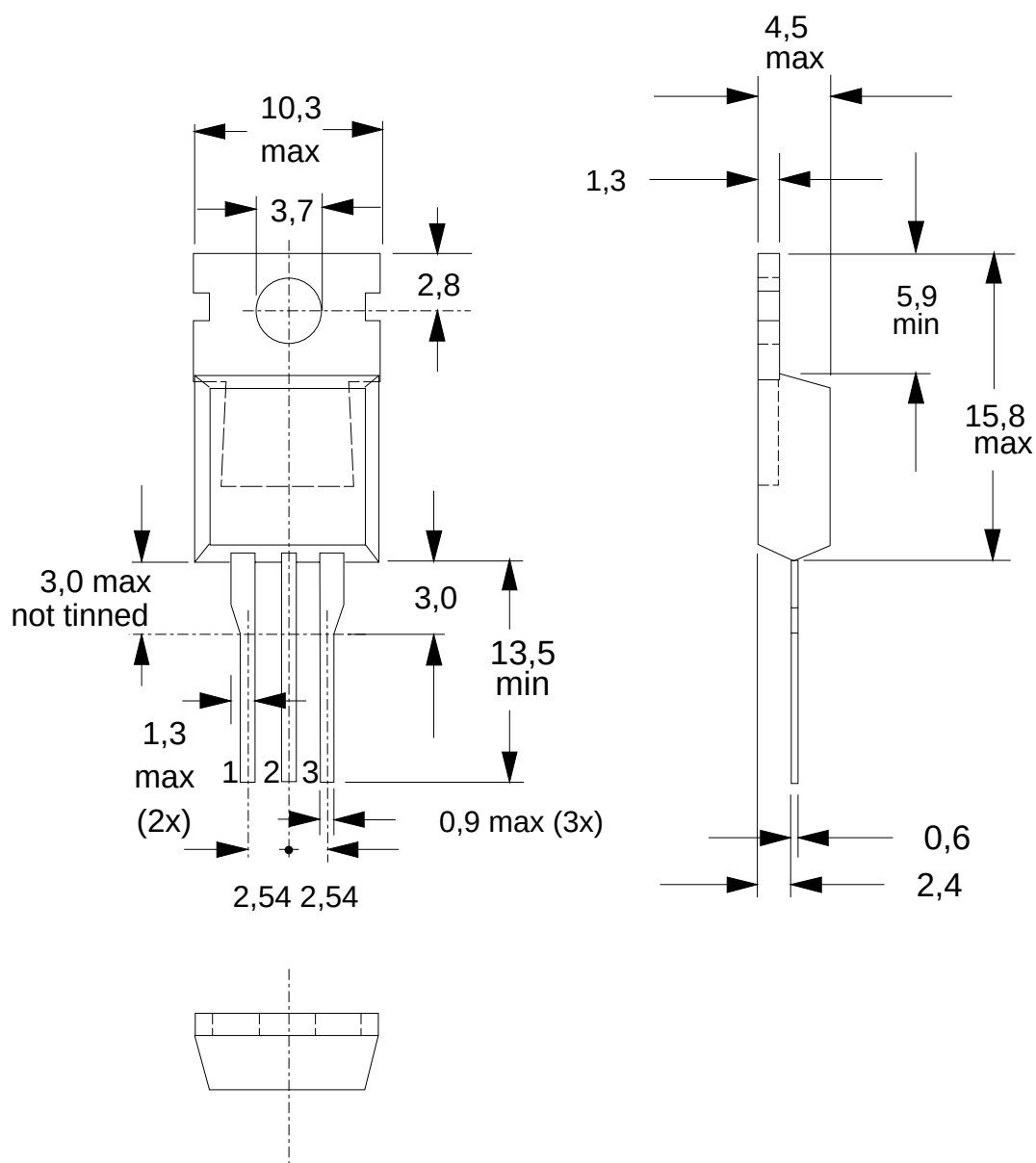


Fig.16. TO220AB; pin 2 connected to mounting base.

Notes

1. Refer to mounting instructions for TO220 envelopes.
2. Epoxy meets UL94 V0 at 1/8".

Silicon Diffused Power Transistor

BUJ105A

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	
© Philips Electronics N.V. 1999	
All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.	
The information presented in this document does not form part of any quotation or contract, it is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent or other industrial or intellectual property rights.	

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.