



EVAL6920D

EVAL6920D 1A high efficiency synchronous Rectifier step up converter

Data Brief

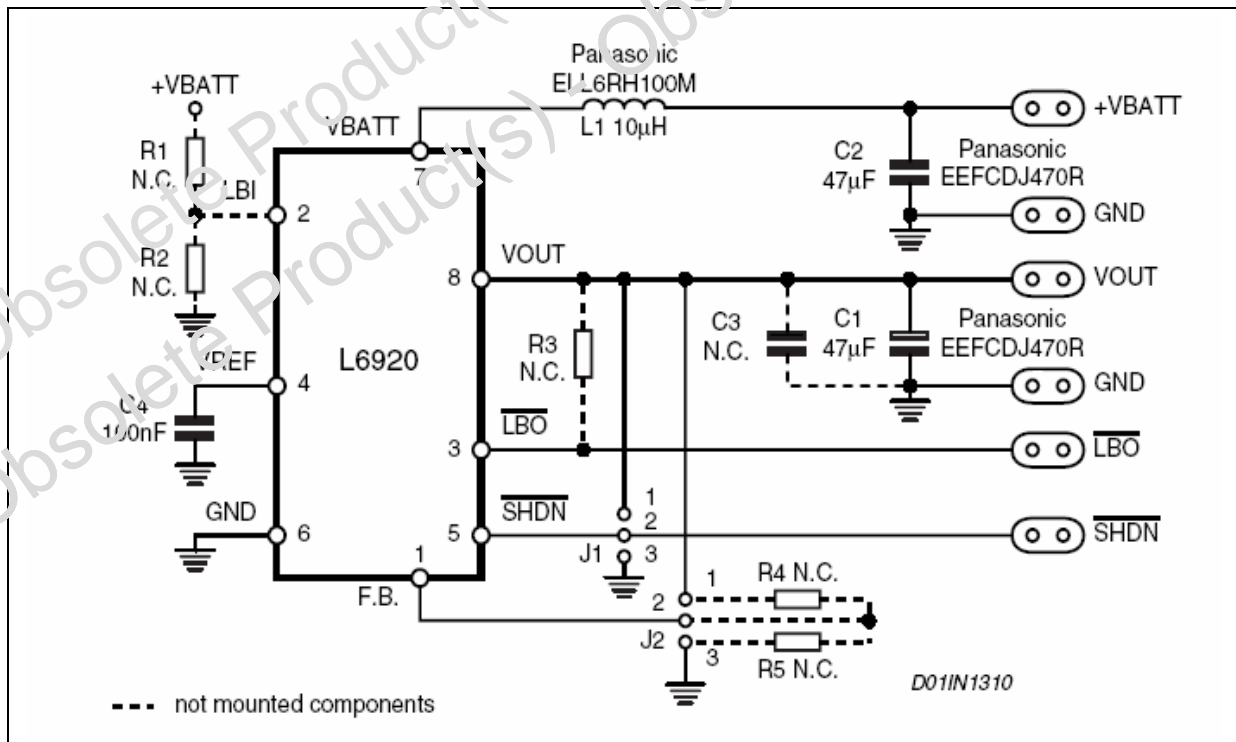
General description

The EVAL6920D Evaluation Board integrates the high efficiency step up converter L6920D as well as the external components required for a typical application.

The start up is guaranteed at 1V and the device is operating down to 0.6V. The output voltage can be adjusted from 2V to 5.2V and two fixed output voltages (3.3V and 5V) are also available.



Evaluation board schematic



1 Features

Table 1. Evaluation board part list

| Jumper | Position | Function |
|--------|----------|--|
| J1 | 1-2 | Device enabled |
| | 2-3 | Device disabled |
| J2 | None | Adjustable using R4 and R5 (not mounted) |
| | 1-2 | 3.3V output voltage |
| | 2-3 | 5V output voltage |

R4, R5 should be selected in the range of 100kΩ - 10MΩ to minimize consumption and error due to current sunk pin (few nA).

Figure 1. Package - TSSOP8

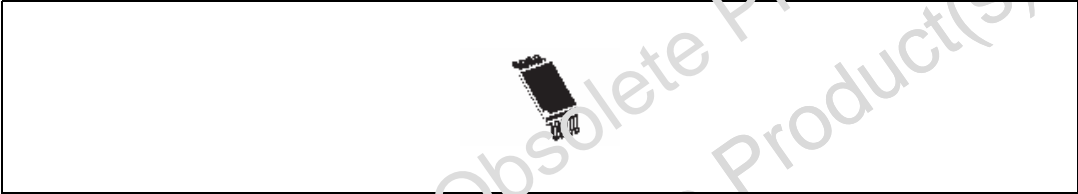


Figure 2. Demoboard components (top side)

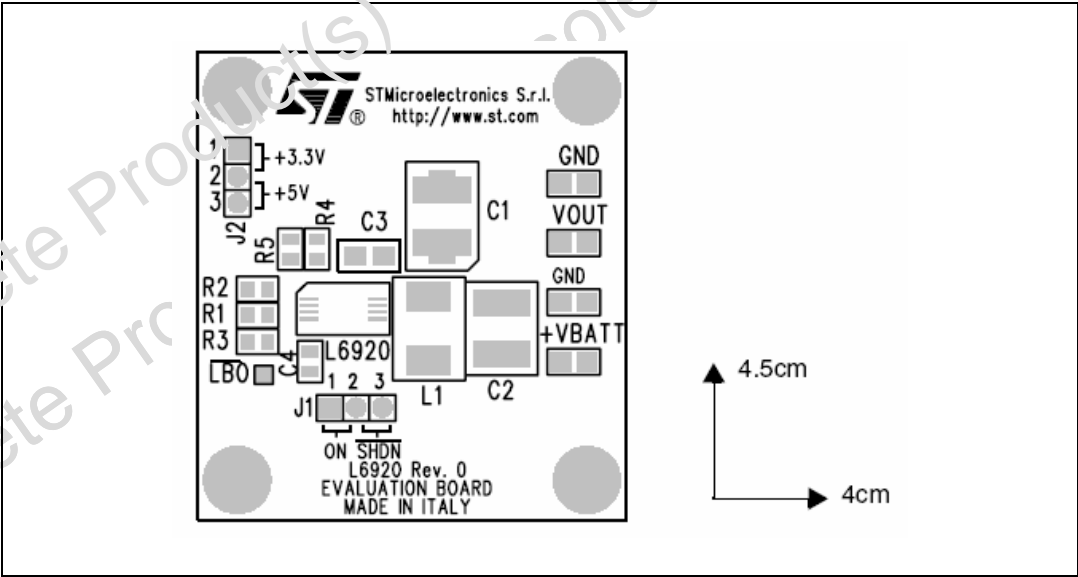


Figure 3. Demoboard layout (top side)

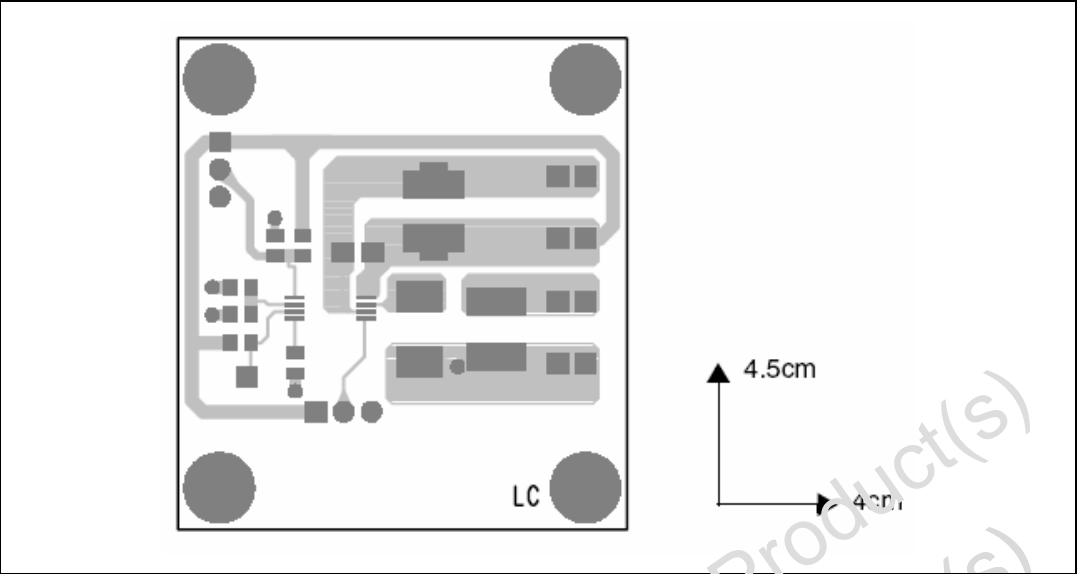
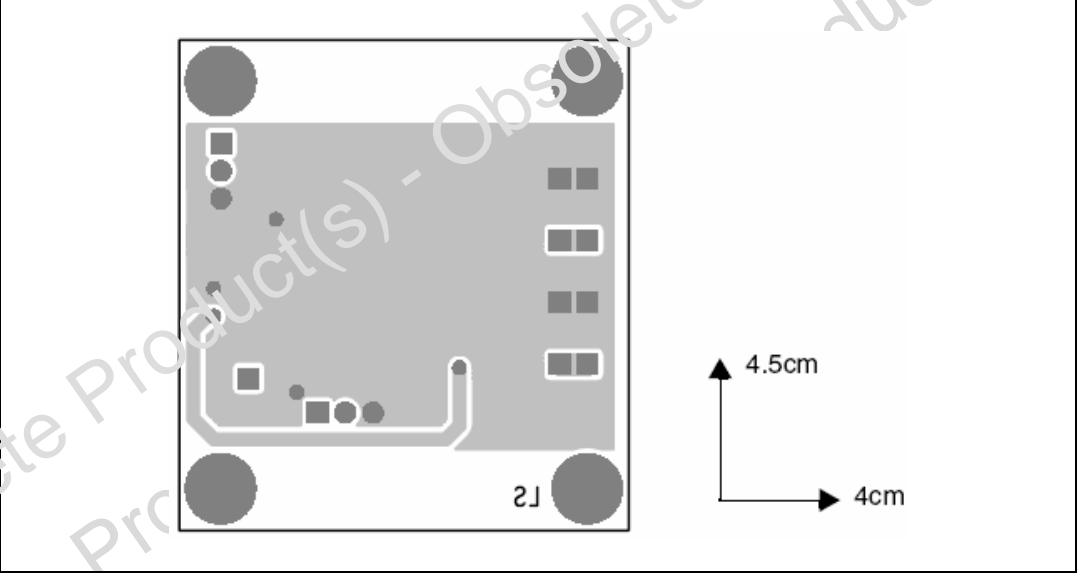


Figure 4. Demoboard layout (bottom side)



2 Revision history

Table 2. Revision history

| Date | Revision | Changes |
|-------------|----------|-------------|
| 22-Nov-2006 | 1 | First issue |

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