

TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

# TA8205AHQ, TA8205ALQ

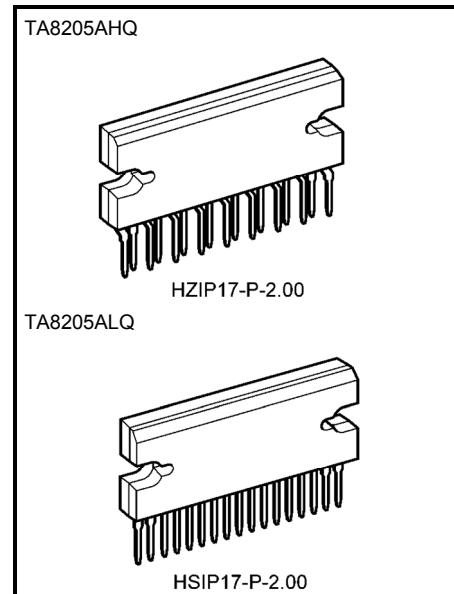
## 18W BTL × 2CH Audio Power Amplifier

The thermal resistance  $\theta_{j-T}$  of TA8205AHQ, TA8205ALQ package designed for low thermal resistance, has a high efficiency of heat radiation.

The temperature rise of chip can be reduced, and the influence from the degradation of the features due to the temperature rise at the high output can also be reduced.

This stereo audio power IC, designed for car audio use, has two built-in channels to reduce the characteristic difference between L and R channels.

In addition, the functions of stand-by and muting, and a variety of protection circuits are involved.



### Weight

HZIP17-P-2.00 : 9.8g (typ.)

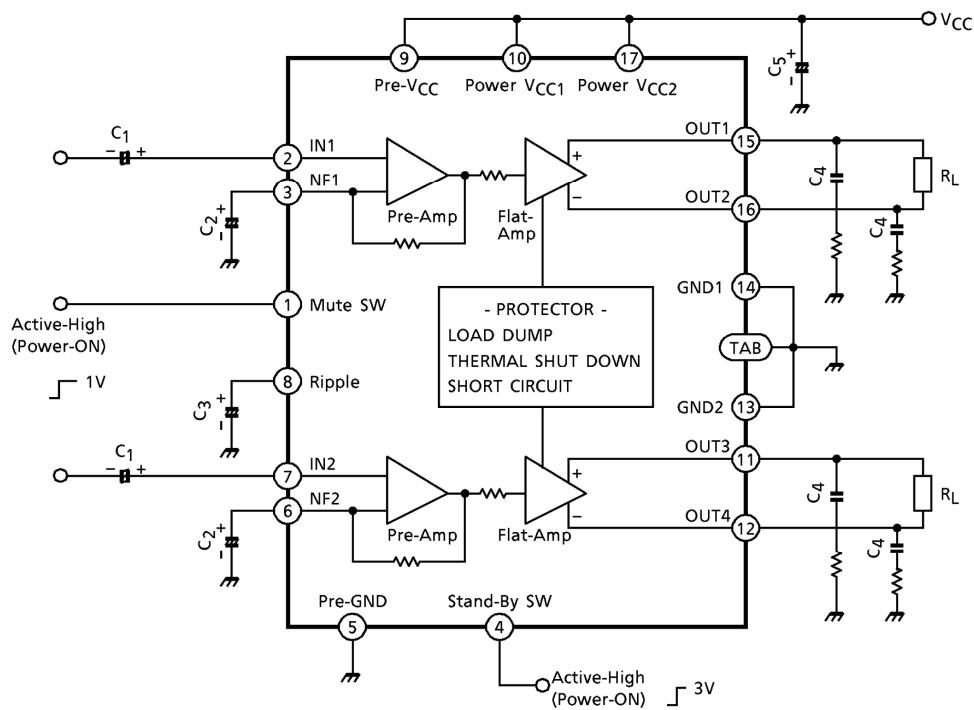
HSIP17-P-2.00 : 9.8g (typ.)

## Features

- Low thermal resistance  
:  $\theta_{j-T} = 1.5^{\circ}\text{C} / \text{W}$  (infinite heat sink)
- High power  
:  $\text{POUT (1)} = 18\text{W}$  (typ.) / channel  
( $\text{VCC} = 14.4\text{V}$ ,  $f = 1\text{kHz}$ ,  $\text{THD} = 10\%$ ,  $\text{RL} = 4\Omega$ )  
 $\text{POUT (2)} = 15\text{W}$  (typ.) / channel  
( $\text{VCC} = 13.2\text{V}$ ,  $f = 1\text{kHz}$ ,  $\text{THD} = 10\%$ ,  $\text{RL} = 4\Omega$ )
- Low distortion ratio :  $\text{THD} = 0.04\%$  (typ.) ( $\text{VCC} = 13.2\text{V}$ ,  $f = 1\text{kHz}$ ,  $\text{POUT} = 1\text{W}$ ,  $\text{RL} = 4\Omega$ ,  $\text{GV} = 50\text{dB}$ )
- Low noise :  $\text{VNO} = 0.30\text{mV}_{\text{rms}}$  (typ.) ( $\text{VCC} = 13.2\text{V}$ ,  $\text{RL} = 4\Omega$ ,  $\text{GV} 50\text{dB}$ ,  $\text{Rg} = 0\Omega$ ,  $\text{BW} = 20\text{Hz} \sim 20\text{kHz}$ )
- Built-in stand-by function (with pin(4) set at low, power is turned off.) :  $\text{ISB} = 1\mu\text{A}$  (typ.)
- Built-in muting function (with pin(1) set at low, power is turned off.) :  $\text{V} (\text{mute}) = 1\text{V}$  (typ.)
- Built-in various protection circuits  
Protection circuits : Thermal shut down, Over voltage, Out → VCC short, Out → GND short and Out – Out short.
- Operating supply voltage :  $\text{VCC} = 9 \sim 18\text{V}$

## Block Diagram

TA8205AHQ, TA8205ALQ ( $G_V = 50\text{dB}$ )



## Cautions And Application Method (description is made only on the single channel.)

### 1. Voltage gain adjustment

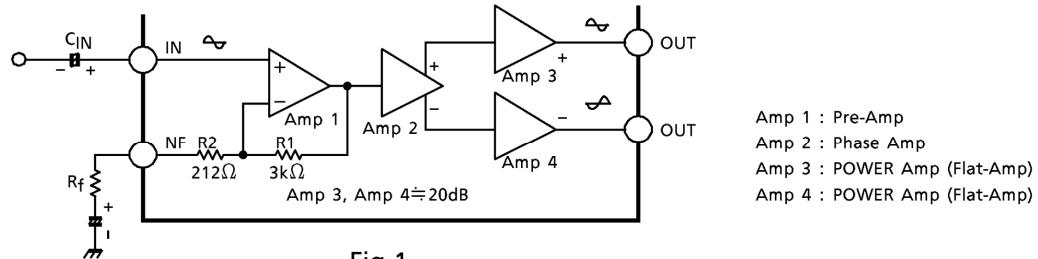


Fig.1

This IC has the amplifier construction as shown in Fig.1. The pre-amp. Amp 1 is provided to the primary stage, and the input voltage is amplified by the flat amps, amp 3 and amp 4 of each channel through the phase amp. Amp 2.

Since the input offset is prevented by pre-amp when VCC is set to on, this circuit can remarkably reduce the pop noise.

The total closed loop gain  $G_V$  of this IC can be obtained by expression below when the closed loop voltage gain of Amp 1 is  $G_{V1}$ .

$$G_{V1} = 20 \log \frac{R_1 + (R_f + R_2)}{R_f + R_2} \text{ (dB)} \quad \dots \quad (1)$$

The closed loop voltage gain of power Amp, Amp 3 and Amp 4 is fixed at  $G_{V3} \approx G_{V4} = 20 \text{ dB}$ .

Therefore, the total closed circuit voltage gain  $G_V$  is obtained through BTL connection by the expression below.

$$G_V = G_{V1} + G_{V3} + 6 \text{ (dB)} \quad \dots \quad (2)$$

For example, when  $R_f = 0 \Omega$ ,  $G_V$  is obtained by the expressions (1) and (2) as shown below.

$$G_V \approx 24 + 20 + 6 = 50 \text{ dB}$$

The voltage gain is reduced when  $R_f$  is increased. (Fig.2)

With the voltage gain reduced, since (1) the oscillation stability is reduced, and (2) the pop noise changes when  $V_{CC}$  is set to on, refer to the items 3 and 4.

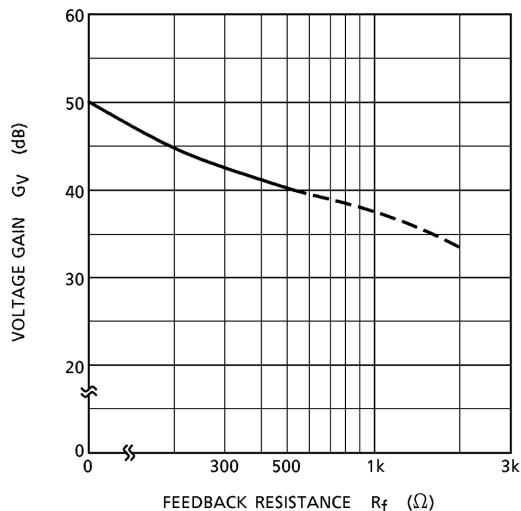


Fig.2

## 2. Stand-by SW function

By means of controlling pin(4) (stand-by terminal) to high and low, the power supply can be set to on and off.

The threshold voltage of pin(4) is set at about 3V (typ.), and the power supply current is about 1μA (typ.) at the stand-by state.

Pin(4) control voltage :  $V_{(SB)}$

Stand-by	Power	$V_{(SB)} (V)$
On	Off	0~2
Off	On	3~ $V_{CC}$

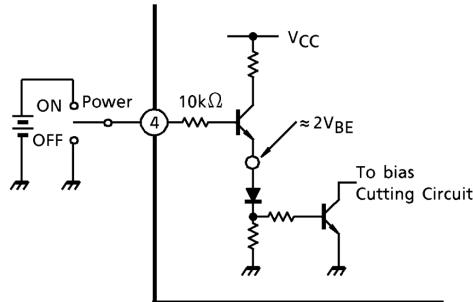
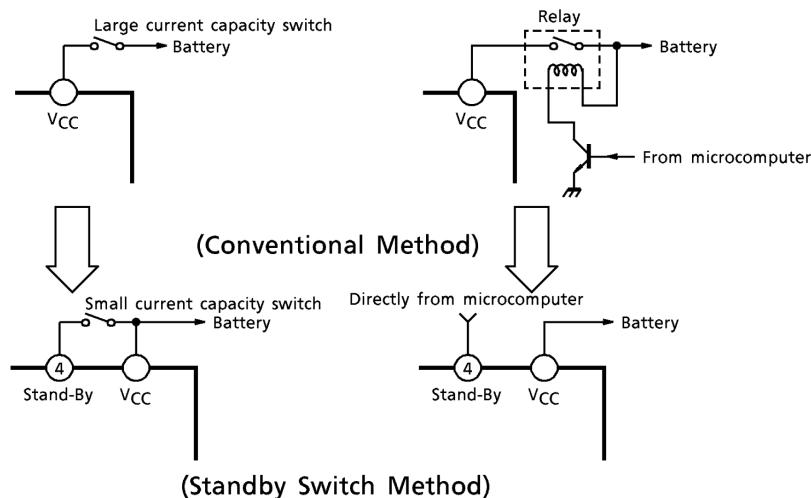


Fig.3 With pin(4) set to high, power is turned on.

Advantage of stand-by SW

- (1) Since  $V_{CC}$  can directly be controlled to on / off by the microcomputer, the switching relay can be omitted.
- (2) Since the control current is microscopic, the switching relay of small current capacity is satisfactory for switching.



### 3. Preventive measure against oscillation

For preventing the oscillation, it is advisable to use C<sub>4</sub>, the condenser of polyester film having small characteristic fluctuation of the temperature and the frequency.

The resistance R to be series applied to C<sub>4</sub> is effective for phase correction of high frequency, and improves the oscillation allowance.

Since the oscillation allowance is varied according to the causes described below, perform the temperature test to check the oscillation allowance.

- (1) Voltage gain to be used (G<sub>V</sub> setting)
- (2) Capacity value of condenser
- (3) Kind of condenser
- (4) Layout of printed board

In case of its use with the voltage gain G<sub>V</sub> reduced or with the feedback amount increased, care must be taken because the phase-inversion is caused by the high frequency resulting in making the oscillation generated.

### 4. Input offset prevention circuit at V<sub>CC</sub>→on

Having pre-amp (amp 1) mounted on the primary stage, this IC contains the circuit for making the amp 1 input voltage and the NF terminal voltage equipotential.

Therefore, the offset voltage produced at the input stage is suppressed to prevent the pop noise at V<sub>CC</sub>→on. The capacity values of the input and NF condense (C<sub>1</sub> and C<sub>2</sub>) shall be set according to the gain to be used.

(reference) (A) At G<sub>V</sub> = 50dB (R<sub>f</sub> = 0Ω)

$$C_1 = 4.7\mu F, C_2 = 47\mu F$$

(B) At G<sub>V</sub> = 40dB (R<sub>f</sub> = 470Ω)

$$C_1 = 3.3\mu F, C_2 = 33\mu F$$

## 5. Muting function

Through setting pin(1) (mute terminal) at about 1V or less, muting becomes possible.

The interval circuit of IC is shown in Fig.4.

When pin(1) is set to low, Q1 and Q2 are turned to on, the charge of the ripple condenser is discharged and the bias is cut. The mute amount of 60dB or over can be obtained.

Since this mute function rapidly discharges the charge of the ripple filter capacitor of pin(8), the pop noise is generated by the DC fluctuation of the bias section.

Therefore, this muting function is not appropriate to the audio muting but it is effective in muting at  $V_{CC} \rightarrow \text{on}$ .

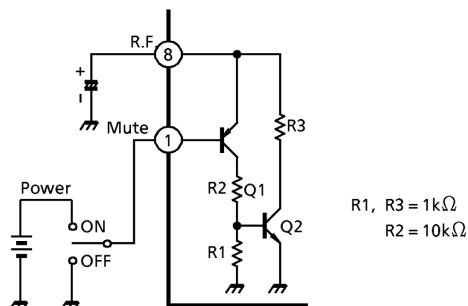


Fig.4 Mute circuit

## 6. External parts list and description

Symbol	Recommended Value	Feature	Influence		Remarks
			Smaller Than Recommended Value	Larger Than Recommended Value	
C <sub>1</sub>	4.7μF	DC blocking	Related to pop noise at V <sub>CC</sub> →on.		Related to gain. Refer to item 4.
C <sub>2</sub>	47μF	Feedback condenser	Related to pop noise at V <sub>CC</sub> →on.		
			determination of low cut-off frequency. $C_2 = \frac{1}{2\pi \cdot f_L \cdot R_f}$		
C <sub>3</sub>	220μF	Ripple reduction	Time constant is small at V <sub>CC</sub> →on or off.	Time constant is large at V <sub>CC</sub> →on or off.	
C <sub>4</sub>	0.12μF	Oscillation prevention	Made liable to oscillate.	Oscillation allowance improved.	Refer to item 3.
C <sub>5</sub>	1000μF	Reipple filter	For filtering power supply hum and ripple. Large at using AC rectified power supply. Small at using DC power supply.		

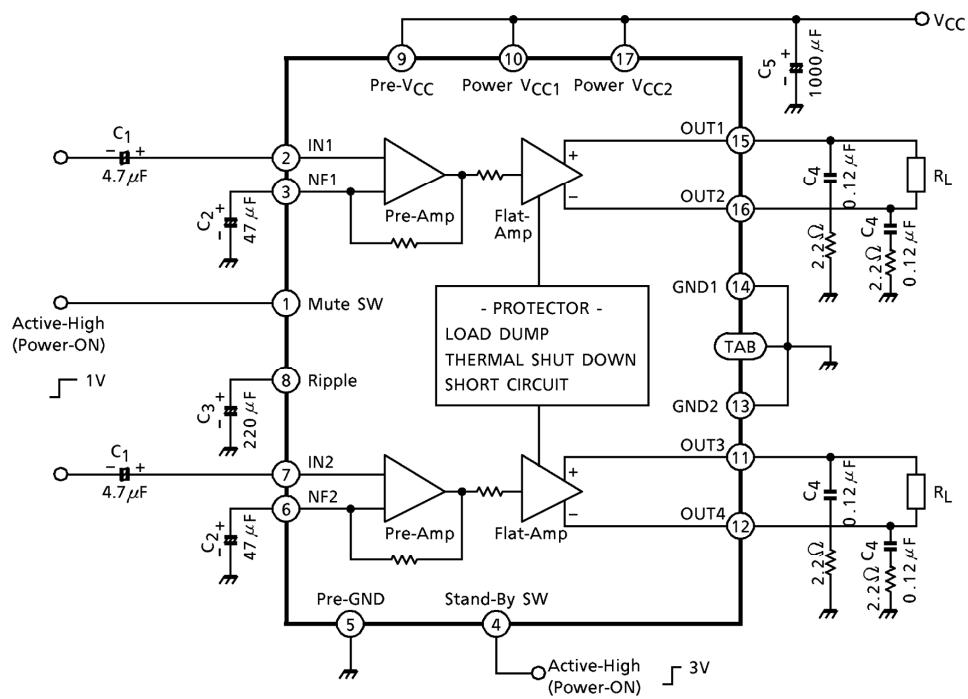
**Absolute Maximum Ratings (Ta = 25°C)**

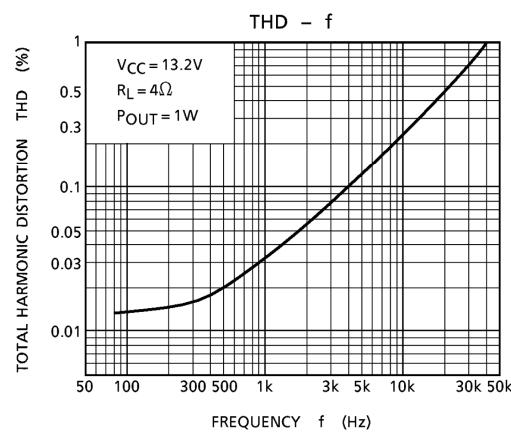
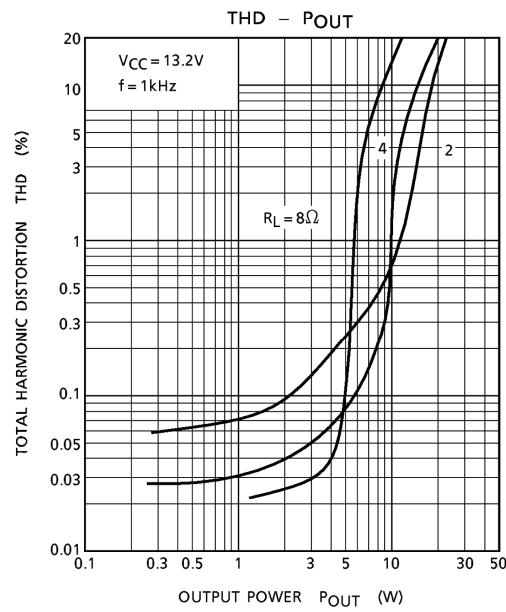
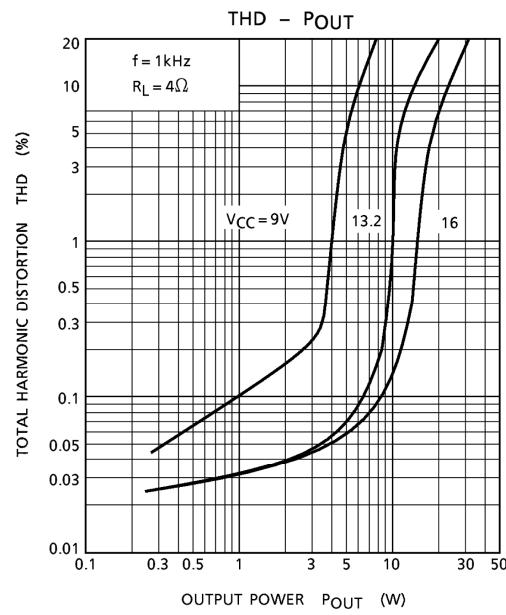
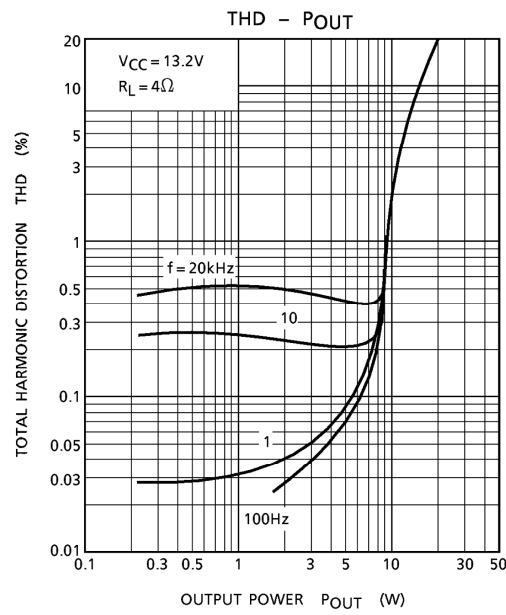
Characteristic	Symbol	Rating	Unit
Peak supply voltage (0.2s)	V <sub>CC</sub> (surge)	50	V
DC supply voltage	V <sub>CC</sub> (DC)	25	V
Operating supply voltage	V <sub>CC</sub> (opr)	18	V
Output current (peak)	I <sub>o</sub> (peak)	9	A
Power dissipation	P <sub>D</sub>	50	W
Operating temperature	T <sub>opr</sub>	-30~85	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C

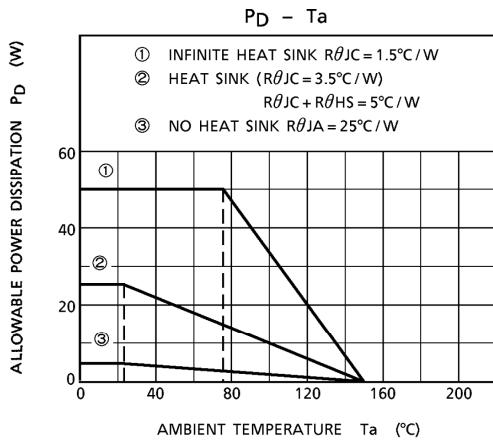
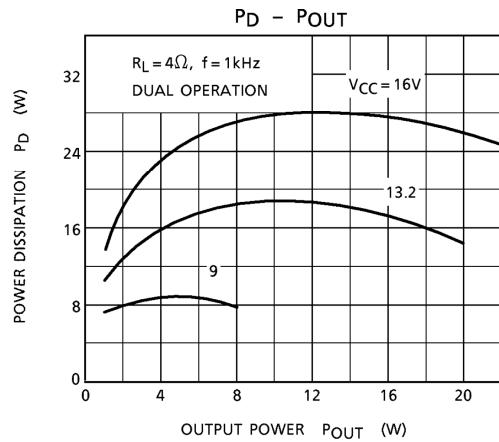
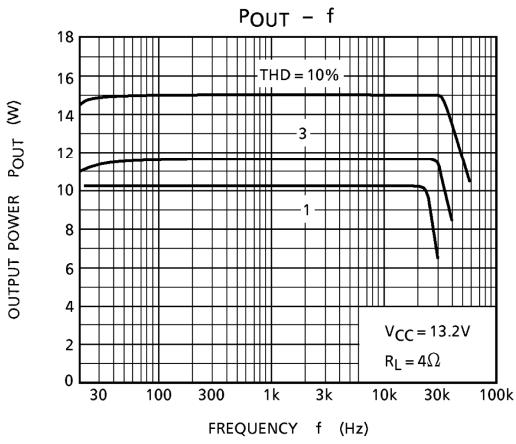
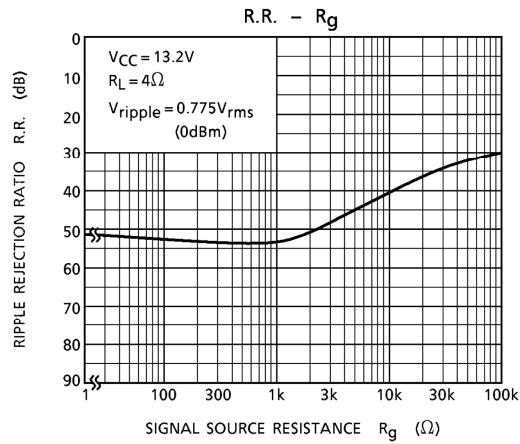
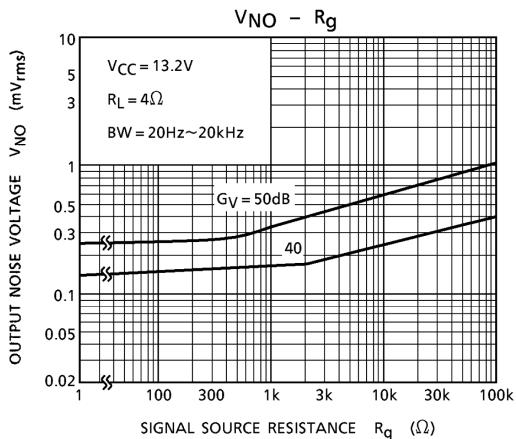
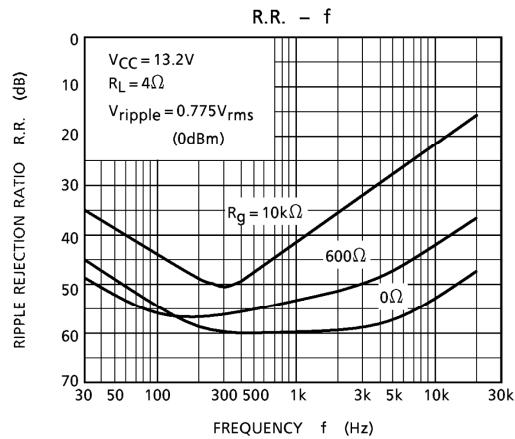
**Electrical Characteristics**(unless otherwise specified, V<sub>CC</sub> = 13.2V, R<sub>L</sub> = 4Ω, f = 1kHz, Ta = 25°C)

Characteristic	Symbol	Test Cur-cuit	Test Condition	Min.	Typ.	Max.	Unit
Quiescent supply current	I <sub>CCQ</sub>	—	V <sub>IN</sub> = 0	—	120	250	mA
Output power	P <sub>OUT</sub> (1)	—	V <sub>CC</sub> = 14.4V, THD = 10%	—	18	—	W
	P <sub>OUT</sub> (2)	—	THD = 10%	11	15	—	
Total harmonic distortion	THD	—	P <sub>OUT</sub> = 1W	—	0.04	0.4	%
Voltage gain	G <sub>V</sub>	—	—	48	50	52	dB
Output noise voltage	V <sub>NO</sub>	—	R <sub>g</sub> = 0Ω, BW = 20Hz~20kHz	—	0.30	0.70	mV <sub>rms</sub>
Ripple fejection ratio	R.R.	—	fripple = 100Hz, R <sub>g</sub> = 600Ω	40	54	—	dB
Input resistance	R <sub>IN</sub>	—	—	—	30	—	kΩ
Output offset voltage	V <sub>offset</sub>	—	V <sub>IN</sub> = 0	-0.3	0	0.3	V
Current at stand-by state	I <sub>SB</sub>	—	—	—	1	10	μA
Cross talk	C.T.	—	R <sub>g</sub> = 600Ω, V <sub>OUT</sub> = 0.775V <sub>rms</sub> (0dBm)	—	60	—	dB
Pn(4) control voltage	V <sub>SB</sub>	—	Stand-by → off (power→on)	2.5	—	V <sub>CC</sub>	V
Pin(1) control voltage	V <sub>(mute)</sub>	—	Mute→on (power→off)	—	1.0	2.0	V

## Test circuit

TA8205AHQ, TA8205ALQ ( $G_v = 50\text{dB}$ )

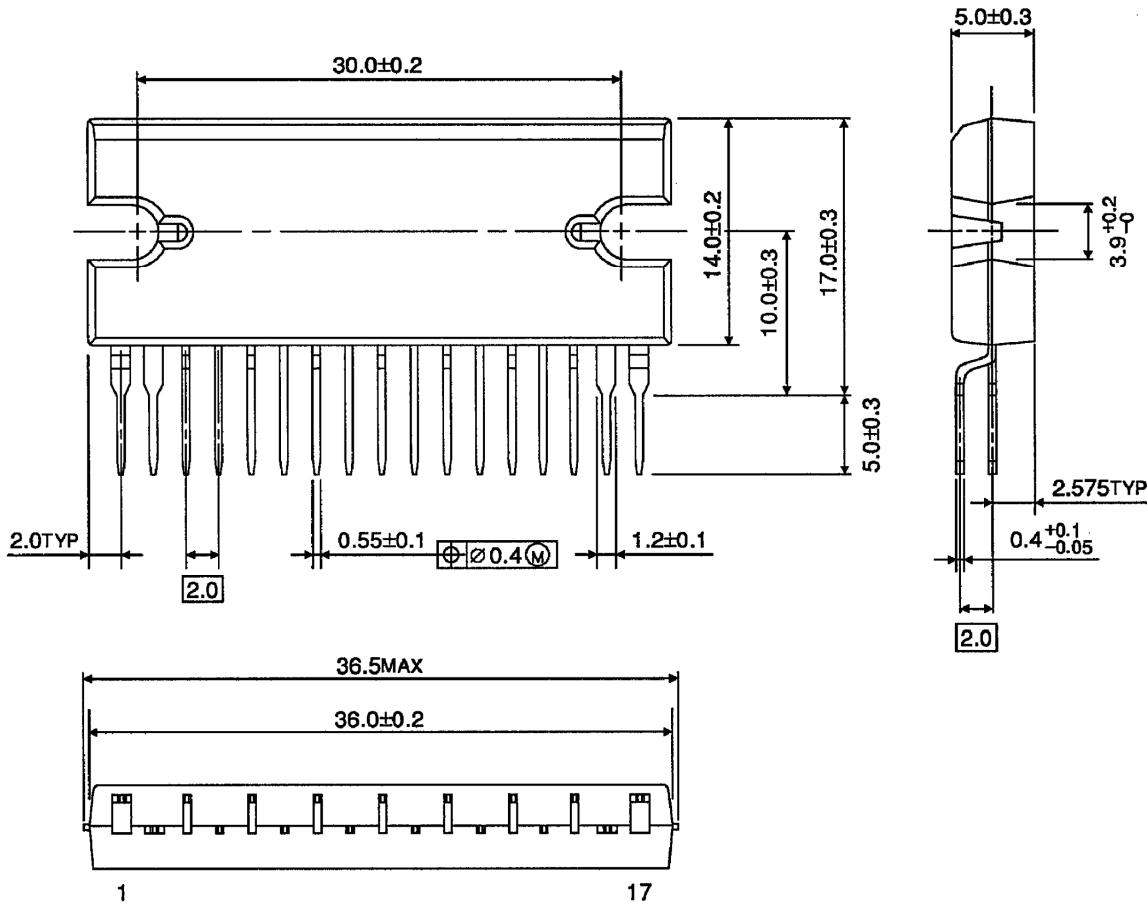




**Package Dimensions**

HZIP17-P-2.00

Unit : mm

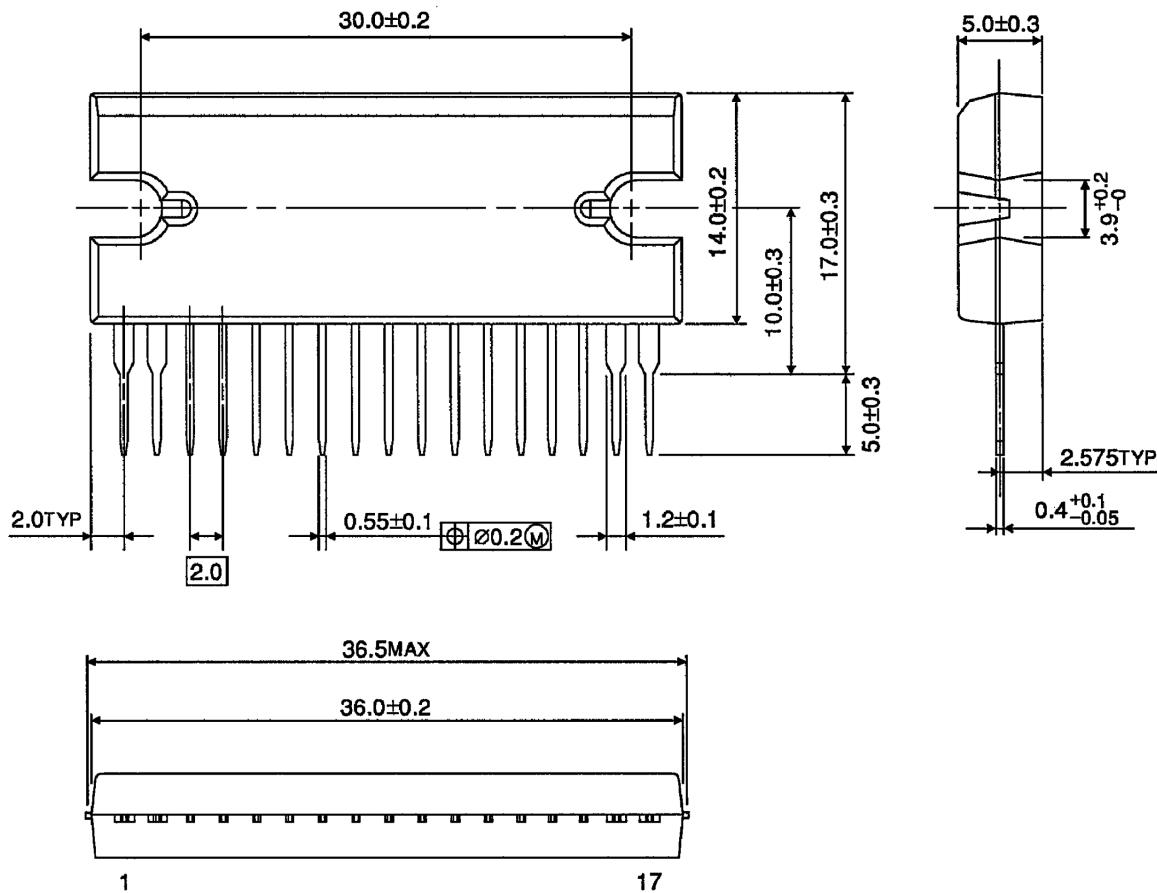


Weight : 9.8g (typ.)

**Package Dimensions**

HSIP17-P-2.00

Unit : mm



Weight : 9.8g (typ.)



- Use an appropriate power supply fuse to ensure that a large current does not continuously flow in case of over current and/or IC failure. The IC will fully break down when used under conditions that exceed its absolute maximum ratings, when the wiring is routed improperly or when an abnormal pulse noise occurs from the wiring or load, causing a large current to continuously flow and the breakdown can lead smoke or ignition. To minimize the effects of the flow of a large current in case of breakdown, appropriate settings, such as fuse capacity, fusing time and insertion circuit location, are required.
- If your design includes an inductive load such as a motor coil, incorporate a protection circuit into the design to prevent device malfunction or breakdown caused by the current resulting from the inrush current at power ON or the negative current resulting from the back electromotive force at power OFF. For details on how to connect a protection circuit such as a current limiting resistor or back electromotive force adsorption diode, refer to individual IC datasheets or the IC databook. IC breakdown may cause injury, smoke or ignition.
- Use a stable power supply with ICs with built-in protection functions. If the power supply is unstable, the protection function may not operate, causing IC breakdown. IC breakdown may cause injury, smoke or ignition.
- Carefully select external components (such as inputs and negative feedback capacitors) and load components (such as speakers), for example, power amp and regulator. If there is a large amount of leakage current such as input or negative feedback condenser, the IC output DC voltage will increase. If this output voltage is connected to a speaker with low input withstand voltage, overcurrent or IC failure can cause smoke or ignition. (The over current can cause smoke or ignition from the IC itself.) In particular, please pay attention when using a Bridge Tied Load (BTL) connection type IC that inputs output DC voltage to a speaker directly.
- Over current Protection Circuit  
Over current protection circuits (referred to as current limiter circuits) do not necessarily protect ICs under all circumstances. If the Over current protection circuits operate against the over current, clear the over current status immediately. Depending on the method of use and usage conditions, such as exceeding absolute maximum ratings can cause the over current protection circuit to not operate properly or IC breakdown before operation. In addition, depending on the method of use and usage conditions, if over current continues to flow for a long time after operation, the IC may generate heat resulting in breakdown.
- Thermal Shutdown Circuit  
Thermal shutdown circuits do not necessarily protect ICs under all circumstances. If the Thermal shutdown circuits operate against the over temperature, clear the heat generation status immediately. Depending on the method of use and usage conditions, such as exceeding absolute maximum ratings can cause the thermal shutdown circuit to not operate properly or IC breakdown before operation.
- Heat Radiation Design  
When using an IC with large current flow such as power amp, regulator or driver, please design the device so that heat is appropriately radiated, not to exceed the specified junction temperature ( $T_j$ ) at any time and condition. These ICs generate heat even during normal use. An inadequate IC heat radiation design can lead to decrease in IC life, deterioration of IC characteristics or IC breakdown. In addition, please design the device taking into considerate the effect of IC heat radiation with peripheral components.
- Installation to Heat Sink  
Please install the power IC to the heat sink not to apply excessive mechanical stress to the IC. Excessive mechanical stress can lead to package cracks, resulting in a reduction in reliability or breakdown of internal IC chip. In addition, depending on the IC, the use of silicon rubber may be prohibited. Check whether the use of silicon rubber is prohibited for the IC you intend to use, or not. For details of power IC heat radiation design and heat sink installation, refer to individual technical datasheets or IC databooks.

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About solderability, following conditions were confirmed

- Solderability
  - (1) Use of Sn-37Pb solder Bath
    - solder bath temperature = 230°C
    - dipping time = 5 seconds
    - the number of times = once
    - use of R-type flux
  - (2) Use of Sn-3.0Ag-0.5Cu solder Bath
    - solder bath temperature = 245°C
    - dipping time = 5 seconds
    - the number of times = once
    - use of R-type flux