ALPHANUMERIC INDEX — CROSS-REFERENCE (Continued)

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MJ13091		MJ16010	3-758
MJ13100		MJ16016	3-774
MJ13101		MJ16016	3-774
MJ13330	MJ13330		3-694
MJ13331	MJ13331		3-694
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MJ14000	MJ14000		3-706
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MJ16004	MJ16004		3-726
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MJ16014	MJ16014		3-774
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MJ2840		2N5877	3-120
MJ2841		2N5878	3-120
MJ2901		MJ15016	3-9
MJ2940		2N5875	3-120
MJ2955	MJ2955		3-6
MJ2955A	MJ2955A		3-9
MJ3000	MJ3000		3-530
MJ3001	MJ3001		3-530
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MJ3247	MJE15030		3-972
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MJ4030	2N6285		3-172
MJ4031	2N6286		3-172
MJ4032	2N6287		3-172
MJ4033	2N6282		3-172
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MJ411	MJ411		3-522
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MJ4237		MJ15012	3-716
MJ4238		MJ15012	3-716
MJ424		2N6308	3-181
MJ4247		MJ15011	3-716
MJ4248		MJ15011	3-716
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MJ4380		MJE13004	3-944
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MJ481		2N3055	3-6
MJ490		2N3789	3-56
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MJ704		MJ12002	3-637
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*Consult Motorola if a direct replacement is necessary.

TABLE 1 — METAL TO-204, TO-204AE (continued)

I_CCont Amps Max	$V_{CEO(sus)}$ Volts Min	Device Type NPN	Device Type PNP	h_{FE} Min/Max	@ I_C Amp	Resistive Switching t_s μs Max	Resistive Switching t_f μs Max	Resistive Switching @ I_C Amp	f_T MHz Min	P_D (Case) Watts @ 25°C
15		MJ16012		7 min	15	0.9 typ	0.15 typ	10		175
		2N6836		10/30	10	3	0.35	10	10	175
	500	MJ16010A		5 min	15	3	0.4	10		175
	850*	MJ12022		5 min	15		0.1 typ	10		175
16	100		BD318	25 min	5				1	200
		2N5629	2N6029	25/100	8	1.2 typ	1.2 typ	8	1	200
	120	2N5630	2N6030	20/80	8	1.2 typ	1.2 typ	8	1	200
	140	2N3773	2N6609	15/60	8	1.1 typ	1.5 typ	8	4	150
		2N5631	2N6031	15/60	8	1.2 typ	1.2 typ	8	1	200
	200	MJ15022	MJ15023	15/60	8				5	250
	250	MJ15024	MJ15025	15/60	8				5	250
18	160	BUX41N		8 min	12	1.2	0.25	12	8	120
20	60	2N3772		15/60	10				2	150
		2N6282##	2N6285##	750/18k	10	2.5 typ	2.5 typ	10	4#	160
	75	2N5039		20/100	10	1.5	0.5	10	60	140
	80	2N5303	2N5745	15/60	10	2	1	10	2	200
		2N6283##	2N6286##	750/18k	10	2.5 typ	2.5 typ	10	4#	160
	90	2N5038		20/100	12	1.5	0.5	12	60	140
	100	2N6284##	2N6287##	750/18k	10	2.5 typ	2.5 typ	10	4#	160
	125	BUX40		8 min	15	1	0.25	15	8	120
	140	MJ15003	MJ15004	25/150	5				2	250
	160	BUV11N		10 min	15	1.2	0.25	15	8	150
	200	BUV11		10 min	12	1.8	0.4	12	8	150
		MJ13330		8/40	10	3.5	0.7	10	5 to 40	175
	250	BUV12		10 min	10	1.5	0.5	10	8	150
		MJ13331		8/40	10	3.5	0.7	10	5 to 40	175
	350	MJ10000##		40/400	10	3	1.8	10	10#	175
		MJ10004##		40/400	10	1.5	0.5	10	10#	175
	400	MJ10001##		40/400	10	3	1.8	10	10#	175
		MJ10005##		40/400	10	1.5	0.5	10	10#	175
		MJ13333		10/60	5	4	0.7	10		175
	450	MJ10008##		30/300	10	2	0.6	10	8#	175
		MJ16014		5 min	20	2.7	0.35	20		250
		MJ16016		7 min	20	2.2	0.25	20		250
		2N6837		10/30	15	2.5	0.25	15		250
	500	MJ10009##		30/300	10	2	0.6	10	8#	175
		MJ13335		10/60	5	4	0.7	10		175
	700	BUT15##		15 min	12	2.5	0.8	12		175
	750	MJ10024##		50/600	20	5	1.8	10		250
	850	MJ10025##		50/600	20	5	1.8	10		250

* $V_{(BR)CEX}$, # $|h_{fe}|$ @ 1 MHz, ## Darlington

(continued)

☐ JAN, JTX, JTXV Available

MOTOROLA
SEMICONDUCTOR
TECHNICAL DATA

NPN
MJ15022, MJ15024

SILICON POWER TRANSISTORS

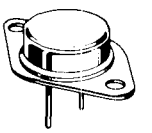
The MJ15022 and MJ15024 are PowerBase power transistors designed for high power audio, disk head positioners and other linear applications.

- High Safe Operating Area (100% Tested) – 2 A @ 80 V
- High DC Current Gain – h_{FE} = 15 (Min) @ I_C = 8 Adc

16 AMPERE

SILICON POWER TRANSISTORS

200 and 250 VOLTS
250 WATTS



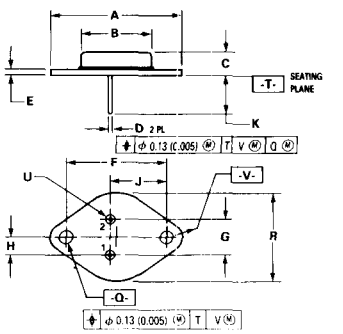
MAXIMUM RATINGS

Rating	Symbol	MJ15022	MJ15024	Unit
Collector-Emitter Voltage	V_{CEO}	200	250	Vdc
Collector-Base Voltage	V_{CBO}	350	400	Vdc
Emitter-Base Voltage	V_{EBO}	5		Vdc
Collector-Emitter Voltage	V_{CEX}	400		Vdc
Collector Current – Continuous Peak (1)	I_C	16 30		Adc
Base Current – Continuous	I_B	5		Adc
Total Power Dissipation @ T_C = 25°C Derate above 25°C	P_D	250 1.43		Watts W/°C
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-65 to +200		°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	0.70	°C/W

(1) Pulse Test: Pulse Width = 5 ms, Duty Cycle ≤ 10%.



NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENS ON: INCH.
3. ALL RULES AND NOTES ASSOCIATED WITH REFERENCED TO-204AA OUTLINE SHALL APPLY.

DIM	MILLIMETERS MIN	MILLIMETERS MAX	INCHES MIN	INCHES MAX
A	—	39.37	—	1.550
B	—	21.08	—	0.830
C	6.35	8.25	0.250	0.325
D	0.97	1.09	0.038	0.043
E	1.40	1.77	0.055	0.070
F	30.15 BSC		1.187 BSC	
G	10.92 BSC		0.430 BSC	
H	5.46 BSC		0.215 BSC	
J	16.89 BSC		0.665 BSC	
K	11.18	12.19	0.440	0.480
Q	3.84	4.19	0.151	0.165
R	—	26.67	—	1.050
U	4.83	5.33	0.190	0.210
V	3.84	4.19	0.151	0.165

STYLE 1:
PIN 1. BASE
2. EMITTER
CASE: COLLECTOR

CASE 1-06
TO-204AA
(TO-3)

3

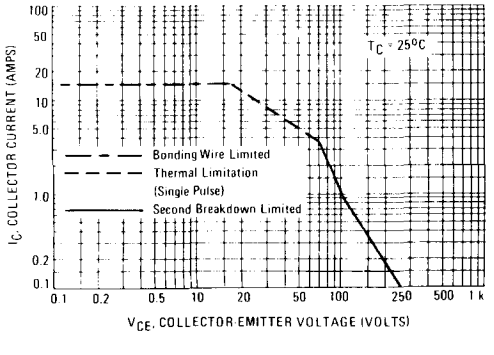
ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector-Emitter Sustaining Voltage (1)		$V_{CEO(sus)}$			
(I_C = 100 mAdc, I_B = 0)	MJ15022		200	–	
	MJ15024		250	–	
Collector Cutoff Current		I_{CEX}			
(V_{CE} = 200 Vdc, $V_{BE(off)}$ = 1.5 Vdc)	MJ15022		–	250	μAdc
(V_{CE} = 250 Vdc, $V_{BE(off)}$ = 1.5 Vdc)	MJ15024		–	250	
Collector Cutoff Current		I_{CEO}			μAdc
(V_{CE} = 150 Vdc, I_B = 0)	MJ15022		–	500	
(V_{CE} = 200 Vdc, I_B = 0)	MJ15024		–	500	
Emitter Cutoff Current		I_{EBO}	–	500	μAdc
(V_{CE} = 5 Vdc, I_B = 0)					
SECOND BREAKDOWN					
Second Breakdown Collector Current with Base Forward Biased		$I_{S/b}$			Adc
(V_{CE} = 50 Vdc, t = 0.5 s (non-repetitive))			5		
(V_{CE} = 80 Vdc, t = 0.5 s (non-repetitive))			2		
ON CHARACTERISTICS					
DC Current Gain		h_{FE}			–
(I_C = 8 Adc, V_{CE} = 4 Vdc)			15	60	
(I_C = 16 Adc, V_{CE} = 4 Vdc)			5	–	
Collector-Emitter Saturation Voltage		$V_{CE(sat)}$			Vdc
(I_C = 8 Adc, I_B = 0.8 Adc)			–	1.4	
(I_C = 16 Adc, I_B = 3.2 Adc)			–	4.0	
Base-Emitter On Voltage		$V_{BE(on)}$	–	2.2	Vdc
(I_C = 8 Adc, V_{CE} = 4 Vdc)					
DYNAMIC CHARACTERISTICS					
Current-Gain – Bandwidth Product		f_T	4	–	MHz
(I_C = 1 Adc, V_{CE} = 10 Vdc, f_{test} = 1 MHz)					
Output Capacitance		C_{ob}	–	500	pF
(V_{CB} = 10 Vdc, I_E = 0, f_{test} = 1 MHz)					

(1) Pulse Test: Pulse Width = 300 μs, Duty Cycle ≤ 2%.

3

FIGURE 1 – ACTIVE-REGION SAFE OPERATING AREA



There are two limitations on the powerhandling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate I_C – V_{CE} limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 1 is based on $T_{J(pk)}$ = 200°C; T_C is variable depending on conditions. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

TYPICAL CHARACTERISTICS

FIGURE 2 – CAPACITANCES

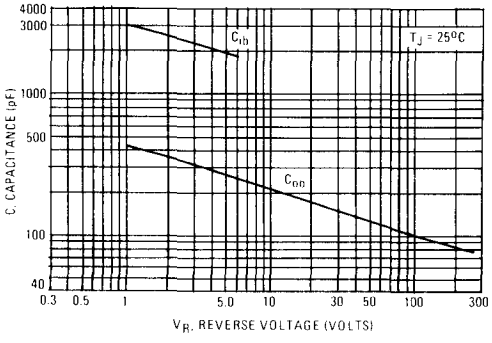


FIGURE 3 – CURRENT-GAIN–BANDWIDTH PRODUCT

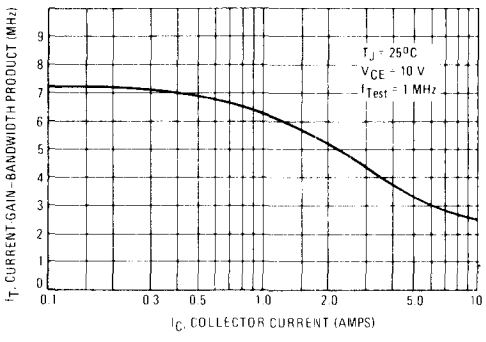


FIGURE 4 – DC CURRENT GAIN

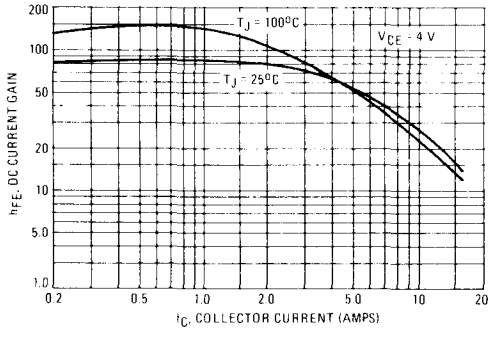


FIGURE 5 – "ON" VOLTAGE

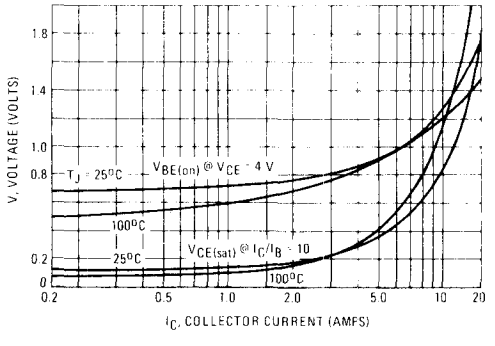
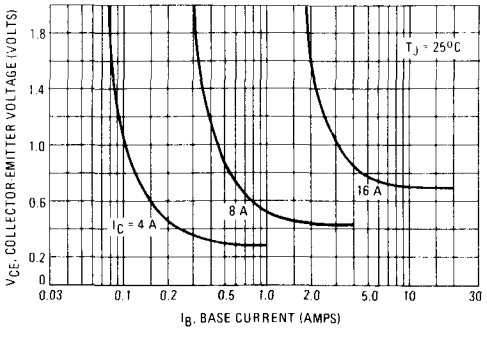


FIGURE 6 – COLLECTOR SATURATION REGION



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