

SF1081A-1

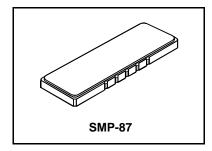
- Designed for GSM BTS Receiver IF Applications
- Simple External Impedance Matching
- Hermetic SMP-87 Surface-mount Case
- Unbalanced Input and Output
- Extended Temperature Range Version of SF1081A
- Complies with Directive 2002/95/EC (RoHS)



### **Absolute Maximum Ratings**

Rating	Value	Units	
Maximum Incident Power in Passband	+10	dBm	
Maximum DC Voltage on any Non-ground Terminal	30	VDC	
Storage Temperature Range	-40 to +85	°C	
Suitable for Lead-free Soldering - Maximum Soldering Profile	260 °C for 30 s		

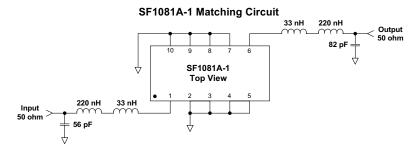
# 71.00 MHz **SAW Filter**



#### **Electrical Characteristics**

Characteristic			Notes	Min	Тур	Max	Units
Nominal Center F	requency	f <sub>C</sub>	4		71.000		MHz
Passband	Insertion Loss at fc	IL	1		6	8.0	dB
	3 dB Passband	BW <sub>3</sub>		±100	±140	±200	kHz
	Amplitude Ripple over fc ±80 kHz		1.0			1.5	dB <sub>P-P</sub>
Group Delay Variation over fc ±50 kHz		GDV	1, 2		300	1000	ns <sub>P-P</sub>
Absolute Group Delay		GD			2.8		μs
Rejection	fc-600 to fc-400 and fc+400 to fc+600 kHz			25	26		
	fc-1.0 to fc-0.6 and fc+0.6 to fc+1.8 MHz 69.6 to 70.0 MHz 1, 2, 3	100	35	40		dB	
		1, 2, 3	40	45		uБ	
31 to 69.6 and 71.8 to 111 MHz				35	50		
Operating Temper	rature Range	T <sub>A</sub>	1	-40		+85	°C

Impedance Matching to 50 $\Omega$ unbalanced	External L-C
Case Style	SMP-87 22.1 X 8 mm Nominal Footprint
Lid Symbolization (YY=year, WW=week)	RFM SF1081A-1 YYWW



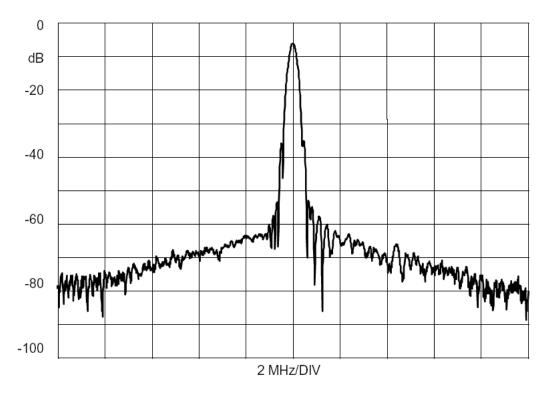


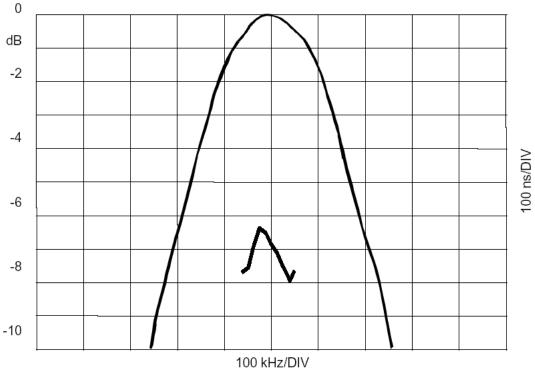
## **CAUTION: Electrostatic Sensitive Device. Observe precautions for handling.**

### Notes:

- Unless noted otherwise, all specifications apply over the operating temperature range with filter soldered to a demonstration board with impedance matching to 50  $\Omega$  and measured with 50  $\Omega$  network analyzer.
- Unless noted otherwise, all frequency specifications are referenced to the nominal center frequency, fc. 2.
- Rejection is measured as attenuation below the minimum IL point in the passband. Rejection in final user application is dependent on PCB layout and external impedance matching design. See Application Note No. 42
- for details.
  "LRIP" or "L" after the part number indicates "low rate initial production"
- and "ENG" or "E" indicates "engineering prototypes." The design, manufacturing process, and specifications of this filter are subject to change.

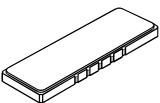
  Either Port 1 or Port 2 may be used for either input or output in the design.
- However, impedances and impedance matching may vary between Port 1 and Port 2, so that the filter must always be installed in one direction per the circuit design.
- 7.
- US and international patents may apply.
  RFM, stylized RFM logo, and RF Monolithics, Inc. are registered trademarks of RF Monolithics, Inc.





# **SMP-87 Case**

# **10-Terminal Ceramic Surface-Mount Case** 22.1 x 8 mm Nominal Footprint



Materials			
Solder Pad	1.015 µm Gold minimum		
Plating	over 2.030 µm Nickel		
Lid Plating	2.0 to 3.0 µm Nickel		
Body	Al <sub>2</sub> O <sub>3</sub> Ceramic		
Pb Free			

Case Dimensions						
Dimension	mm			Inches		
	Min	Nom	Max	Min	Nom	Max
Α	21.90	22.10	22.40	0.862	0.870	0.882
В	7.80	8.00	8.30	0.307	0.315	0.327
С		1.78	2.00		0.070	0.079
D		2.29			0.090	
E		1.02			0.040	
Н		1.0			0.039	
М		4.83			0.190	
N		2.41			0.095	
Р		1.905			0.075	

Electrical Connections				
	Connection	Terminals		
Port 1	Input or Return	10		
	Return or Input	1		
Port 2	Output or Return	5		
	Return or Output	6		
	Ground	All others		
Single-ended Operation		Return is ground		
Differential Operation		Return is hot		

