



General Description

The MAX1501 intelligent, constant-current, constant-voltage (CCCV), temperature-regulated battery charger charges a single lithium-ion (Li+) cell or three-cell NiMH/NiCd batteries. The device integrates the current-sense resistor, PMOS pass element, and thermal-regulation circuitry, while eliminating the reverse-blocking Schottky diode to create the simplest charging solution for hand-held equipment.

For single-cell Li+ batteries, the MAX1501 functions as a stand-alone charger to control the charging sequence from the prequalification state through fast charge, top-off, and charge termination. With 3-cell NiMH/NiCd batteries, the MAX1501 requires collaboration with a microcontroller to determine the best charging algorithm. Proprietary thermal-regulation circuitry limits the die temperature when fast charging or while exposed to high ambient temperatures, allowing maximum charging current without damaging the charger. The MAX1501 continually supplies a regulated output voltage under no-battery conditions, allowing battery changing without interrupting system power.

The device achieves high flexibility by providing an adjustable fast-charge current, top-off current, safety timer (disabled in the MAX1501Z), and thermal-regulation setpoint. Other features include input power detection (ACOK) and input under-/overvoltage protection. The MAX1501 provides active-low control inputs.

The MAX1501 accepts a 4.5V to 13V supply, but disables charging when the input voltage exceeds 6.5V, preventing excessive power dissipation. The MAX1501 operates over the extended temperature range (-40°C to +85°C) and is available in a compact 16-pin thermally enhanced 5mm x 5mm thin QFN package with 0.8mm profile.

Applications

Cellular and Cordless Phones

PDAs

Digital Cameras and MP3 Players

USB Appliances

Charging Cradles and Docks

Bluetooth™ Equipment

Pin Configuration appears at end of data sheet.

Bluetooth is a trademark of Ericsson.

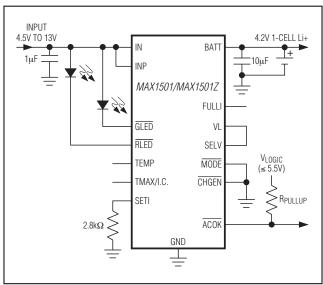
Features

- ♦ Stand-Alone 1-Cell Li+ Charging, Microprocessor-Controlled 3-Cell NiMH/NiCd Charging
- No FET, Reverse-Blocking Diode, or Current-Sense Resistor Required
- ♦ 1.4A (max) Programmable Fast-Charge Current
- ♦ +95°C, +115°C, and +135°C Proprietary Programmable Die Temperature Regulation Control
- ♦ 4.5V to 13V Input Voltage Range with Input Overvoltage (OVLO) Protection Above 6.5V
- Programmable Top-Off Current Threshold: 10%, 20%, or 30% of the Fast-Charge Current
- **♦** Charge-Current Monitor for Fuel Gauging
- **♦** Programmable Safety Timer (3, 4.5, or 6 hours)
- ♦ Input Power <u>Detection</u> Output (ACOK) and Charge Enable Input (CHGEN)
- **♦ Automatic Recharge**
- ♦ Digital Soft-Start Limits Inrush Current
- ♦ Charge Status Outputs for LEDs or µP Interface

Ordering Information

PART	TEMP RANGE	PIN-PACKAGE
MAX1501ETE	-40°C to +85°C	16 Thin QFN
MAX1501ZETE	-40°C to +85°C	16 Thin QFN

Typical Operating Circuit



Highly Integrated, Linear Battery Charger with Thermal Regulation for Portable Applications

ABSOLUTE MAXIMUM RATINGS

IN, INP, RLED, GLED to GND0.3V to +14V	Continuous Power Dissipation ($T_A = +70^{\circ}C$)
IN to INP0.3V to +0.3V	16-Pin 5mm × 5mm Thin QFN
VL, BATT, SETI, ACOK, MODE, CHGEN, SELV,	(derate 21.3mW/°C above +70°C)1702mW
FULLI, TMAX/I.C., TEMP to GND0.3V to +6V	Operating Temperature Range40°C to +85°C
VL to IN14V to +0.3V	Junction Temperature+150°C
	Storage Temperature Range65°C to +150°C
	Lead Temperature (soldering, 10s)+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{IN} = V_{INP} = 5V, V_{BATT} = 3.5V, \overline{ACOK} = \overline{GLED} = \overline{RLED} = TEMP = TMAX/I.C. = FULLI = open, \overline{CHGEN} = \overline{MODE} = GND, R_{SETI} = 2.8k\Omega$, $C_{IN} = 1\mu F$, $C_{BATT} = 10\mu F$, $T_A = 0^{\circ}C$ to +85°C, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$.)

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
IN, INP Input Voltage			0		13	V
IN, INP Input Operating Range					6.25	V
VL Output Voltage	4.5V ≤ V _{IN} ≤ 6.25V, I _{VL} < 250µ	$4.5V \le V_{IN} \le 6.25V$, $I_{VL} < 250\mu A$		3	3.3	V
	V _{IN} - V _{BATT} , rising		40	70	100	
ACOK Trip Point	V _{IN} - V _{BATT} , falling		30	55	85	mV
	V _{IN} - V _{BATT} , hysteresis			15		
ACOK Sink Current	$4.5V \le V_{IN} \le 6.25V$, $V_{\overline{ACOK}} = 0.000$	0.6V	75			μΑ
	V _{IN} rising		4.05	4.125	4.20	V
Undervoltage Lockout Trip Point	V _{IN} falling		3.9	4.025	4.1	V
	Hysteresis			100		mV
Overvoltage Lockout Trip Point	V _{IN} rising		6.25	6.50	6.75	V
	Li+, NiMH/NiCd, and no-battery modes			5	8	
IN Input Current	Disable mode			1.5	3	mA
	Off mode (V _{IN} = 4V)				0.25	
	V _{BATT} = 4.3V			45	80	
BATT Input Current	$V_{IN} = 0$			3	10	μΑ
	Disable mode			2	6	
Leakage into Battery	V _{IN} = V _{INP} = 13V, V _{BATT} = 0	Disable mode			5	μΑ
RMS Charge Current					1.4	Α
	Li. manda	SELV = VL	4.166	4.2	4.234	
	Li+ mode	SELV = GND	4.067	4.1	4.133	
Battery Regulation Voltage	NiMH/NiCd mode	SELV = VL, V _{IN} = V _{INP} = 6V	4.85	4.95	5.05	V
		SELV = GND	4.4	4.5	4.6	1
Output Regulation Voltage	No-battery mode		3.700	4.0	4.234	V
BATT Precharge Threshold Voltage	BATT rising		2.675	2.8	2.925	V
Fast-Charge Current-Loop	$R_{SETI} = 2.8k\Omega$		460	500	540	m ^
System Accuracy	$R_{SETI} = 1.75 \text{ k}\Omega$		736	800	864	mA

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{IN} = V_{INP} = 5V, V_{BATT} = 3.5V, \overline{ACOK} = \overline{GLED} = \overline{RLED} = TEMP = TMAX/I.C. = FULLI = open, \overline{CHGEN} = \overline{MODE} = GND, R_{SETI} = 2.8k\Omega, C_{IN} = 1\mu F, C_{BATT} = 10\mu F, T_{A} = 0^{\circ}C$ to +85°C, unless otherwise noted. Typical values are at $T_{A} = +25^{\circ}C$.)

PARAMETER		CONDITIONS	MIN	TYP	MAX	UNITS
Precharge Current System Accuracy	% of fast-charge	% of fast-charge current, V _{BATT} = 2V		10	15	%
	TEMP = GND	TEMP = GND		95		
Die Temperature Regulation Setpoint (Note 1)	TEMP = open			115		°C
Setpoint (Note 1)	TEMP = VL			135		Ī
Current Conso Amplifier Coin	IBATT to ISETI, pre	charge mode, V _{BATT} = 2V	0.70	1	1.30	mA/A
Current-Sense Amplifier Gain	IBATT to ISETI, fast	t-charge mode	0.95	1	1.05	MA/A
Internal Current-Sense Resistance				84		mΩ
Regulator Dropout Voltage	V _{IN} - V _{BATT} , NiMH I _{BATT} = 425mA	H/NiCd mode, V _{BATT} = 4.3V,		190	350	mV
Logic Input Low Voltage	CHGEN, MODE, 4	4.5V ≤ V _{IN} ≤ 6.25V			0.52	V
Logic Input High Voltage	CHGEN, MODE, 4	CHGEN, MODE, 4.5V ≤ V _{IN} ≤ 6.25V				V
Internal Pulldown Resistance	CHGEN, MODE	CHGEN, MODE		175	400	kΩ
Internal Pullup Resistance	SELV	SELV		175	400	kΩ
Internal Bias Resistance	FULLI, TEMP, TM.	FULLI, TEMP, TMAX		90	200	kΩ
Internal Bias Voltage	FULLI, TEMP, TM	AX		V _{VL} /2		V
RLED Output Low Current	$V_{\overline{RLED}} = 1V$		7	10	18	mA
GLED Output Low Current	VGLED = 1V		14	20	34	mA
GLED, RLED Output High Leakage Current	VGLED = VRLED =	$=$ $V_{IN} = V_{INP} = 13V$		0.1	1	μА
		FULLI = GND	5	10	15	
Full-Battery Detection Current Threshold	% of fast-charge current	FULLI = VL	15	20	25	%
Titleshold	Current	FULLI = open	25	30	35	
	Li. manda	SELV = VL	3.9	4.0	4.1	
V _{BATT} Restart Threshold	Li+ mode	SELV = GND	3.8	3.9	4.0	V
	NiMH/NiCd mode	,	3.9	4.0	4.1]
Charge-Timer Accuracy			-10		+10	%
O. T. D. I	TMAX = GND			3		
Charge-Timer Duration (MAX1501 Only)	TMAX = open	TMAX = open		4.5		hrs
(W. OCTOOT OTHY)	TMAX = VL	TMAX = VL		6		

Highly Integrated, Linear Battery Charger with Thermal Regulation for Portable Applications

ELECTRICAL CHARACTERISTICS

 $(V_{IN} = V_{INP} = 5V, V_{BATT} = 3.5V, \overline{ACOK} = \overline{GLED} = \overline{RLED} = TEMP = TMAX/I.C. = FULLI = open, \overline{CHGEN} = \overline{MODE} = GND, R_{SETI} = 2.8k\Omega, C_{IN} = 1\mu F, C_{BATT} = 10\mu F, T_{A} = -40^{\circ}C$ to +85°C, unless otherwise noted. Typical values are at $T_{A} = +25^{\circ}C$.) (Note 2)

PARAMETER	CONDIT	IONS	MIN	TYP	MAX	UNITS
IN, INP Input Voltage			0		13	V
IN, INP Input Operating Range			4.50		6.25	V
VL Output Voltage	4.5V ≤ V _{IN} ≤ 6.25V, I _{VL} < 250µ	A	2.7		3.3	V
AOOK Trice Delies	V _{IN} - V _{BATT} , rising		40		100	\/
ACOK Trip Point	V _{IN} - V _{BATT} , falling		30		85	mV
ACOK Sink Current	$4.5V \le V_{IN} \le 6.25V$, $V_{\overline{ACOK}} = 0$.6V	75			μΑ
	V _{IN} rising		4.00		4.25	.,,
Undervoltage Lockout Trip Point	I _{IN} falling		3.90		4.15	V
Overvoltage Lockout Trip Point			6.25		6.75	V
IN Invest Comment	Li+, NiMH/NiCd, and no-batter	y modes			8	
IN Input Current	Disable mode				3	mA
	VBATT = 4.3V				80	
BATT Input Current	V _{IN} = 0 Disable mode				10	μΑ
					6	
Leakage into Battery	V _{IN} = V _{INP} = 13V, V _{BATT} = 0	Disable mode			5	μΑ
RMS Charge Current		•			1.4	А
Battery Regulation Voltage	1.	SELV = VL	4.148		4.252	
	Li+ mode	SELV = GND	4.05		4.15	.,
	NEW HAREOUT	SELV = VL	4.85		5.05	- V
	NiMH/NiCd mode	SELV = GND	4.4		4.6	
Output Regulation Voltage	No-battery mode	•	3.700		4.234	V
BATT Precharge Threshold Voltage	BATT rising		2.675		2.925	V
Fast-Charge Current-Loop	$R_{SETI} = 2.8k\Omega$		460		540	
System Accuracy	$R_{SETI} = 1.75k\Omega$		736		864	mA
Precharge Current System Accuracy	% of fast-charge current, V _{BAT}	T = 2V	5		15	%
	IBATT to ISETI, precharge mode	IBATT to ISETI, precharge mode, VBATT = 2V			1.40	
Current-Sense Amplifier Gain	IBATT to ISETI, fast-charge mod		0.93		1.07	mA/A
Regulator Dropout Voltage	V _{IN} - V _{BATT} , NiMH/NiCd mode, V _{BATT} = 4.3V, I _{BATT} = 425mA				350	mV
Logic Input Low Voltage	<u>CHGEN, MODE, 4.5V < VIN < 6.25V</u>				0.52	V
Logic Input High Voltage	CHGEN, MODE, 4.5V < VIN <		1.3			V
Internal Pulldown Resistance	CHGEN, MODE		100		400	kΩ
Internal Pullup Resistance	SELV		100		400	kΩ
Internal Bias Resistance	FULLI, TEMP, TMAX		50		200	kΩ

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{IN} = V_{INP} = 5V, V_{BATT} = 3.5V, \overline{ACOK} = \overline{GLED} = \overline{RLED} = TEMP = TMAX/I.C. = FULLI = open, \overline{CHGEN} = \overline{MODE} = GND, R_{SETI} = 2.8k\Omega, C_{IN} = 1\mu F, C_{BATT} = 10\mu F, T_A = -40^{\circ}C$ to +85°C, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$.) (Note 2)

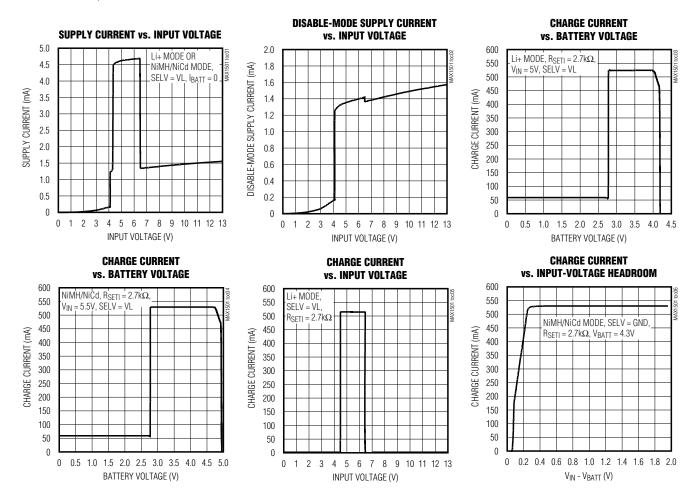
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
RLED Output Low Current	V _{RLED} = 1V	7		18	mA
GLED Output Low Current	$V_{\overline{GLED}} = 1V$	14		34	mA
GLED, RLED Output High Leakage Current	VGLED = VRLED = VIN = VINP = 13V			1	μΑ
Charge-Timer Accuracy (MAX1501 Only)		-10		+10	%

Note 1: Temperature regulation setpoint variation is typically ±9°C.

Note 2: Specifications to T_A = -40°C are guaranteed by design, not production tested.

Typical Operating Characteristics

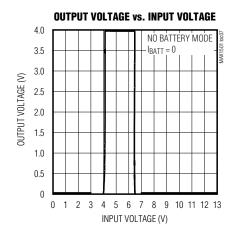
 $(V_{IN} = V_{INP} = 5V, \overline{ACOK} = \overline{RLED} = \overline{GLED} = TEMP = TMAX/I.C. = FULLI = open, C_{BATT} = 10\mu F, C_{IN} = 1\mu F, T_A = +25^{\circ}C, unless otherwise noted.)$



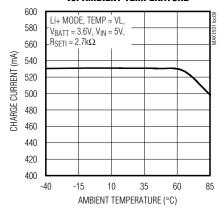
Highly Integrated, Linear Battery Charger with Thermal Regulation for Portable Applications

Typical Operating Characteristics (continued)

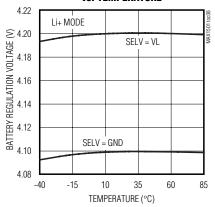
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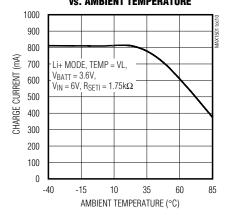
CHARGE CURRENT vs. AMBIENT TEMPERATURE



BATTERY REGULATION VOLTAGE vs. Temperature



CHARGE CURRENT vs. AMBIENT TEMPERATURE



Highly Integrated, Linear Battery Charger with Thermal Regulation for Portable Applications

Pin Description

PIN	NAME	FUNCTION
1	INP	High-Current Charger Input. Connect an AC adapter to INP and IN as close to the device as possible. INP provides charge current to the battery. INP draws current while the device is in shutdown mode.
2	IN	Low-Current Charger Input. Bypass IN to GND with a 1µF ceramic capacitor. Connect IN to INP as close to the device as possible. IN powers the internal LDO and reference. IN draws current while the device is in shutdown mode.
3, 13	GND	Ground. Connect the exposed paddle to GND.
4	SETI	Current-Sense Transconductance Amplifier Output. Connect a resistor from SETI to GND to program the maximum charge current and to monitor the actual charge current. SETI pulls to GND during shutdown.
5	VL	Linear Regulator Output. Connect CHGEN, TEMP, TMAX, FULLI, and MODE to VL to program logic high. VL discharges to GND during shutdown.
6	TMAX	Maximum Charging-Time Select Input (MAX1501 Only). TMAX sets the maximum charging time. Connect TMAX to GND to set the maximum charging time to 3 hours. Leave TMAX floating to set the maximum charging time to 4.5 hours. Connect TMAX to VL to set the maximum charging time to 6 hours. TMAX pulls to GND through a $50k\Omega$ resistor in shutdown.
	I.C.	Internally Connected in the MAX1501Z. Leave floating.
7	FULLI	Top-Off-Current Select Input. FULLI sets the end-of-charge threshold as a percentage of the fast-charge current. Connect FULLI to GND to set the end-of-charge threshold to 10% of the fast-charge current. Connect FULLI to VL to set the end-of-charge threshold to 20% of the fast-charge current. Leave FULLI floating to set the end-of-charge threshold to 30% of the fast-charge current. FULLI pulls to GND through a $50k\Omega$ resistor in shutdown.
8	TEMP	Die Temperature Select Input. TEMP sets the die temperature regulation point for the thermal-control loop. Connect TEMP to GND to regulate the die temperature at $+95^{\circ}$ C. Leave TEMP floating to regulate the die temperature at $+115^{\circ}$ C. Connect TEMP to VL to regulate the die temperature at $+135^{\circ}$ C. TEMP pulls to GND through a $50k\Omega$ resistor in shutdown.
9	MODE	Mode Select Input. MODE and CHGEN together control charging functions (Table 1). An internal 175kΩ pulldown resistor pulls MODE low.
10	CHGEN	Charge Enable Input. CHGEN and MODE together control charging functions (Table 1). An internal 175kΩ pulldown resistor pulls CHGEN low.
11	ACOK	Input Voltage Range Indicator. The open-drain \overline{ACOK} output asserts low when $4.2V \le V_{IN} \le 6.25V$ and $V_{IN} - V_{BATT} \ge 100 \text{mV}$. \overline{ACOK} requires an external $100 \text{k}\Omega$ pullup resistor. \overline{ACOK} floats in shutdown.
12	BATT	Battery Connection. Connect the positive terminal of the battery to BATT. BATT draws less than 5µA during shutdown.
14	SELV	Battery Voltage Selection Input. SELV sets the battery regulation voltage in Li+ and NiMH/NiCd modes (Table 2). For no-battery mode, the battery voltage defaults to 4.0V. An internal $175k\Omega$ resistor to VL pulls SELV high.
15	RLED	Battery Charging Indicator. Connect the anode of a red LED to IN and the cathode to RLED. RLED asserts low when the input supply is present and the battery is charging, regardless of cell chemistry. RLED sinks 10mA. RLED goes high impedance in shutdown. Connect a pullup resistor to the µP's I/O supply when interfacing with a µP logic input.
16	GLED	Full-Charge Indicator. Connect the anode of a green LED to IN and the cathode to $\overline{\text{GLED}}$. $\overline{\text{GLED}}$ asserts low when the input supply is present and the battery has reached the top-off current threshold set by FULLI, regardless of cell chemistry. $\overline{\text{GLED}}$ sinks 20mA. $\overline{\text{GLED}}$ goes high impedance in shutdown. Connect a pullup resistor to the $\mu\text{P's}$ I/O supply when interfacing with a μP logic input.

Highly Integrated, Linear Battery Charger with Thermal Regulation for Portable Applications

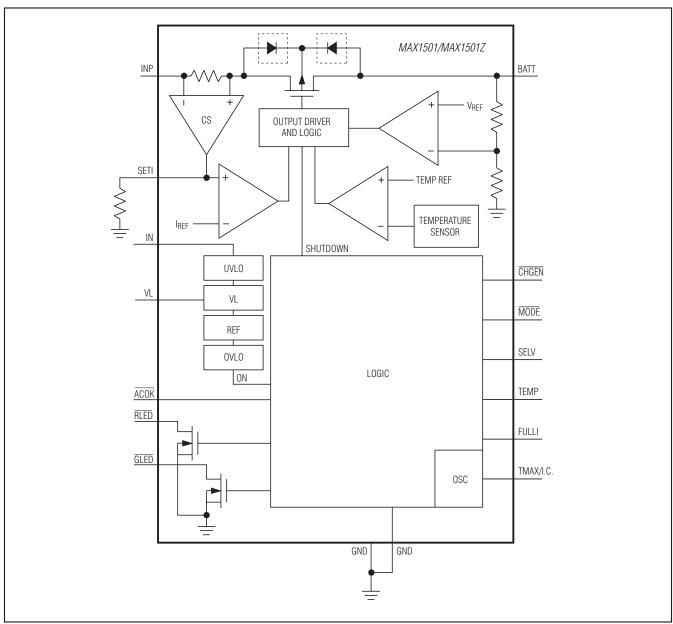


Figure 1. Functional Diagram

Highly Integrated, Linear Battery Charger with Thermal Regulation for Portable Applications

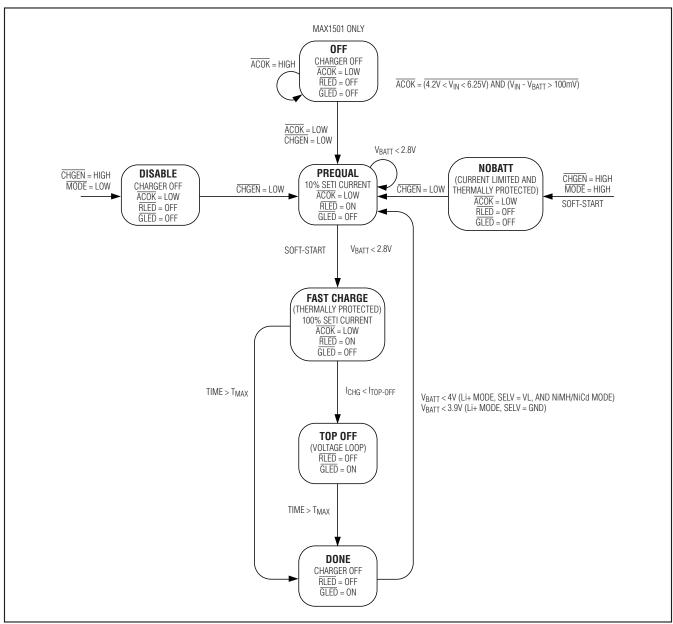


Figure 2. MAX1501 Charge State Diagram

Highly Integrated, Linear Battery Charger with Thermal Regulation for Portable Applications

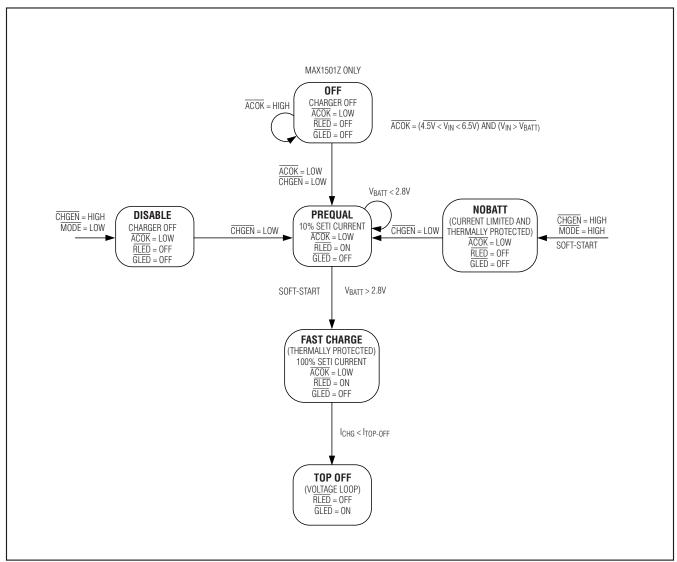


Figure 3. MAX1501Z Charge State Diagram

Detailed Description

Modes of Operation

 $\overline{\text{CHGEN}}$ and $\overline{\text{MODE}}$ together set the operating modes of the MAX1501. Both inputs possess internal 175kΩ pulldown resistors to GND. Table 1 describes the four operating modes of the MAX1501.

Table 1. Modes of Operation

•						
MODE	CHGEN	MODE				
Li+ Charge Mode	0	0				
NiMH/NiCd Charge Mode	0	1				
Disable Mode	1	0				
No-Battery Mode	1	1				

Table 2. Battery Regulation Voltage

CHARGING MODE	SE	LV
CHARGING MODE	GND	VL
Li+	4.1V	4.2V
NiMH/NiCd	4.5V	4.95V

Li+ Charge Mode

Connect CHGEN and MODE to GND to place the MAX1501 in Li+ charging mode. The Li+ charger consists of a voltage-control loop, a current-control loop, and a thermal-control loop. Connect SELV to GND to set the Li+ battery voltage to 4.1V. Connect SELV to VL to set the Li+ battery voltage to 4.2V (Table 2).

The MAX1501 precharges the Li+ battery with 10% of the user-programmed fast-charge current at the start of a charge cycle. A soft-start algorithm ramps up the charging current (10% steps with 20ms duration per step) to the fast-charge current when the battery voltage reaches 2.8V. The MAX1501 enters constant-voltage mode and decreases the charge current when the BATT voltage reaches the selected regulation voltage (4.1V or 4.2V). Set the fast-charge current with a resistor between SETI and GND (see the *Charge-Current Selection* section).

The thermal-regulation loop limits the MAX1501 die temperature to the value selected by the TEMP input by reducing the charge current as necessary (see the *Thermal-Regulation Selection* section). This feature not only protects the MAX1501 from overheating, but also allows the charge current to be set higher without risking damage to the system.

Set the top-off-current threshold with the three-state FULLI input (see the *Top-Off-Current Selection* section). RLED goes high impedance and GLED asserts low when the top-off-current threshold is reached. The MAX1501 automatically initiates recharging when the battery voltage drops below 95% of the voltage set by SELV. (The MAX1501Z does not time out.)

NiMH/NiCd Charge Mode

Connect $\overline{\text{MODE}}$ to VL to place the MAX1501 in NiMH/NiCd charging mode. NiMH/NiCd mode uses only current- and voltage-control loops, and is intended for trickle- or timer-based chargers. Fast charge termination schemes, such as negative ΔV or ΔT , are not included in the MAX1501 but can be implemented using an external microcontroller. Trickle- or timer-based chargers should connect SELV to GND to set the regulation voltage to 4.5V. μP controlled chargers should connect SELV to VL

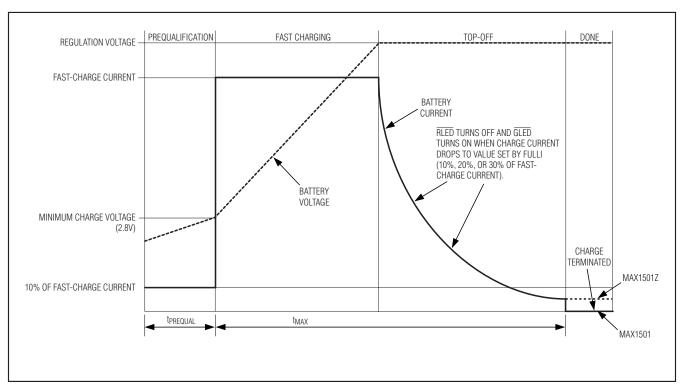


Figure 4. Li+ Charge Sequence

Highly Integrated, Linear Battery Charger with Thermal Regulation for Portable Applications

Table 3. RLED and GLED Behavior

MODE	STATE	RLED	GLED
Li+ Mode or NiMH/NiCd Mode	 10% current-limited precharge Current-limited charge Voltage-limited charge before top-off Temperature-limited charge before top-off 	Sinks 10mA	High impedance
	Voltage-limited charge after top-offSafety timer expires (MAX1501 only)	High impedance	Sinks 20mA
Disable Mode	_	High impedance	
No Battery Mode	_	High impedance	High impedance

to set the regulation voltage to 4.95V to not interfere with μP charge control. When the regulation voltage is 4.95V, V_{IN} must be $\geq 5.25V$.

The MAX1501 precharges with 10% of the user-programmed charge current at the start of a charge cycle. Fast charge begins when VBATT exceeds 2.8V. A resistor from SETI to GND sets charge current. See the *Charge-Current Selection* section. The MAX1501 enters voltage mode and decreases charge current when VBATT reaches 4.5V.

The thermal-regulation loop limits die temperature to the value selected by TEMP by reducing charge current. See the *Thermal-Regulation Selection* section. This protects the MAX1501 from overheating when supplying high charge currents, or operating from high input voltages.

Set the top-off-current threshold with the three-state FULLI input. See the *Top-Off-Current Selection* section. RLED goes high impedance and GLED asserts low when the top-off current threshold is reached. The MAX1501 automatically initiates recharging when the battery voltage drops below 4V. The MAX1501Z does not time out.

No-Battery Mode

Connect CHGEN and MODE to VL to place the MAX1501 in no-battery mode. An external load can be connected to BATT in this mode. VBATT regulates to 4V in no-battery mode, regardless of the state of SELV.

The current-control loop, voltage-control loop, and thermal-control loop all function in no-battery mode. The loop gain of the voltage-control loop decreases to ensure stability with no battery present. Connect a 10µF ceramic capacitor to BATT for stability. RLED and GLED are both high impedance in no-battery mode.

ACOK

The ACOK output asserts low when V_{IN} is present, 4.2V \leq V_{IN} \leq 6.25V, and V_{IN} - V_{BATT} > 100mV. The \overline{ACOK} open-drain output requires an external 100k Ω pullup

resistor to an external supply voltage. The external supply voltage must be less than 5.5V.

RLED and **GLED** Indicators

RLED and $\overline{\text{GLED}}$ serve as visual indicators that power is applied as well as the charge status of a battery. RLED asserts low when a wall adapter is connected and a battery is charging, regardless of cell chemistry. $\overline{\text{GLED}}$ asserts low when power is applied and the battery is fully charged. Both outputs go high-impedance in shutdown. Connect the anode of each LED to IN, and the cathode to $\overline{\text{RLED}}$ or $\overline{\text{GLED}}$. Table 3 summarizes the behavior of $\overline{\text{RLED}}$ and $\overline{\text{GLED}}$ under normal operating conditions. Connect pullup resistors to the μP I/O supply when interfacing $\overline{\text{RLED}}$ and $\overline{\text{GLED}}$ with a μP 's logic inputs.

Soft-Start

A ten-step, soft-start algorithm activates when entering fast-charge mode. The charging current ramps up in 10% increments, 20ms per step, to the full charging current when VBATT exceeds 2.8V.

_Applications Information

Charge-Current Selection

Program the charging current using an external resistor between SETI and GND. Set the charge-current resistor with the following equation:

$$R_{SETI} = 1000 \times \frac{1.4V}{I_{BATT}}$$

If VSETI = 1.4V, the current-control loop controls the battery charging. If VSETI < 1.4V, either the voltage-control loop or the thermal-control loop operates. Measure the charging current by monitoring VSETI and using the following equation:

$$V_{SETI} = \frac{I_{BATT}}{1000} \times R_{SETI}$$

Thermal-Regulation Selection

Set the regulated die temperature of the MAX1501 with the TEMP three-level logic input. The MAX1501 reduces the charge current to limit the die temperature to the value set by TEMP. The MAX1501 operates normally while the thermal loop is active. An active thermal loop does not indicate a fault condition. TEMP allows the MAX1501 to maximize the charge current while providing protection against excessive power dissipation.

Connect TEMP to GND to regulate the die temperature at $+95^{\circ}$ C. Leave TEMP floating to regulate the die temperature at $+115^{\circ}$ C. Connect TEMP to VL to regulate the die temperature at $+135^{\circ}$ C.

Top-Off-Current Selection

Set the top-off-current threshold in the Li+ and NiMH/NiCd charge modes with the FULLI three-level logic input. The top-off-current threshold determines when RLED turns off and GLED turns on, indicating the charge status of the battery.

Connect FULLI to GND to set the top-off-current threshold to 10% of the fast-charge current. Connect FULLI to VL to set the top-off-current threshold to 20% of the fast-charge current. Leave FULLI floating to set the top-off-current threshold to 30% of the fast-charge current.

Charge-Timer Selection (MAX1501 Only)

Set the maximum charging time with the TMAX three-level logic input. TMAX limits the duration of charging to protect the battery from overcharging. Connect TMAX to GND to set the maximum charging time to 3 hours. Leave TMAX floating to set the maximum charging time

4.5V TO 6.5V 4.2V 1-CELL Li+ AC ADAPTER INP MAX1501/MAX1501Z GLED MODE RLED CHGEN TMAX/I.C. ACOK TEMP SELV FULLI GND GND

Figure 5. Stand-Alone Li+ Battery Charger

to 4.5 hours. Connect TMAX to VL to set the maximum charging time to 6 hours.

Capacitor Selection

Connect a ceramic capacitor from BATT to GND for proper stability. Use a $10\mu F$ X5R ceramic capacitor for most applications.

Connect IN and INP together and bypass to GND with a $1\mu F$ ceramic capacitor. Use a larger input bypass capacitor for high input voltages or high charging currents to reduce supply noise.

Thermal Considerations

The MAX1501 is available in a thermally enhanced thin QFN package with exposed paddle. Connect the exposed paddle of the MAX1501 to a large copper ground plane to provide a thermal contact between the device and the circuit board. The exposed paddle transfers heat away from the device, allowing the MAX1501 to charge the battery with maximum current, while minimizing the increase in die temperature.

Application Circuits

Figure 5 shows the MAX1501 as a stand-alone Li+ battery charger. The $2.8k\Omega$ resistor connected to SETI sets a charging current of 500mA.

Figure 6 shows the MAX1501 as a μ P-based Li+ battery charger. Drive \overline{CHGEN} low to charge the battery. Drive \overline{CHGEN} high to disable the charger. Connect a $100k\Omega$ pullup resistor from \overline{ACOK} to the logic supply voltage of the μ P to detect the presence of an input supply. The logic supply voltage must be less than 5.5V.

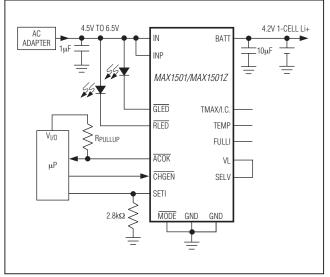


Figure 6. μP-Based Li+ Battery Charger

Highly Integrated, Linear Battery Charger with Thermal Regulation for Portable Applications

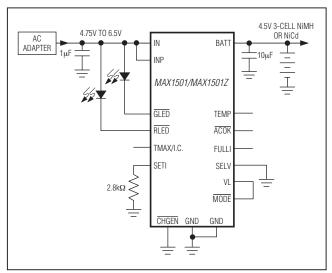


Figure 7. Stand-Alone 3-Cell NiMH or NiCd Battery Charger

Figure 7 shows the MAX1501 as a stand-alone trickle or timer-based NiMH/NiCd battery charger. Connecting SELV to GND sets the charge termination voltage to 4.5V.

Figure 8 shows the MAX1501 as a μP -based NiMH/NiCd battery charger. Connecting SELV to VL sets the charge regulation voltage at 4.95V. Connect a 100k Ω pullup resistor from \overline{ACOK} to the logic supply voltage of the μP . The logic supply voltage must be less than 5.5V.

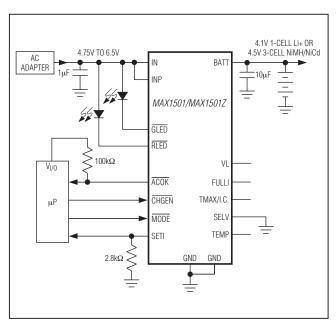


Figure 9. µP-Based Single Li+/3-Cell NiMH/NiCd Battery Charger

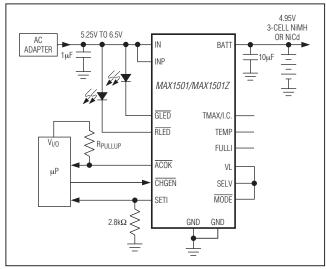


Figure 8. µP-Based NiMH or NiCd Battery Charge

Figure 9 shows the MAX1501 as a $\mu P\text{-based}$ single Li+ or 3-cell NiMH/NiCd charger. The states of $\overline{\text{MODE}}$ and $\overline{\text{CHGEN}}$ set the operating mode of the MAX1501 (Table 1). Connect a $100 \text{k}\Omega$ pullup resistor from $\overline{\text{ACOK}}$ to the logic supply voltage of the μP . The logic supply voltage must be less than 5.5V.

Figure 10 shows the MAX1501 as an accurate current-limited low-dropout linear regulator with input overvoltage protection (no-battery mode). The output voltage regulates to 4V, regardless of the state of SELV. Connect MODE to VL to enable the linear regulator. Connect MODE to GND to put the device into shutdown. RSETI sets the maximum output current.

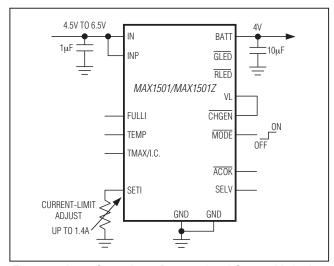


Figure 10. Input Overvoltage-Protected and Current-Limited Low-Dropout Linear Regulator

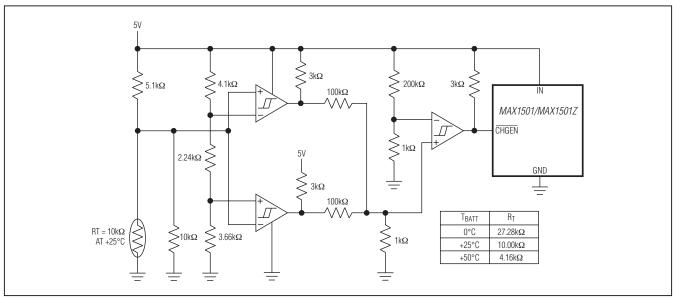


Figure 11. Battery Temperature Protection

Figure 11 shows a circuit that adds temperature protection to the battery. Install the thermistor as close to the battery as possible to ensure accurate temperature measurement. The output of this circuit is logic high when the battery temperature is less than 0°C and greater than +50°C. Driving CHGEN high disables the charger.

Layout and Bypassing

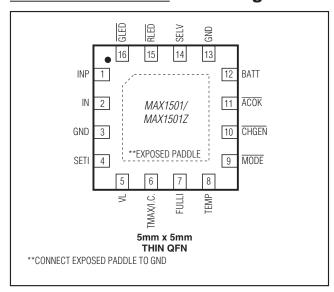
Connect IN and INP together as close to the device as possible and bypass with a 1µF ceramic capacitor. Bypass BATT to GND with a 10µF ceramic capacitor.

Provide a large copper GND plane to allow the exposed paddle to sink heat away from the device. Connect the battery to BATT as close to the device as possible to provide the most accurate battery voltage sensing. Make all high-current traces short and wide to minimize voltage drops.

Chip Information

TRANSISTOR COUNT: 5717
PROCESS: BICMOS

Pin Configuration



Package Information

For the latest package outline information and land patterns (footprints), go to www.maximintegrated.com/package. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
16 TQFN	T1655+3	<u>21-0140</u>	90-0073

Highly Integrated, Linear Battery Charger with Thermal Regulation for Portable Applications

Revision History

REVISION	REVISION	DESCRIPTION	PAGES
NUMBER	DATE		CHANGED
2	4/14	Updated General Description, NiMH/NiCd Charge Mode, and Application Circuits sections	1, 11, 12, 14, 16



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