

## COMPLEMENTARY SILICON HIGH-POWER TRANSISTORS

... designed for use in general purpose power amplifier and switching applications.

### FEATURES:

\* Collector-Emitter Sustaining Voltage -

$V_{CE(sus)}$  = 40V(Min)- TIP33, TIP34  
60V(Min)- TIP33A, TIP34A  
80V(Min)- TIP33B, TIP34B  
100V(Min)- TIP33C, TIP34C

\* DC Current Gain  $hFE=40(\text{Min})@I_C=1.0A$

\* Current Gain-Bandwidth Product  $f_T=3.0\text{ MHz}(\text{Min})@I_C=0.5A$

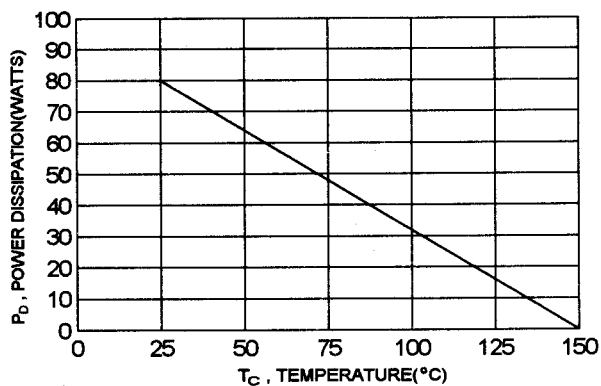
### MAXIMUM RATINGS

| Characteristic                                                                     | Symbol         | TIP33<br>TIP34 | TIP33A<br>TIP34A | TIP33B<br>TIP34B | TIP33C<br>TIP34C | Unit                     |
|------------------------------------------------------------------------------------|----------------|----------------|------------------|------------------|------------------|--------------------------|
| Collector-Emitter Voltage                                                          | $V_{CEO}$      | 40             | 60               | 80               | 100              | V                        |
| Collector-Base Voltage                                                             | $V_{CBO}$      | 40             | 60               | 80               | 100              | V                        |
| Emitter-Base Voltage                                                               | $V_{EBO}$      | 5.0            |                  |                  |                  | V                        |
| Collector Current - Continuous<br>- Peak                                           | $I_C$          | 10<br>15       |                  |                  |                  | A                        |
| Base Current                                                                       | $I_B$          | 3.0            |                  |                  |                  | A                        |
| Total Power Dissipation@ $T_C=25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$ | $P_D$          | 80<br>0.64     |                  |                  |                  | W<br>W/ $^\circ\text{C}$ |
| Operating and Storage Junction<br>Temperature Range                                | $T_J, T_{STG}$ | -65 to +150    |                  |                  |                  | $^\circ\text{C}$         |

### THERMAL CHARACTERISTICS

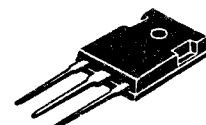
| Characteristic                      | Symbol          | Max  | Unit               |
|-------------------------------------|-----------------|------|--------------------|
| Thermal Resistance Junction to Case | $R_{\theta jc}$ | 1.56 | $^\circ\text{C/W}$ |

FIGURE -1 POWER DERATING

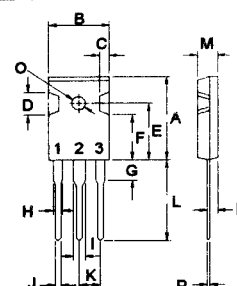


| NPN    | PNP    |
|--------|--------|
| TIP33  | TIP34  |
| TIP33A | TIP34A |
| TIP33B | TIP34B |
| TIP33C | TIP34C |

10 AMPERE  
COMPLEMENTARY SILICON  
POWER TRANSISTORS  
40 -100 VOLTS  
80 WATTS



TO-247(3P)



PIN 1.BASE  
2.COLLECTOR  
3.EMITTER

| DIM | MILLIMETERS |       |
|-----|-------------|-------|
|     | MIN         | MAX   |
| A   | 20.63       | 22.38 |
| B   | 15.38       | 16.20 |
| C   | 1.90        | 2.70  |
| D   | 5.10        | 6.10  |
| E   | 14.81       | 15.22 |
| F   | 11.72       | 12.84 |
| G   | 4.20        | 4.50  |
| H   | 1.82        | 2.46  |
| I   | 2.92        | 3.23  |
| J   | 0.89        | 1.53  |
| K   | 5.26        | 5.66  |
| L   | 18.50       | 21.50 |
| M   | 4.68        | 5.36  |
| N   | 2.40        | 2.80  |
| O   | 3.25        | 3.65  |
| P   | 0.55        | 0.70  |

**TIP33, TIP33A, TIP33B, TIP33C NPN / TIP34, TIP34A, TIP34B, TIP34C PNP**

**ELECTRICAL CHARACTERISTICS (  $T_C = 25^\circ\text{C}$  unless otherwise noted )**

| Characteristic | Symbol | Min | Max | Unit |
|----------------|--------|-----|-----|------|
|----------------|--------|-----|-----|------|

**OFF CHARACTERISTICS**

|                                                                                                                                                                                                               |                                                                    |                |                          |    |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------|----------------|--------------------------|----|
| Collector-Emitter Sustaining Voltage(1)<br>( $I_C = 30\text{ mA}$ , $I_B = 0$ )                                                                                                                               | TIP33, TIP34<br>TIP33A, TIP34A<br>TIP33B, TIP34B<br>TIP33C, TIP34C | $V_{CEO(sus)}$ | 40<br>60<br>80<br>100    | V  |
| Collector Cutoff Current<br>( $V_{CE} = 30\text{ V}$ , $I_B = 0$ )<br>( $V_{CE} = 60\text{ V}$ , $I_B = 0$ )                                                                                                  | TIP33, TIP34, TIP33A, TIP34A<br>TIP33B, TIP34B, TIP33C, TIP34C     | $I_{CEO}$      | 0.7<br>0.7               | mA |
| Collector Cutoff Current<br>( $V_{CE} = 40\text{ V}$ , $V_{EB} = 0$ )<br>( $V_{CE} = 60\text{ V}$ , $V_{EB} = 0$ )<br>( $V_{CE} = 80\text{ V}$ , $V_{EB} = 0$ )<br>( $V_{CE} = 100\text{ V}$ , $V_{EB} = 0$ ) | TIP33, TIP34<br>TIP33A, TIP34A<br>TIP33B, TIP34B<br>TIP33C, TIP34C | $I_{CES}$      | 0.4<br>0.4<br>0.4<br>0.4 | mA |
| Emitter Cutoff Current<br>( $V_{EB} = 5.0\text{ V}$ , $I_C = 0$ )                                                                                                                                             |                                                                    | $I_{EBO}$      | 1.0                      | mA |

**ON CHARACTERISTICS (1)**

|                                                                                                                                           |               |          |            |   |
|-------------------------------------------------------------------------------------------------------------------------------------------|---------------|----------|------------|---|
| DC Current Gain<br>( $V_{CE} = 4.0\text{ V}$ , $I_C = 1.0\text{ A}$ )<br>( $V_{CE} = 4.0\text{ V}$ , $I_C = 3.0\text{ A}$ )               | $h_{FE}$      | 40<br>20 | 100        |   |
| Collector-Emitter Saturation Voltage<br>( $I_C = 3.0\text{ A}$ , $I_B = 0.3\text{ A}$ )<br>( $I_C = 10\text{ A}$ , $I_B = 2.5\text{ A}$ ) | $V_{CE(sat)}$ |          | 1.0<br>4.0 | V |
| Base-Emitter On Voltage<br>( $I_C = 3.0\text{ A}$ , $V_{CE} = 4.0\text{ V}$ )<br>( $I_C = 10\text{ A}$ , $V_{CE} = 4.0\text{ V}$ )        | $V_{BE(on)}$  |          | 1.6<br>3.0 | V |

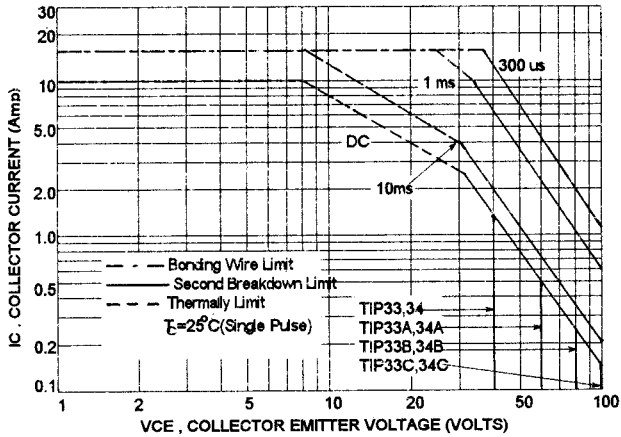
**DYNAMIC CHARACTERISTICS**

|                                                                                                                       |          |     |  |     |
|-----------------------------------------------------------------------------------------------------------------------|----------|-----|--|-----|
| Current Gain - Bandwidth Product (2)<br>( $I_C = 0.5\text{ A}$ , $V_{CE} = 10\text{ V}$ , $f_{TEST} = 1\text{ MHz}$ ) | $f_T$    | 3.0 |  | MHz |
| Small Signal Current Gain<br>( $I_C = 0.5\text{ A}$ , $V_{CE} = 10\text{ V}$ , $f = 1\text{ kHz}$ )                   | $h_{fe}$ | 20  |  |     |

(1) Pulse Test: Pulse width  $\leq 300\text{ }\mu\text{s}$ , Duty Cycle  $\leq 2.0\%$

(2)  $f_T \approx |h_{fe}| \cdot f_{TEST}$

FIG-2 ACTIVE- REGION SAFE OPERATING AREA

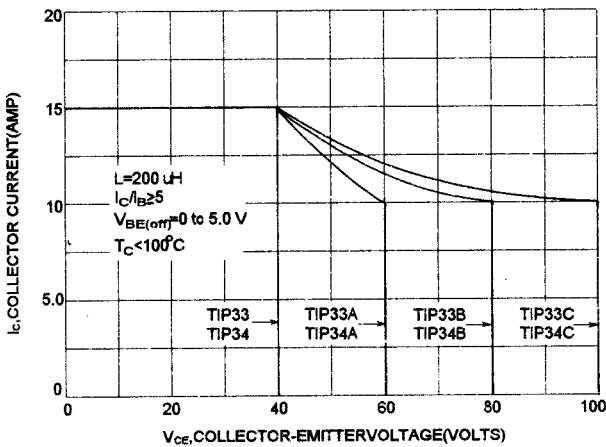


#### FORWARD BIAS

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown safe operating area curves indicate  $I_C$ - $V_{CE}$  limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than curves indicate.

The data of FIG-2 is based on  $T_C = 25^\circ\text{C}$ ;  $T_{J(PK)}$  is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to 10% but must be derated when  $T_C \geq 25^\circ\text{C}$ , second breakdown limitations do not derate the same as thermal limitations.

FIG-3 REVERSE BIAS SAFE OPERATING AREA



#### REVERSE BIAS

For inductive loads, high voltage and high current must be sustained simultaneously during turn-off, in most cases with the base-to-emitter junction reverse biased under these conditions the collector voltage must be held to a safe level at or below a specific value of collector current. This can be accomplished by several means such as active clamping, RC snubbing, load line shaping, etc. the safe level for these devices is specified as Reverse Bias Safe Operating Area and represents the voltage-current condition allowable during reverse biased turn-off. This rating is verified under clamped conditions so that the device is never subjected to an avalanche mode. FIG-3 gives the RBSOA characteristics.

FIG-4 DC CURRENT GAIN

