

# AN1324 (AN6564), AN1324NS (AN6564NS)

## Quadruple Operational Amplifiers

### Overview

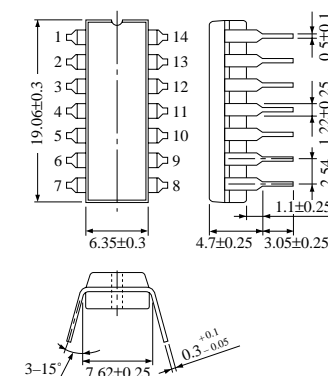
The AN1324 (AN6564) and the AN1324NS (AN6564NS) are quadruple operational amplifiers with phase compensation circuits built-in, and with wide range of operating voltages, allowing single power supply operation. They have electrical characteristics equal to the conventional operational amplifiers, and are low powered and suitable for application to various circuits.

### Features

- Built-in phase compensation circuit
- Wide range of common-mode input voltage  
0V to  $V_{CC}-1.5V$
- Wide range of operating voltages  
Single supply: 3 to 30V  
Dual supply:  $\pm 1.5$  to 15V

AN1324 (AN6564)

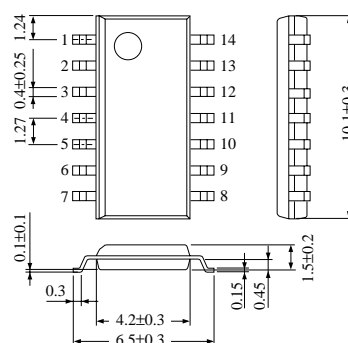
Unit : mm



14-pin DIL Plastic Package (DIP014-P-0300D)

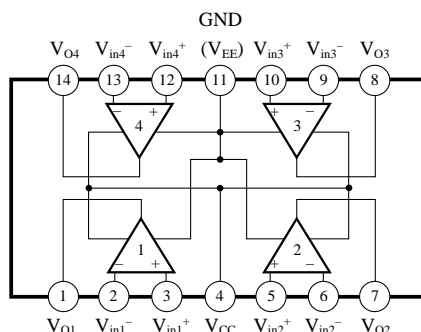
AN1324NS (AN6564NS)

Unit: mm



14-pin PANAFLAT Plastic Package (SOP014-P-0225A)

### Block Diagram



## Pin Descriptions

Pin No.	Pin name	Pin No.	Pin name
1	Ch.1 output	8	Ch.3 output
2	Ch.1 inverting input	9	Ch.3 inverting input
3	Ch.1 non inverting input	10	Ch.3 non inverting input
4	V <sub>CC</sub>	11	GND (V <sub>EE</sub> )
5	Ch.2 non inverting input	12	Ch.4 non inverting input
6	Ch.2 inverting input	13	Ch.4 inverting input
7	Ch.2 output	14	Ch.4 output

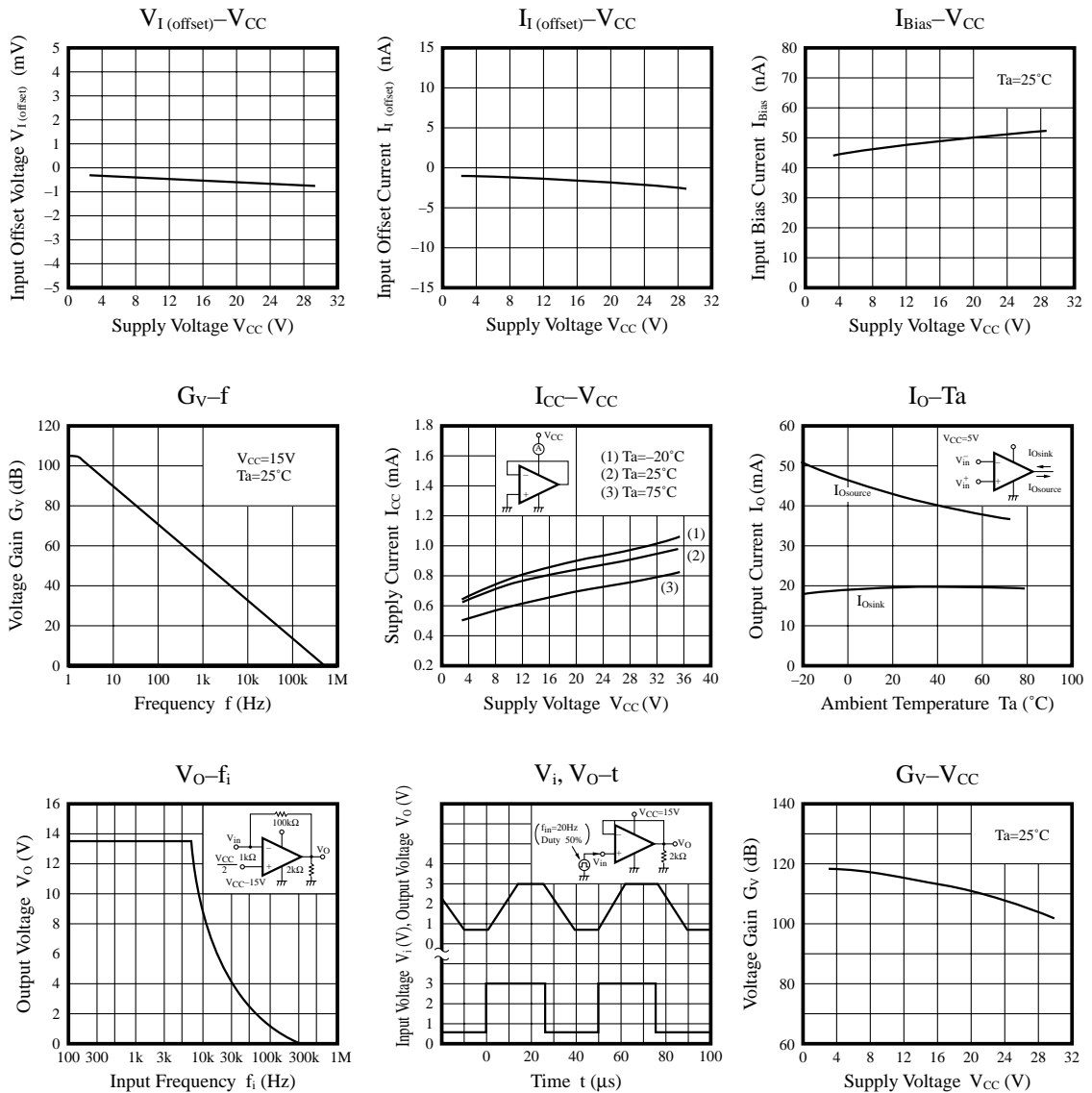
## Absolute Maximum Ratings (Ta=25°C)

Parameter		Symbol	Rating	Unit
Voltage	Supply voltage	V <sub>CC</sub>	32	V
	Differential input voltage	V <sub>ID</sub>	32	V
	Common-mode input voltage	V <sub>ICM</sub>	-0.3 to 32	V
	Output voltage	V <sub>O</sub>	24	V
Power dissipation	AN1324 (AN6564)	P <sub>D</sub>	570	mW
	AN1324NS (AN6564NS)		380	
Operating ambient temperature		T <sub>opr</sub>	-20 to +75	°C
Storage temperature	AN1324 (AN6564)	T <sub>stg</sub>	-55 to +150	°C
	AN1324NS (AN6564NS)		-55 to +125	

## Electrical Characteristics (V<sub>CC</sub>=5V, Ta=25°C)

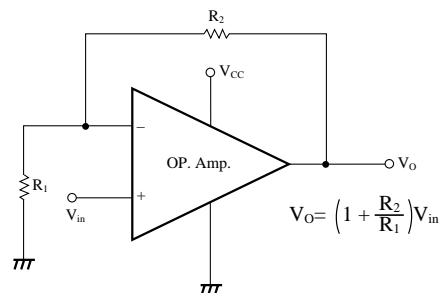
Parameter	Symbol	Condition	min	typ	max	Unit
Input offset voltage	V <sub>I(offset)</sub>	R <sub>S</sub> =50Ω	—	2	7	mV
Input bias current	I <sub>Bias</sub>		—	—	500	nA
Input offset current	I <sub>IO</sub>		—	—	50	nA
Common-mode input voltage range	V <sub>CM</sub>		0	—	V <sub>CC</sub> -1.5	V
Supply current	I <sub>CC</sub>	R <sub>L</sub> =∞	—	—	2	mA
Voltage gain	G <sub>V</sub>	R <sub>L</sub> ≥2kΩ	—	100	—	dB
Maximum output voltage	V <sub>O(max.)</sub>	R <sub>L</sub> =2kΩ	V <sub>CC</sub> -1.5	—	—	V
Common-mode rejection ratio	CMR		65	85	—	dB
Supply voltage rejection ratio	SVR		65	100	—	dB
Channel separation	CS	f=1kHz to 20kHz	—	120	—	dB
Output source current	I <sub>O(source)</sub>	V <sub>in+</sub> =1V, V <sub>in-</sub> =0V	20	40	—	mA
Output sink current	I <sub>SINK</sub>	V <sub>in+</sub> =0V, V <sub>in-</sub> =1V	10	20	—	mA

## Characteristics Curve



## Application Circuit

Positive Phase Amplifier



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