



Dual P-Channel 2.5-V (G-S) MOSFET

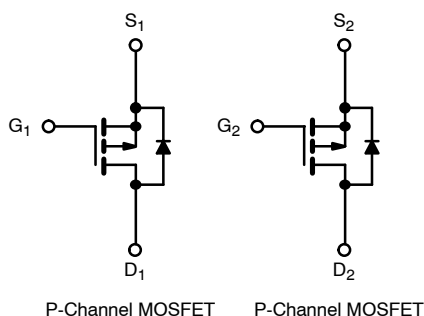
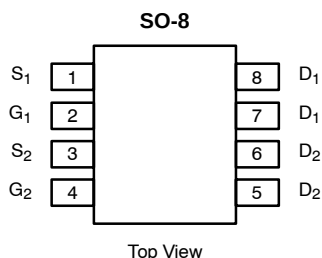


Available

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
-20	0.033 @ $V_{GS} = -4.5$ V	-6.2
	0.050 @ $V_{GS} = -2.5$ V	-5

FEATURES

- TrenchFET® Power MOSFET
- Lead (Pb)-Free Version is RoHS Compliant



Ordering Information: Si4963DY-T1
Si4963DY-T1—E3 (Lead (Pb)-Free)

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	10 secs	Steady State	Unit
Drain-Source Voltage		V _{DS}	−20		V
Gate-Source Voltage		V _{GS}	± 12		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	I _D	−6.2	−4.6	A
	T _A = 70 °C		−4.9	−3.7	
Pulsed Drain Current		I _{DM}	−40		
continuous Source Current (Diode Conduction) ^a		I _S	−1.7	−0.9	
Maximum Power Dissipation ^a	T _A = 25 °C	P _D	2.0	1.1	W
	T _A = 70 °C		1.3	0.7	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	−55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 10$ sec	R_{thJA}	50	62.5	$^\circ\text{C/W}$
	Steady State		90	110	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	30	35	

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

For SPICE model information via the Worldwide Web: <http://www.vishay.com/www/product/spice.htm>

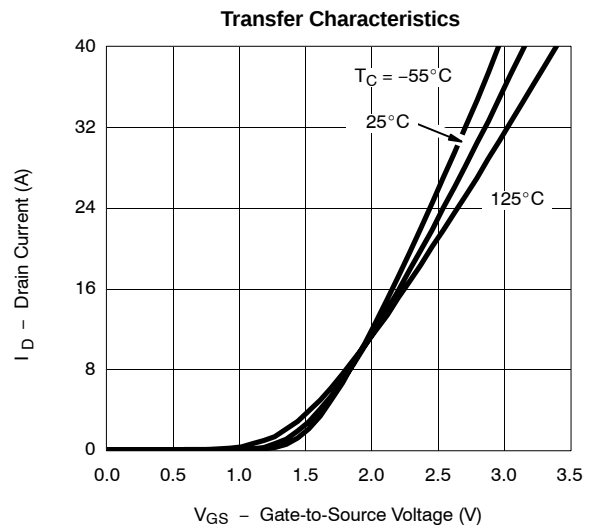
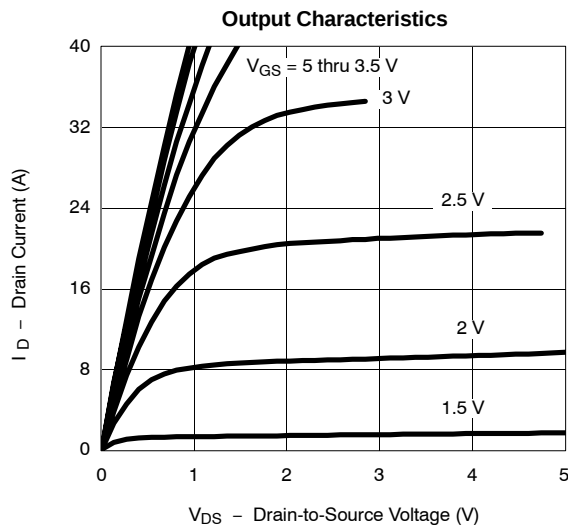
SPECIFICATIONS ($T_J = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\ \mu\text{A}$	-0.6			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\ \text{V}, V_{GS} = \pm 12\ \text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20\ \text{V}, V_{GS} = 0\ \text{V}$			-1	μA
		$V_{DS} = -20\ \text{V}, V_{GS} = 0\ \text{V}, T_J = 55^\circ\text{C}$			-5	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \leq -5\ \text{V}, V_{GS} = -4.5\ \text{V}$	-20			A
Drain-Source On-State Resistance ^a	$r_{DS(on)}$	$V_{GS} = -4.5\ \text{V}, I_D = -6.2\ \text{A}$		0.022	0.033	Ω
		$V_{GS} = -2.5\ \text{V}, I_D = -5\ \text{A}$		0.035	0.050	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -10\ \text{V}, I_D = -6.2\ \text{A}$		17		S
Diode Forward Voltage ^a	V_{SD}	$I_S = -1.7\ \text{A}, V_{GS} = 0\ \text{V}$		-0.75	-1.2	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = -10\ \text{V}, V_{GS} = -4.5\ \text{V}, I_D = -6.2\ \text{A}$		17	35	nC
Gate-Source Charge	Q_{gs}			4.1		
Gate-Drain Charge	Q_{gd}			4.3		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -10\ \text{V}, R_L = 10\ \Omega$ $I_D \cong -1\ \text{A}, V_{GEN} = -4.5\ \text{V}, R_G = 6\ \Omega$		25	50	ns
Rise Time	t_r			30	50	
Turn-Off Delay Time	$t_{d(off)}$			70	150	
Fall Time	t_f			50	70	
Source-Drain Reverse Recovery Time	t_{rr}			40	80	

Notes

- a. Pulse test; pulse width $\leq 300\ \mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.

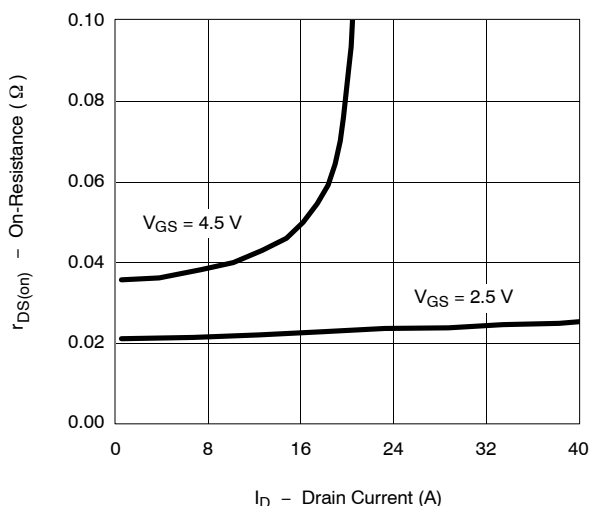
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

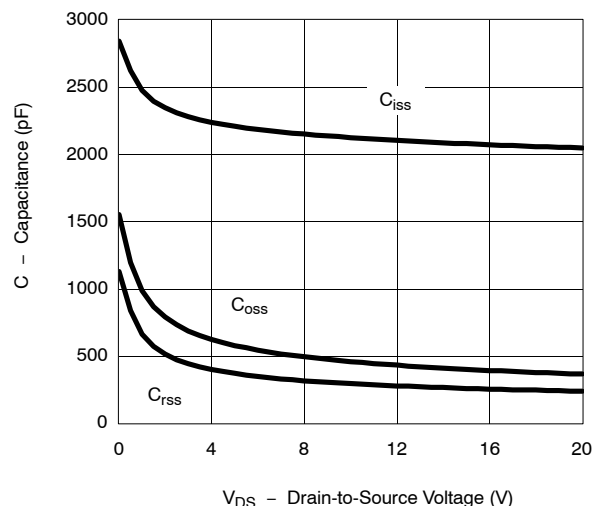


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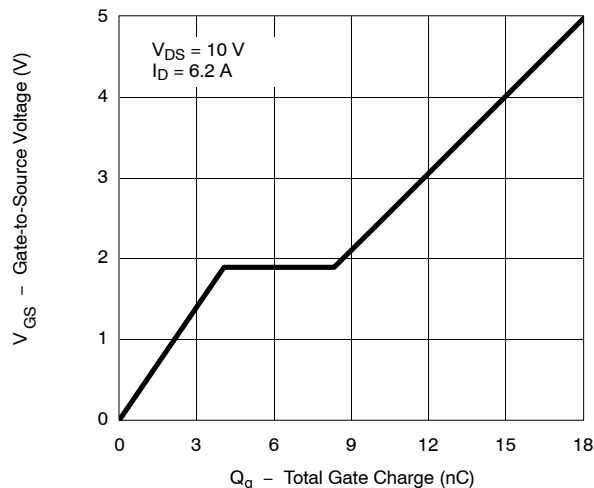
On-Resistance vs. Drain Current



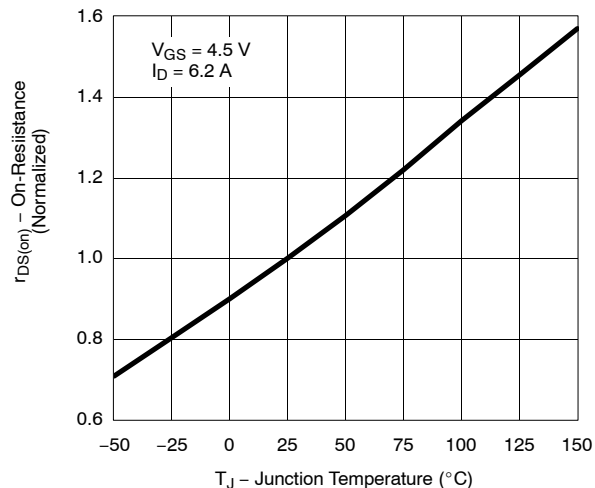
Capacitance



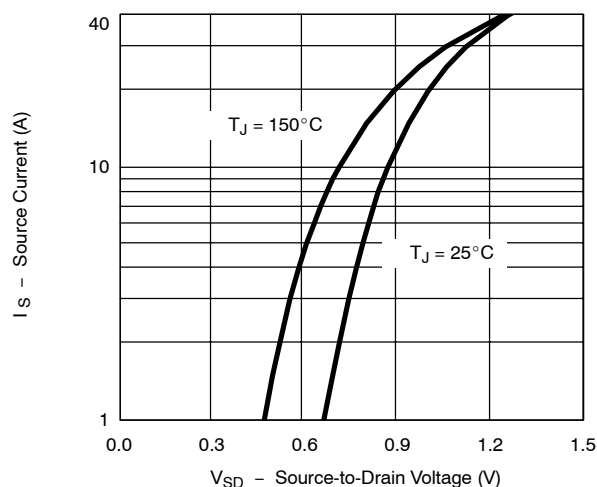
Gate Charge



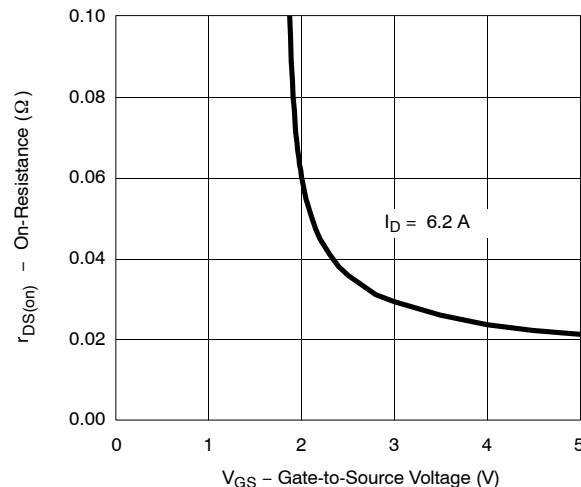
On-Resistance vs. Junction Temperature



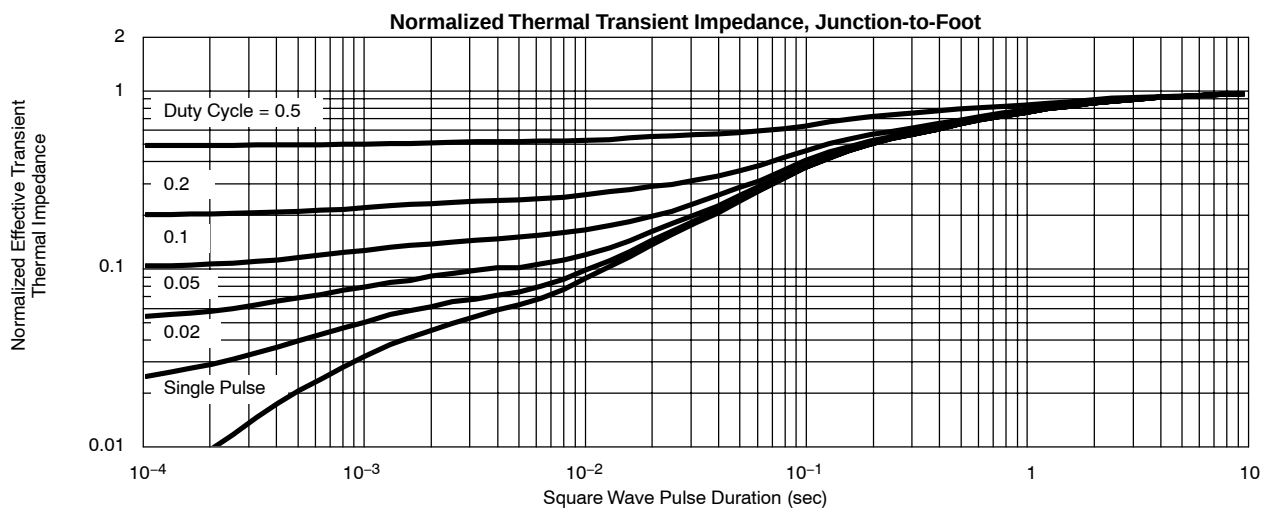
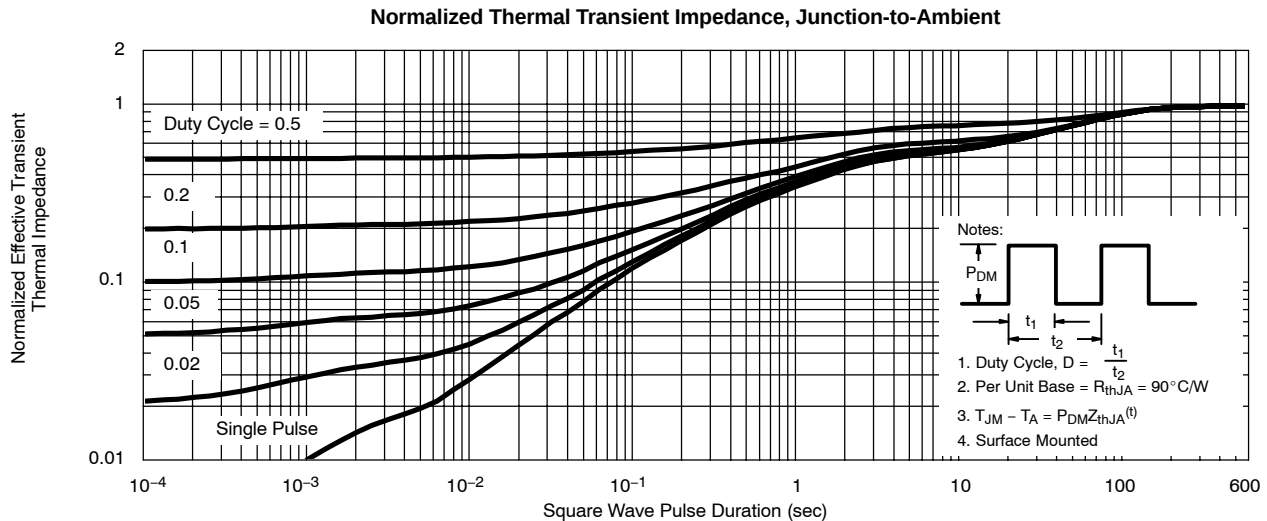
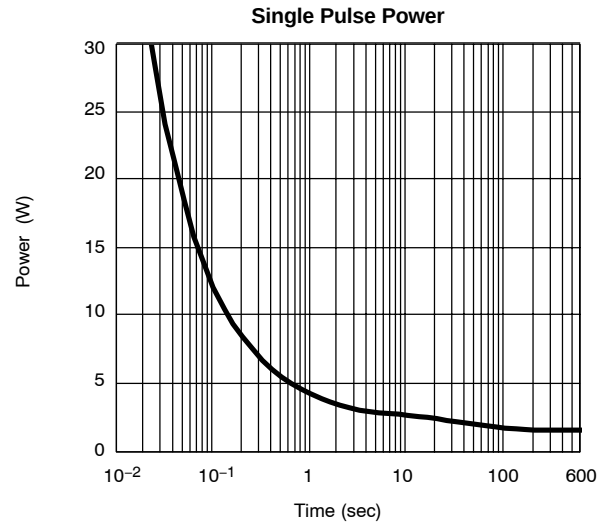
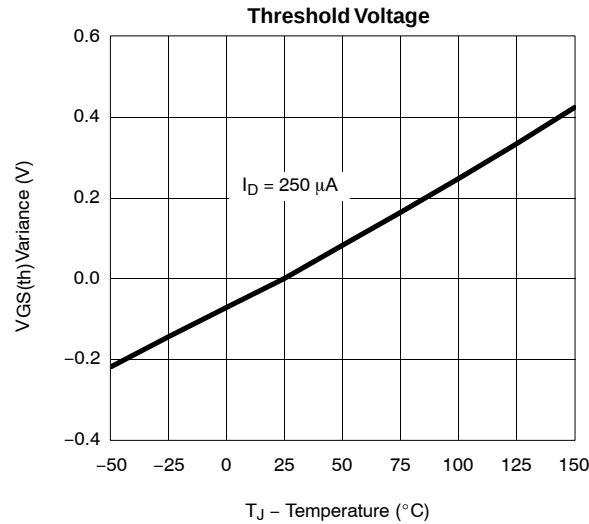
Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



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