

APT1004RBN 1000V 4.4A 4.00Ω

APT1004R2BN 1000V 4.0A 4.20Ω

POWER MOS IV®

N-CHANNEL ENHANCEMENT MODE HIGH VOLTAGE POWER MOSFETS

MAXIMUM RATINGS

All Ratings: $T_C = 25^\circ\text{C}$ unless otherwise specified.

Symbol	Parameter	APT 1004RBN	APT 1004R2BN	UNIT
V _{DSS}	Drain-Source Voltage	1000	1000	Volts
I _D	Continuous Drain Current @ T _C = 25°C	4.4	4.0	Amps
I _{DM}	Pulsed Drain Current ^①	17.6	16	
V _{GS}	Gate-Source Voltage	±30		Volts
P _D	Total Power Dissipation @ T _C = 25°C	180		Watts
	Linear Derating Factor	1.44		W/°C
T _J ,T _{STG}	Operating and Storage Junction Temperature Range	-55 to 150		°C
T _L	Lead Temperature: 0.063" from Case for 10 Sec.	300		

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions / Part Number	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage ($V_{GS} = 0V, I_D = 250 \mu A$)	APT1004RBN	1000		Volts
		APT1004R2BN	1000		
$I_{D(ON)}$	On State Drain Current ^② ($V_{DS} > I_{D(ON)} \times R_{DS(ON)}$ Max, $V_{GS} = 10V$)	APT1004RBN	4.4		Amps
		APT1004R2BN	4.0		
$R_{DS(ON)}$	Drain-Source On-State Resistance ^② ($V_{GS} = 10V, 0.5 I_D$ [Cont.])	APT1004RBN		4.00	Ohms
		APT1004R2BN		4.20	
I_{DSS}	Zero Gate Voltage Drain Current ($V_{DS} = V_{DSS}, V_{GS} = 0V$)			250	μA
	Zero Gate Voltage Drain Current ($V_{DS} = 0.8 V_{DSS}, V_{GS} = 0V, T_C = 125^\circ\text{C}$)			1000	
I_{GSS}	Gate-Source Leakage Current ($V_{GS} = \pm 30V, V_{DS} = 0V$)			± 100	nA
$V_{GS(TH)}$	Gate Threshold Voltage ($V_{DS} = V_{GS}, I_D = 1.0mA$)	2		4	Volts

THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case			0.68	$^\circ\text{C/W}$
$R_{\theta JA}$	Junction to Ambient			40	



CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

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DYNAMIC CHARACTERISTICS

APT1004R/1004R2BN

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C_{iss}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 25V$ $f = 1 \text{ MHz}$		805	950	pF
C_{oss}	Output Capacitance			115	160	
C_{rss}	Reverse Transfer Capacitance			37	60	
Q_g	Total Gate Charge ③	$V_{GS} = 10V$ $V_{DD} = 0.5 V_{DSS}$ $I_D = I_D [\text{Cont.}] @ 25^\circ\text{C}$		35	55	nC
Q_{gs}	Gate-Source Charge			4.3	6.5	
Q_{gd}	Gate-Drain ("Miller") Charge			18	27	
$t_d(\text{on})$	Turn-on Delay Time	$V_{GS} = 15V$ $V_{DD} = 0.5 V_{DSS}$ $I_D = I_D [\text{Cont.}] @ 25^\circ\text{C}$ $R_G = 1.8\Omega$		12	24	ns
t_r	Rise Time			10	20	
$t_d(\text{off})$	Turn-off Delay Time			33	50	
t_f	Fall Time			14	27	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic / Test Conditions / Part Number		MIN	TYP	MAX	UNIT
I_S	Continuous Source Current (Body Diode)	APT1004RBN			4.4	Amps
		APT1004R2BN			4.0	
I_{SM}	Pulsed Source Current ① (Body Diode)	APT1004RBN			17.6	
		APT1004R2BN			16	
V_{SD}	Diode Forward Voltage ② ($V_{GS} = 0V$, $I_S = -I_D [\text{Cont.}]$)				1.3	Volts
t_{rr}	Reverse Recovery Time ($I_S = -I_D [\text{Cont.}]$, $dI_S/dt = 100A/\mu s$)			290	580	ns
Q_{rr}	Reverse Recovery Charge ($I_S = -I_D [\text{Cont.}]$, $dI_S/dt = 100A/\mu s$)			1.65	3.3	μC

SAFE OPERATING AREA CHARACTERISTICS

Symbol	Characteristic	Test Conditions / Part Number	MIN	TYP	MAX	UNIT
SOA1	Safe Operating Area	$V_{DS} = 0.4 V_{DSS}$, $I_{DS} = P_D / 0.4 V_{DSS}$, $t = 1 \text{ Sec.}$	180			Watts
SOA2	Safe Operating Area	$I_{DS} = I_D [\text{Cont.}]$, $V_{DS} = P_D / I_D [\text{Cont.}]$, $t = 1 \text{ Sec.}$	180			
I_{LM}	Inductive Current Clamped	APT1004RBN	17.6			Amps
		APT1004R2BN	16			

① Repetitive Rating: Pulse width limited by maximum junction temperature. See Transient Thermal Impedance Curve. (Fig.1)

② Pulse Test: Pulse width < 380 μs , Duty Cycle < 2%

③ See MIL-STD-750 Method 3471

APT Reserves the right to change, without notice, the specifications and information contained herein.

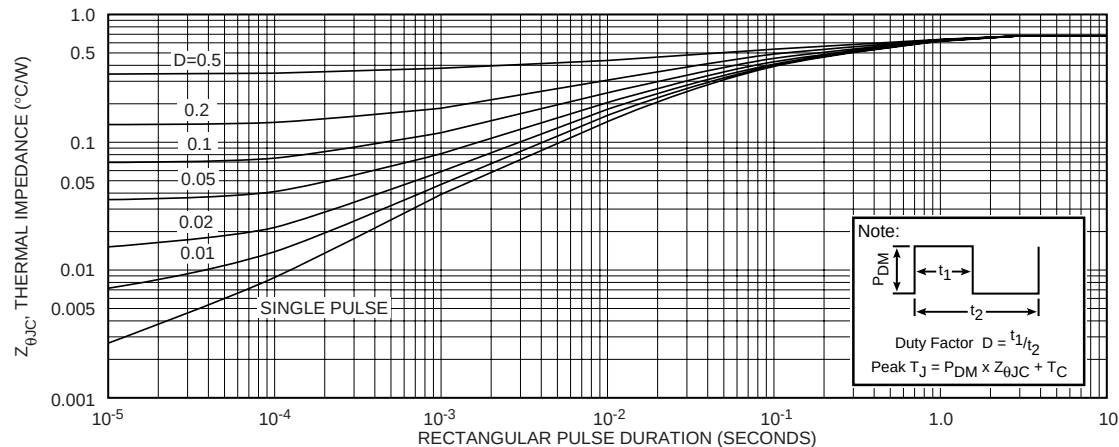


FIGURE 1, MAXIMUM EFFECTIVE TRANSIENT THERMAL IMPEDANCE, JUNCTION-TO-CASE vs PULSE DURATION

APT1004R/1004R2BN

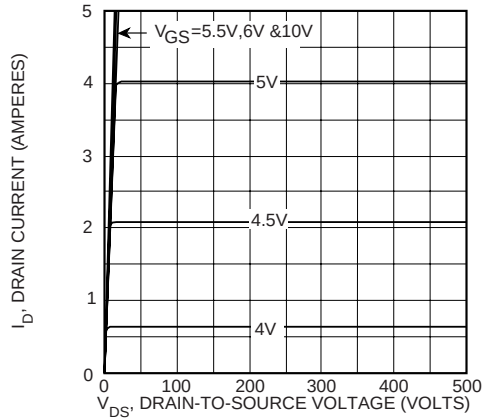


FIGURE 2, TYPICAL OUTPUT CHARACTERISTICS

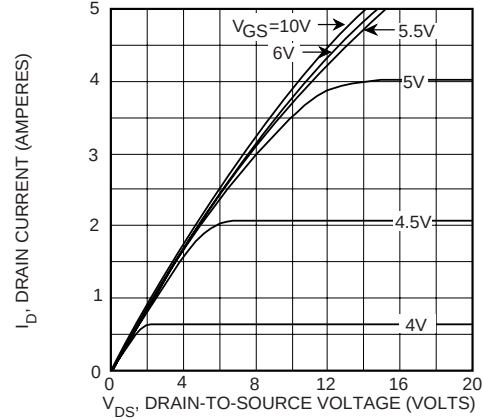


FIGURE 3, TYPICAL OUTPUT CHARACTERISTICS

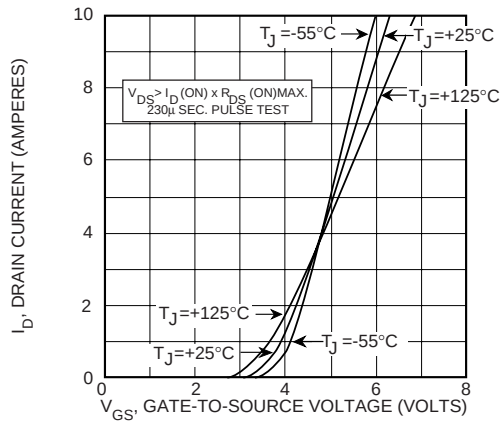


FIGURE 4, TYPICAL TRANSFER CHARACTERISTICS

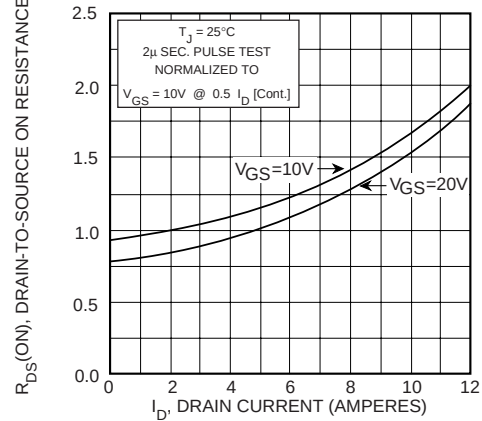


FIGURE 5, $R_{DS(ON)}$ vs DRAIN CURRENT

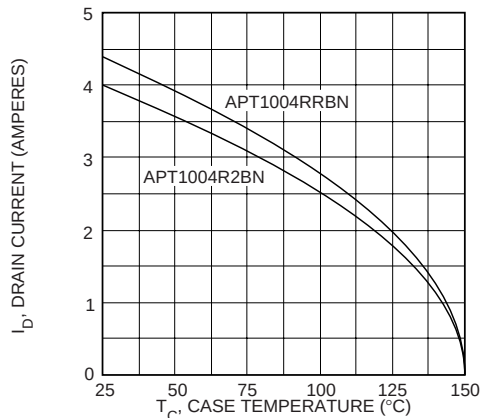


FIGURE 6, MAXIMUM DRAIN CURRENT vs CASE TEMPERATURE

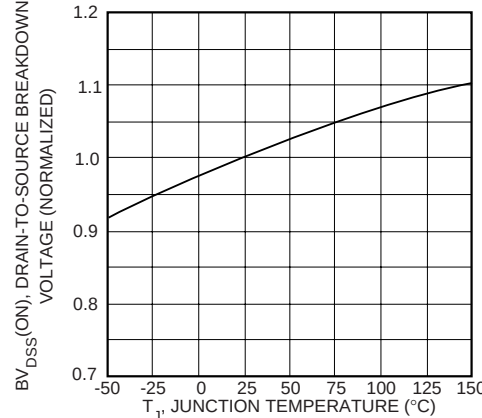


FIGURE 7, BREAKDOWN VOLTAGE vs TEMPERATURE

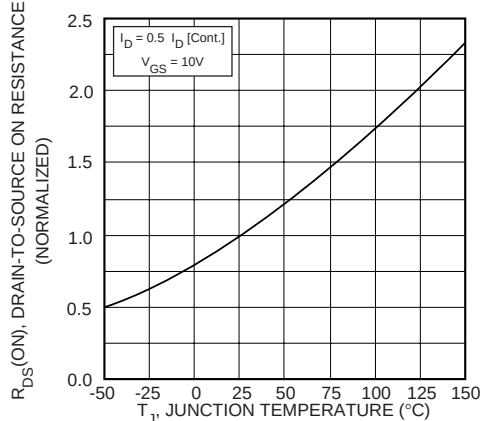


FIGURE 8, ON-RESISTANCE vs. TEMPERATURE

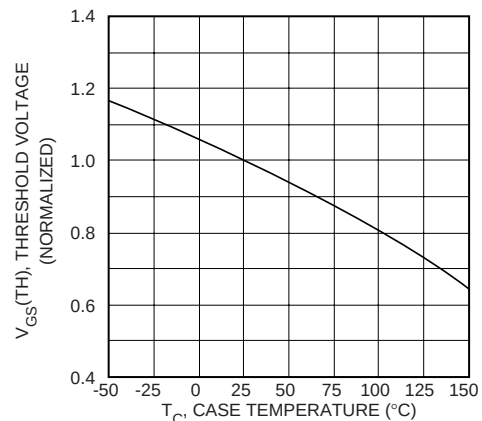


FIGURE 9, THRESHOLD VOLTAGE vs TEMPERATURE

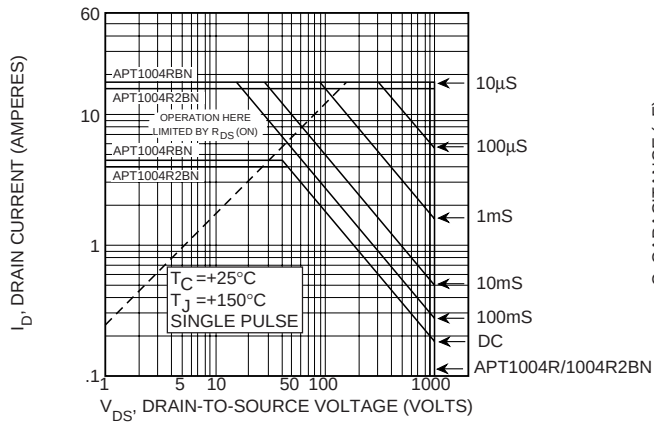


FIGURE 10, MAXIMUM SAFE OPERATING AREA

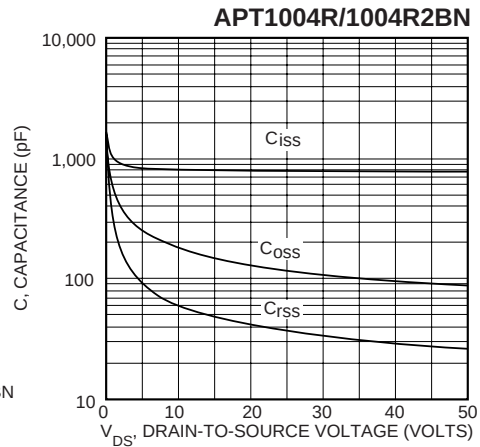


FIGURE 11, TYPICAL CAPACITANCE vs DRAIN-TO-SOURCE VOLTAGE

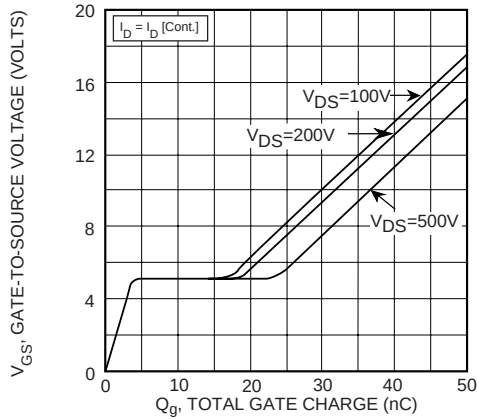


FIGURE 12, GATE CHARGES vs GATE-TO-SOURCE VOLTAGE

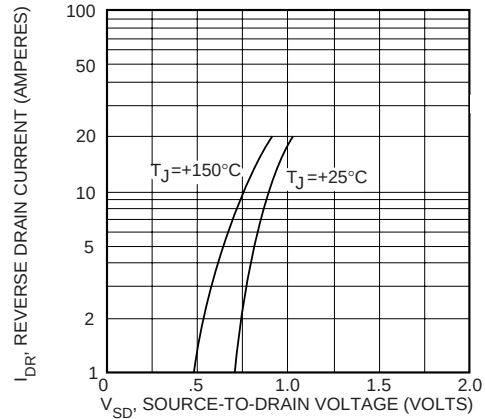


FIGURE 13, TYPICAL SOURCE-DRAIN DIODE FORWARD VOLTAGE

TO-247AD Package Outline

