

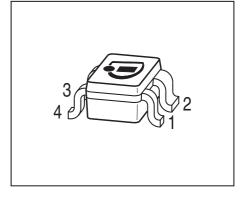
#### **Active Bias Controller**

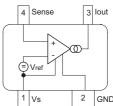
#### **Characteristics**

- Supplies stable bias current from 1.8V operating voltage on
- Low voltage drop:
   110mV for 10mA collector currrent

## **Application notes**

- Stabilizing bias current of NPN transistors and FET's from 100µA to 20mA
- Ideal supplement for Sieget and other transistors





- Pb-free (RoHS compliant) package 1)
- Qualified according AEC Q101





Type	Marking	Pin Configuration				Package
BCR410W	W8s	1= Vs	2=GND	3=lout	4=Sense	SOT343

## **Maximum Ratings**

Parameter	Symbol	Value	Unit
Supply voltage	V <sub>S</sub>	18	V
Output current	l <sub>out</sub>	0.5	mA
Total power dissipation, $T_S$ = 110 °C	P <sub>tot</sub>	100	mW
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	T <sub>stq</sub>	-65 150	

#### **Thermal Resistance**

Junction - soldering point <sup>2)</sup>	R <sub>thJS</sub>	≤ 470	K/W

<sup>&</sup>lt;sup>1</sup>Pb-containing package may be available upon special request

 $<sup>^2</sup>$ For calculation of  $R_{\mathrm{thJA}}$  please refer to Application Note Thermal Resistance



# **Electrical Characteristics** at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics	•	•	•	•	•
Additional current consumption	10	-	200	400	μΑ
V <sub>S</sub> = 3 V					
DC Characteristics with stabilized NPN	N-Transistors	•	•	•	•
Lowest sufficient battery voltage	V <sub>Smin</sub>	-	1.8	_	V
Voltage drop	$V_{ m drop}$	-	110	-	mV
$I_{\rm C}$ = 10 mA					
Change of I <sub>C</sub> versus h <sub>FE</sub>	$\Delta I_{\rm C}/I_{\rm C}$	-	tbd	-	Δh <sub>FE</sub> /
$h_{FE} = 50$					h <sub>FE</sub>
Change of $I_{\mathbb{C}}$ versus $V_{\mathbb{S}}$	$\Delta I_{\rm C}/I_{\rm C}$	-	2	_	%/V
V <sub>S</sub> = 3 V					
Change of $I_{\mathbb{C}}$ versus $T_{\mathbb{A}}$	$\Delta l_{\rm C}/l_{\rm C}$	-	0.15	-	%/K

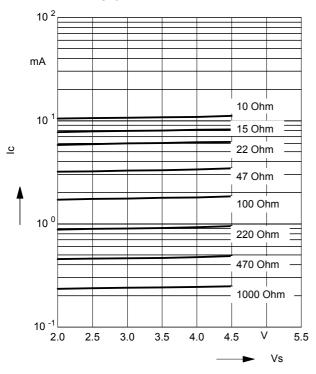
2 2007-05-29



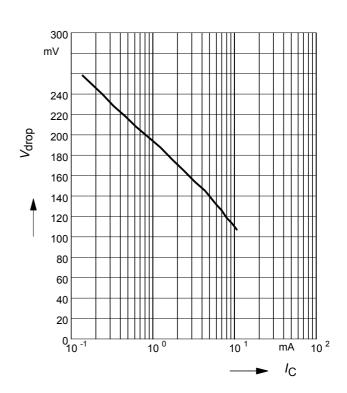
## Collector Current $I_C = f(V_S)$

of stabilized NPN Transistor

Parameter  $R_{\text{ext.}}(\Omega)$ 

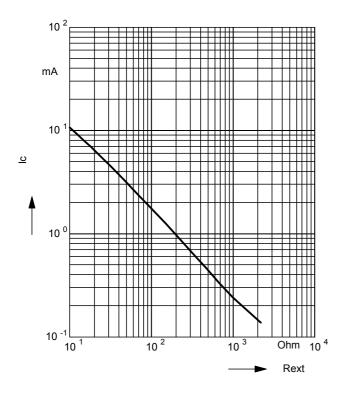


# Voltage drop $V_{\text{drop}} = f(I_{\text{C}})$

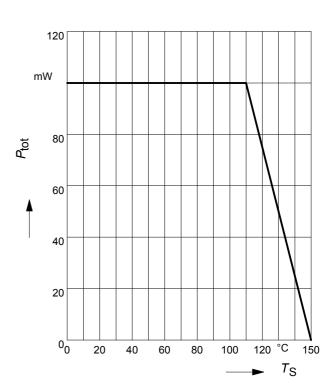


# Collector current $I_C = f(R_{ext.})$

of stabilized NPN Transistor

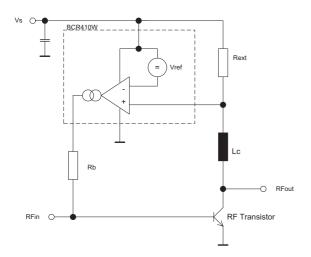


Total power dissipation  $P_{tot} = f(T_S)$ 





## **Application Circuit:**

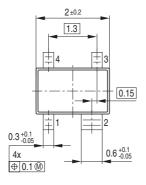


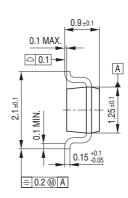
4 2007-05-29



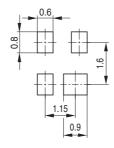
## Package Outline



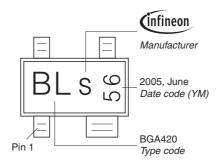




## Foot Print

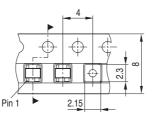


## Marking Layout (Example)



## Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel





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6

2007-05-29