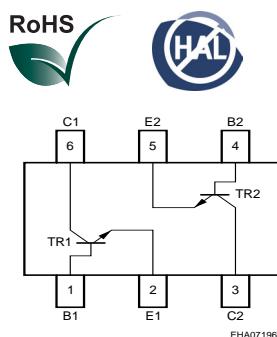
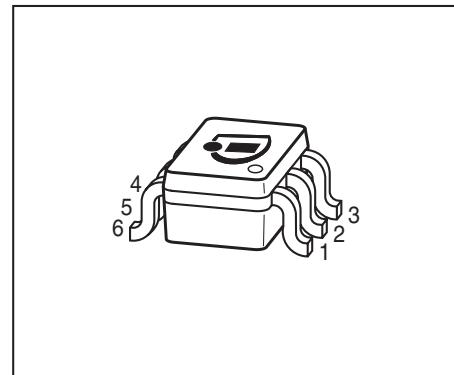


## Low Noise Silicon Bipolar RF Transistor

- For low noise, high-gain broadband amplifiers at collector currents from 0.5 mA to 12 mA
- $f_T = 8$  GHz,  $NF_{min} = 0.9$  dB at 900 MHz
- Two (galvanic) internal isolated Transistors in one package
- For orientation in reel see package information below
- Easy to use Pb-free (RoHS compliant) and halogen free industry standard package with visible leads
- Qualification report according to AEC-Q101 available



**ESD (Electrostatic discharge) sensitive device, observe handling precaution!**

Type	Marking	Pin Configuration						Package
BFS481	RFs	1=B	2=E	3=C	4=B	5=E	6=C	SOT363

**Maximum Ratings** at  $T_A = 25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{CEO}$	12	V
Collector-emitter voltage	$V_{CES}$	20	
Collector-base voltage	$V_{CBO}$	20	
Emitter-base voltage	$V_{EBO}$	2	
Collector current	$I_C$	20	mA
Base current	$I_B$	2	
Total power dissipation <sup>1)</sup> $T_S \leq 83^\circ\text{C}$	$P_{\text{tot}}$	175	mW
Junction temperature	$T_J$	150	$^\circ\text{C}$
Storage temperature	$T_{\text{Stg}}$	-55 ... 150	

**Thermal Resistance**

Parameter	Symbol	Value	Unit
Junction - soldering point <sup>2)</sup>	$R_{\text{thJS}}$	380	K/W

**Electrical Characteristics** at  $T_A = 25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

**DC Characteristics**

Collector-emitter breakdown voltage $I_C = 1 \text{ mA}, I_B = 0$	$V_{(\text{BR})\text{CEO}}$	12	-	-	V
Collector-emitter cutoff current $V_{CE} = 20 \text{ V}, V_{BE} = 0$	$I_{CES}$	-	-	100	$\mu\text{A}$
Collector-base cutoff current $V_{CB} = 10 \text{ V}, I_E = 0$	$I_{CBO}$	-	-	100	nA
Emitter-base cutoff current $V_{EB} = 1 \text{ V}, I_C = 0$	$I_{EBO}$	-	-	1	$\mu\text{A}$
DC current gain $I_C = 5 \text{ mA}, V_{CE} = 8 \text{ V}, \text{pulse measured}$	$h_{\text{FE}}$	70	100	140	-

<sup>1)</sup> $T_S$  is measured on the collector lead at the soldering point of the pcb

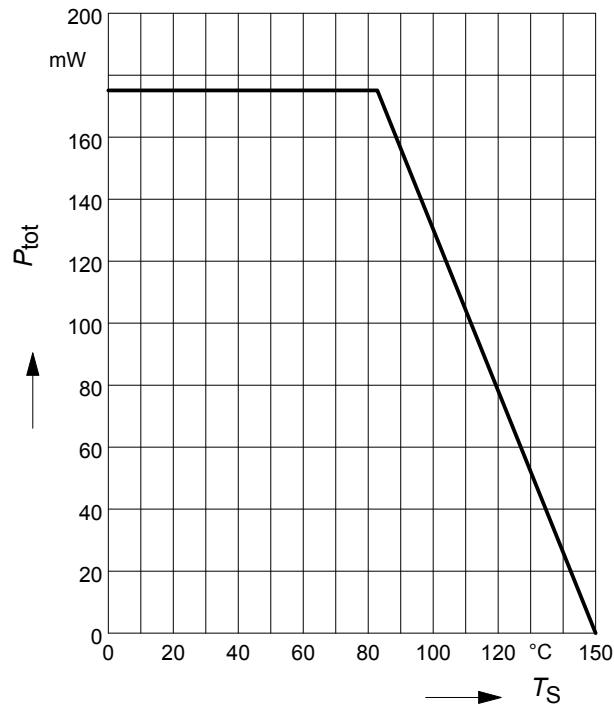
<sup>2)</sup>For the definition of  $R_{\text{thJS}}$  please refer to Application Note AN077 (Thermal Resistance Calculation)

**Electrical Characteristics** at  $T_A = 25^\circ\text{C}$ , unless otherwise specified

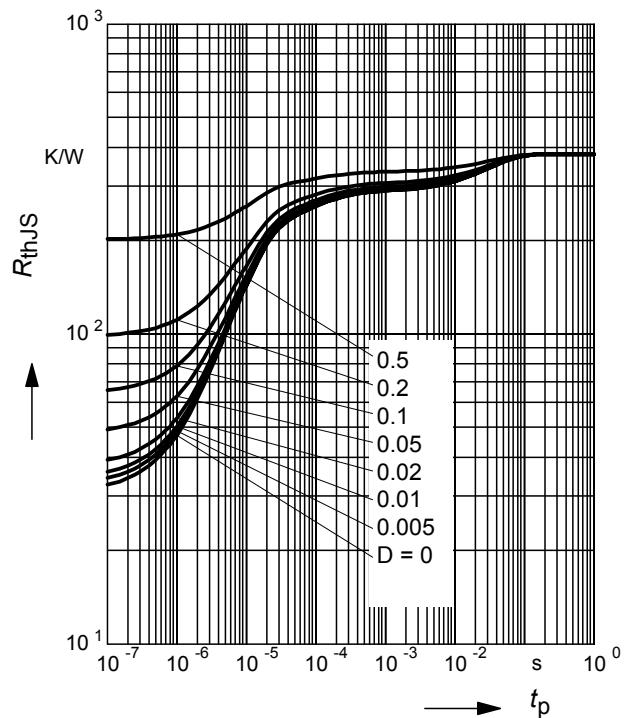
Parameter	Symbol	Values			Unit
		min.	typ.	max.	
<b>AC Characteristics</b> (verified by random sampling)					
Transition frequency $I_C = 10 \text{ mA}, V_{CE} = 8 \text{ V}, f = 500 \text{ MHz}$	$f_T$	6	8	-	GHz
Collector-base capacitance $V_{CB} = 10 \text{ V}, f = 1 \text{ MHz}, V_{BE} = 0 \text{ V}$ , emitter grounded	$C_{cb}$	-	0.23	0.4	pF
Collector emitter capacitance $V_{CE} = 10 \text{ V}, f = 1 \text{ MHz}, V_{BE} = 0 \text{ V}$ , base grounded	$C_{ce}$	-	0.13	-	
Emitter-base capacitance $V_{EB} = 0.5 \text{ V}, f = 1 \text{ MHz}, V_{CB} = 0 \text{ V}$ , collector grounded	$C_{eb}$	-	0.4	-	
Minimum noise figure $I_C = 2 \text{ mA}, V_{CE} = 8 \text{ V}, Z_S = Z_{\text{Sopt}}$ , $f = 900 \text{ MHz}$ $I_C = 2 \text{ mA}, V_{CE} = 8 \text{ V}, Z_S = Z_{\text{Sopt}}$ , $f = 1.8 \text{ GHz}$	$NF_{\min}$	-	0.9	-	dB
Power gain, maximum stable <sup>1)</sup> $I_C = 5 \text{ mA}, V_{CE} = 8 \text{ V}, Z_S = Z_{\text{Sopt}}, Z_L = Z_{\text{Lopt}}$ , $f = 900 \text{ MHz}$	$G_{ms}$	-	20	-	dB
Power gain, maximum available <sup>2)</sup> $I_C = 5 \text{ mA}, V_{CE} = 8 \text{ V}, Z_S = Z_{\text{Sopt}}, Z_L = Z_{\text{Lopt}}$ , $f = 1.8 \text{ GHz}$	$G_{ma}$	-	15	-	dB
Transducer gain $I_C = 5 \text{ mA}, V_{CE} = 8 \text{ V}, Z_S = Z_L = 50 \Omega$ , $f = 900 \text{ MHz}$ $I_C = 5 \text{ mA}, V_{CE} = 8 \text{ V}, Z_S = Z_L = 50 \Omega$ , $f = 1.8 \text{ MHz}$	$ S_{21e} ^2$	-	16	-	dB
<sup>1</sup> $G_{ms} =  S_{21} / S_{12} $					
<sup>2</sup> $G_{ma} =  S_{21e} / S_{12e}  (k - (k^2 - 1)^{1/2})$					

<sup>1</sup> $G_{ms} = |S_{21} / S_{12}|$ 
<sup>2</sup> $G_{ma} = |S_{21e} / S_{12e}| (k - (k^2 - 1)^{1/2})$

**Total power dissipation  $P_{\text{tot}} = f(T_S)$**

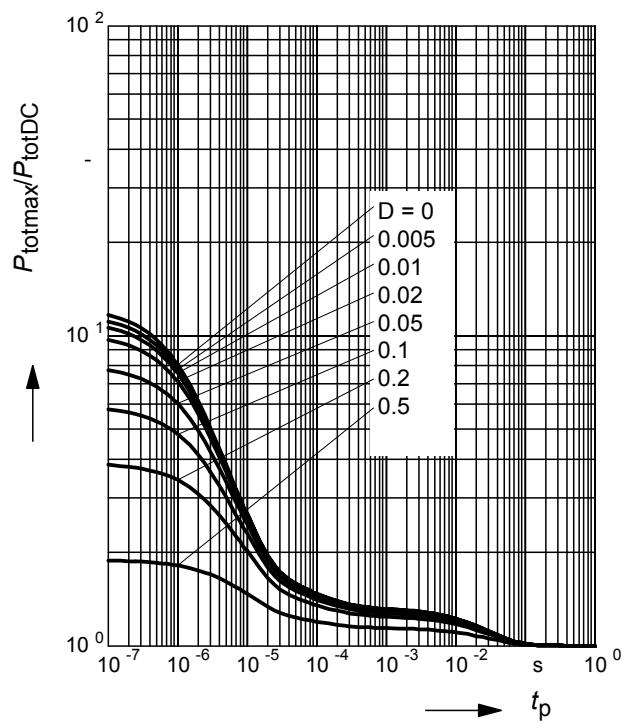


**Permissible Pulse Load  $R_{\text{thJS}} = f(t_p)$**

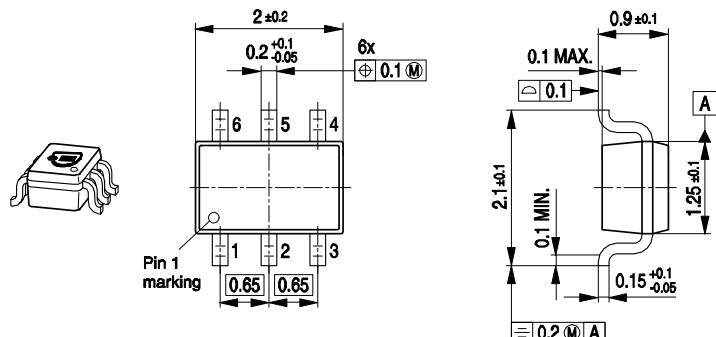


**Permissible Pulse Load**

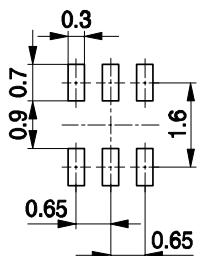
$P_{\text{totmax}}/P_{\text{totDC}} = f(t_p)$



## Package Outline

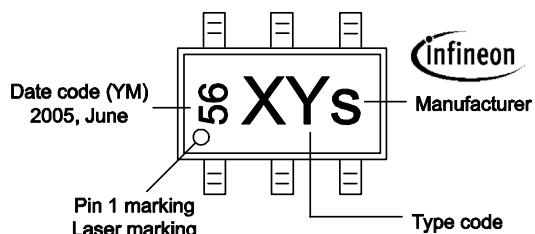


## Foot Print



## Marking Layout (Example)

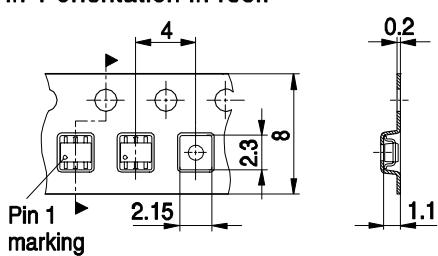
Small variations in positioning of Date code, Type code and Manufacture are possible.



## Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel  
Reel ø330 mm = 10.000 Pieces/Reel

For symmetric types no defined Pin 1 orientation in reel.



---

**Edition 2009-11-16**

**Published by**  
**Infineon Technologies AG**  
**81726 Munich, Germany**

**© 2009 Infineon Technologies AG**  
**All Rights Reserved.**

### **Legal Disclaimer**

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

### **Information**

For further information on technology, delivery terms and conditions and prices, please contact the nearest Infineon Technologies Office ([www.infineon.com](http://www.infineon.com)).

### **Warnings**

Due to technical requirements, components may contain dangerous substances. For information on the types in question, please contact the nearest Infineon Technologies Office.

Infineon Technologies components may be used in life-support devices or systems only with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.