

TN4033A



PNP General Purpose Amplifier

This device is designed for general purpose amplifier and switching applications at currents to 500 mA and collector voltages up to 70V. Sourced from Process 67.

Absolute Maximum Ratings*

TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CEO}	Collector-Emitter Voltage	80	V
V _{CBO}	Collector-Base Voltage	80	V
V _{EBO}	Emitter-Base Voltage	5.0	V
I _C	Collector Current - Continuous	1.0	A
T _J , T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:

- 1) These ratings are based on a maximum junction temperature of 150 degrees C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
- 3) All voltages (V) and currents (A) are negative polarity for PNP transistors.

Thermal Characteristics

TA = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units
		TN4033A	
P _D	Total Device Dissipation Derate above 25°C	1.0 8.0	W mW/°C
R _{θJC}	Thermal Resistance, Junction to Case	125	°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient	50	°C/W

PNP General Purpose Amplifier

(continued)

Electrical Characteristics

TA = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Max	Units
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OFF CHARACTERISTICS

$V_{(BR)CEO}$	Collector-Emitter Sustaining Voltage*	$I_C = 10 \text{ mA}, I_B = 0$	80		V
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage	$I_C = 10 \text{ } \mu\text{A}, I_E = 0$	80		V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E = 10 \text{ } \mu\text{A}, I_C = 0$	5.0		V
I_{CBO}	Collector-Cutoff Current	$V_{CB} = 60 \text{ V}, I_E = 0$ $V_{CB} = 60 \text{ V}, I_E = 0, T_A = 150^\circ\text{C}$		50 50	nA μA
I_{EBO}	Emitter-Cutoff Current	$V_{EB} = 5.0 \text{ V}, I_C = 0$		10	μA

ON CHARACTERISTICS

h_{FE}	DC Current Gain	$I_C = 100 \text{ } \mu\text{A}, V_{CE} = 5.0 \text{ V}$ $I_C = 100 \text{ mA}, V_{CE} = 5.0 \text{ V}, T_A = -55^\circ\text{C}$ $I_C = 100 \text{ mA}, V_{CE} = 5.0 \text{ V}$ $I_C = 500 \text{ mA}, V_{CE} = 5.0 \text{ V}$ $I_C = 1.0 \text{ A}, V_{CE} = 5.0 \text{ V}$	75 40 100 70 25	300	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = 150 \text{ mA}, I_B = 15 \text{ mA}$ $I_C = 500 \text{ mA}, I_B = 50 \text{ mA}$		0.15 0.5	V V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C = 150 \text{ mA}, I_B = 15 \text{ mA}$		0.9	V
$V_{BE(on)}$	Base-Emitter On Voltage	$I_C = 500 \text{ mA}, V_{CE} = 0.5 \text{ V}$		1.1	V

SMALL SIGNAL CHARACTERISTICS

C_{obo}	Output Capacitance	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1.0 \text{ MHz}$		20	pF
C_{ibo}	Input Capacitance	$V_{EB} = 0.5 \text{ V}, I_C = 0, f = 1.0 \text{ MHz}$		110	pF
h_{fe}	Small-Signal Current Gain	$I_C = 50 \text{ mA}, V_{CE} = 10 \text{ V},$ $f = 100 \text{ MHz}$	1.0	4.0	

SWITCHING CHARACTERISTICS

t_s	Storage Time	$I_C = 500 \text{ mA}, I_{B1} = I_{B2} = 50 \text{ mA}$		350	ns
t_{on}	Turn-On Time	$I_C = 500 \text{ mA}, I_{B1} = 50 \text{ mA}$		100	ns
t_f	Fall Time	$I_C = 500 \text{ mA}, I_{B1} = I_{B2} = 50 \text{ mA}$		50	ns

*Pulse Test: Pulse Width $\leq 300 \text{ } \mu\text{s}$, Duty Cycle $\leq 1.0\%$

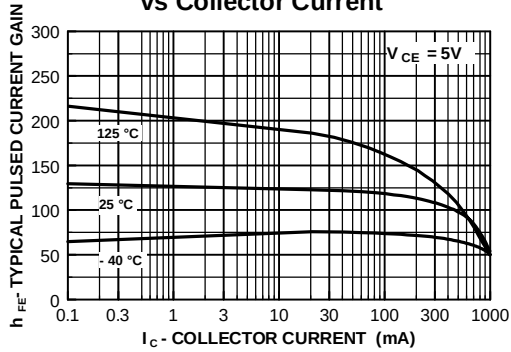
NOTE: All voltages (V) and currents (A) are negative polarity for PNP transistors.

PNP General Purpose Amplifier (continued)

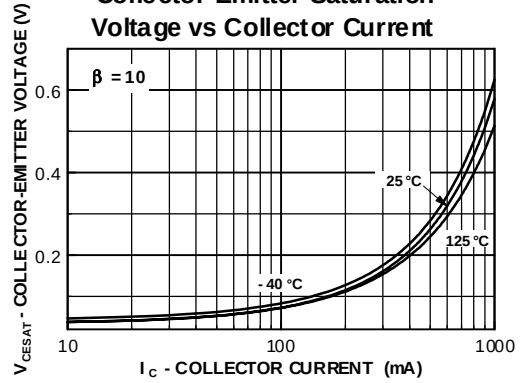
TN4033A

Typical Characteristics

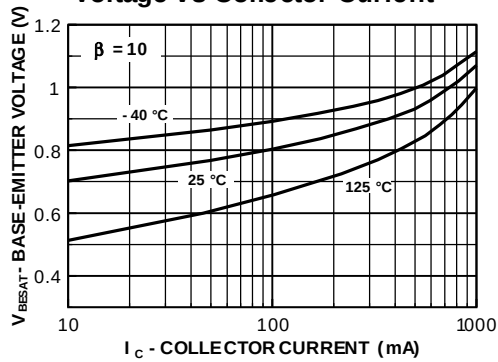
Typical Pulsed Current Gain
vs Collector Current



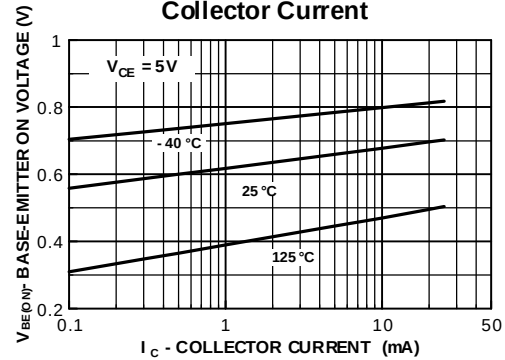
Collector-Emitter Saturation
Voltage vs Collector Current



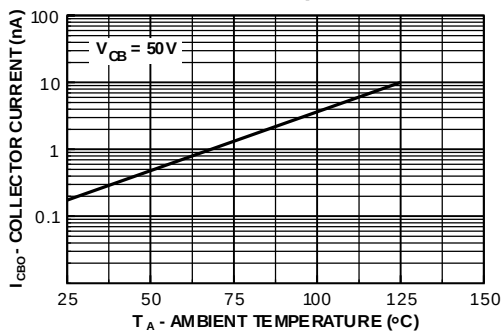
Base-Emitter Saturation
Voltage vs Collector Current



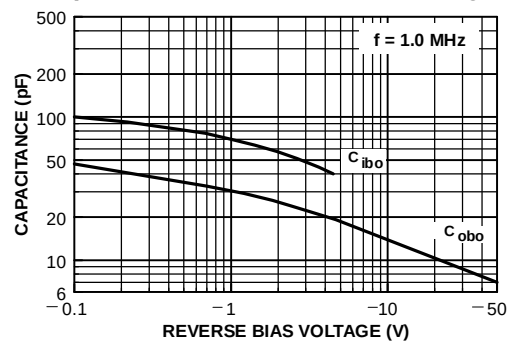
Base-Emitter ON Voltage vs
Collector Current



Collector-Cutoff Current
vs Ambient Temperature



Collector-Base and Emitter-Base
Capacitance vs Reverse Bias Voltage



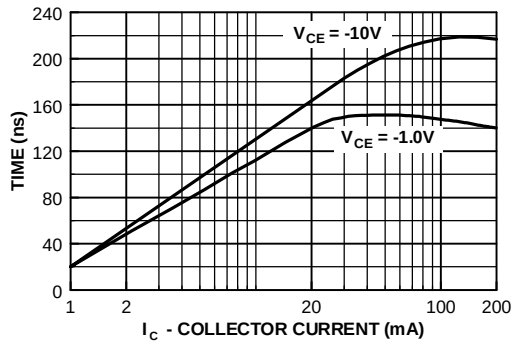
PNP General Purpose Amplifier

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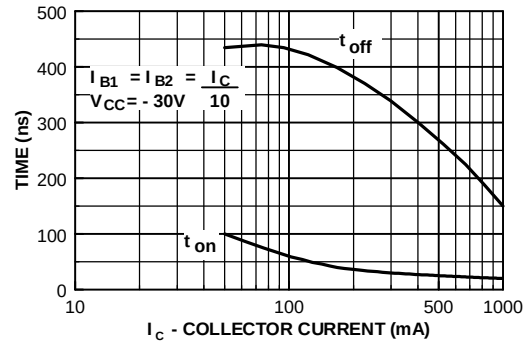
TN4033A

Typical Characteristics (continued)

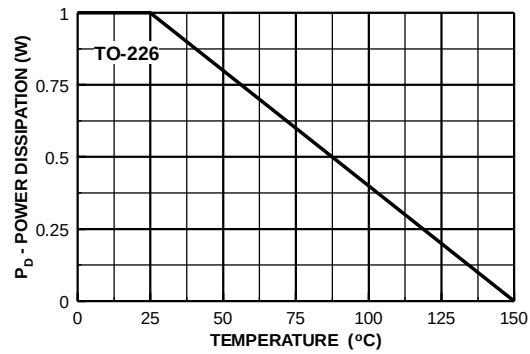
Switching Times vs
Collector Current



Turn On and Turn Off Times vs
Collector Current



Power Dissipation vs
Ambient Temperature

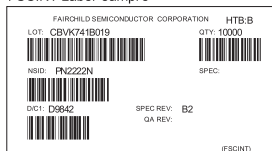


TO-226AE Tape and Reel Data



TO-226AE Packaging
Configuration: Figure 1.0

FSCINT Label sample

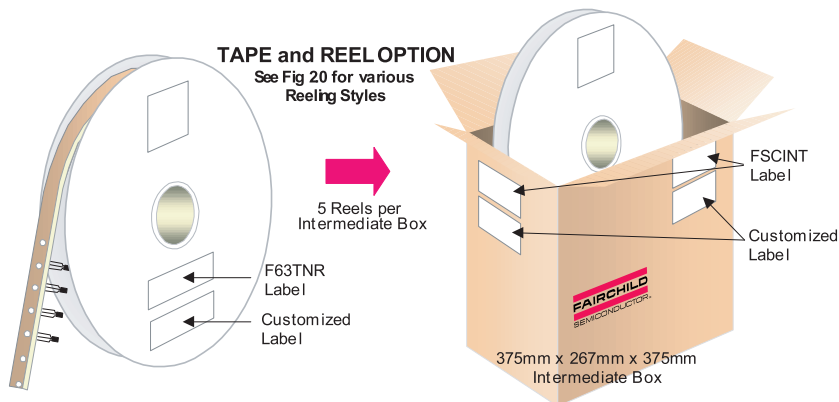


F63TNR Label sample



TAPE and REEL OPTION

See Fig 20 for various Reeling Styles



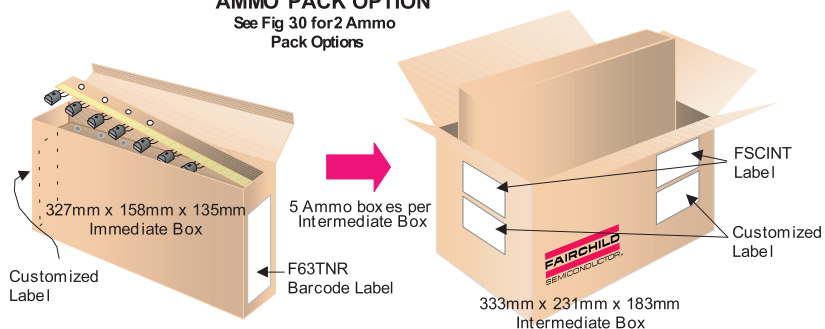
AMMO PACK OPTION

See Fig 30 for 2 Ammo Pack Options

TO-226AE TNR/AMMO PACKING INFORMATION

Packing	Style	Quantity	EOL code
Reel	A	2,000	D26Z
	E	2,000	D27Z
Ammo	M	2,000	D74Z
	P	2,000	D75Z

Unit weight = 0.300gm
Reel weight with components = 0.868 kg
Ammo weight with components = 0.880 kg
Max quantity per intermediate box = 10,000 units

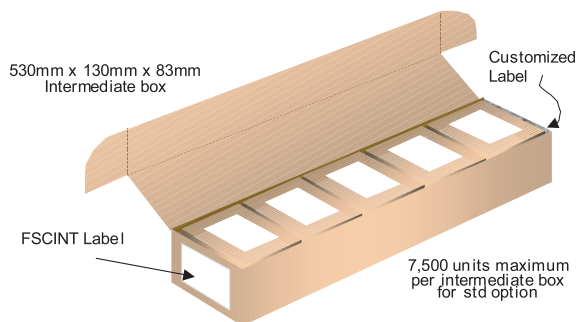
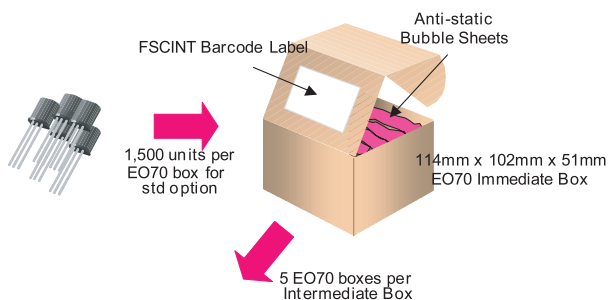


BULK OPTION

See Bulk Packing Information table

(TO-226AE) BULK PACKING INFORMATION

EOL CODE	DESCRIPTION	LEADCLIP DIMENSION	QUANTITY
J18Z	TO-18 OPTION STD	NO LEAD CLIP	1.0 K / BOX
J05Z	TO-5 OPTION STD	NO LEAD CLIP	1.0 K / BOX
NO EOL CODE	TO-226 STANDARD STRAIGHT	NO LEADCLIP	1.5 K / BOX

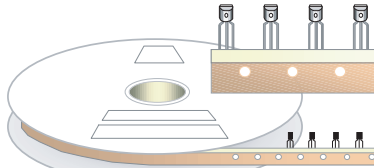


TO-226AE Tape and Reel Data, continued

TO-226AE Reeling Style

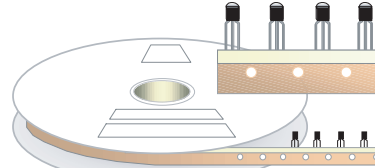
Configuration: Figure 2.0

Machine Option "A"(H)



Style "A" D26Z, D70Z (s/h)

Machine Option "E"(J)

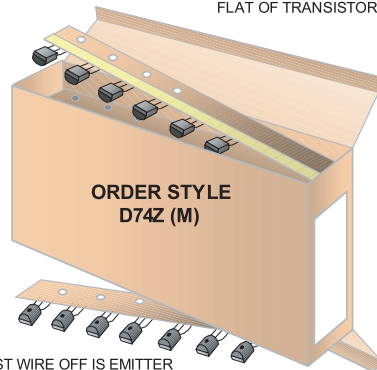


Style "E" D27Z, D71Z (s/h)

TO-226AE Radial Ammo Packaging

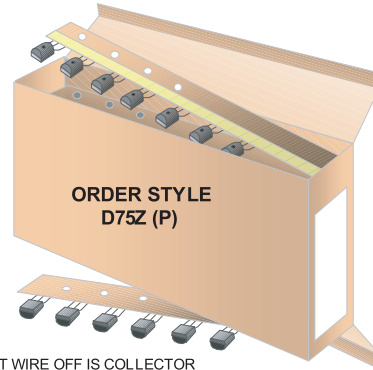
Configuration: Figure 3.0

FIRST WIRE OFF IS COLLECTOR (ON PKG. 92)
ADHESIVE TAPE IS ON THE TOP SIDE
FLAT OF TRANSISTOR IS ON TOP



FIRST WIRE OFF IS EMITTER
ADHESIVE TAPE IS ON BOTTOM SIDE
FLAT OF TRANSISTOR IS ON BOTTOM

FIRST WIRE OFF IS EMITTER (ON PKG. 92)
ADHESIVE TAPE IS ON THE TOP SIDE
FLAT OF TRANSISTOR IS ON BOTTOM

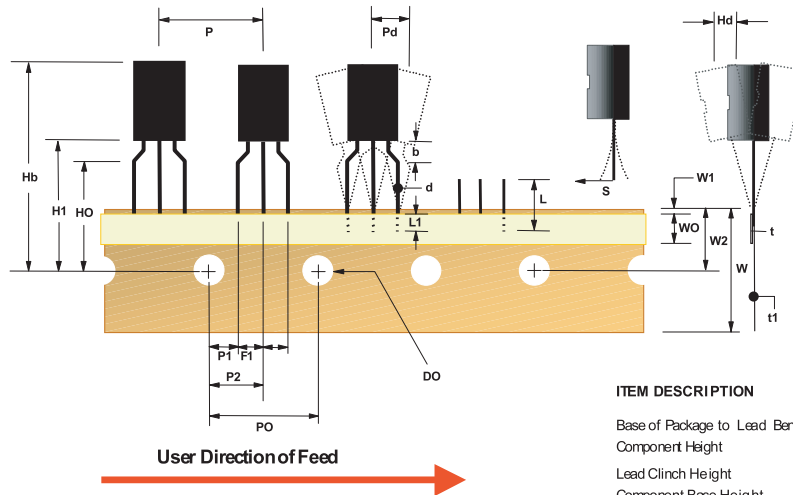


FIRST WIRE OFF IS COLLECTOR
ADHESIVE TAPE IS ON BOTTOM SIDE
FLAT OF TRANSISTOR IS ON TOP

TO-226AE Tape and Reel Data, continued

TO-226AE Tape and Reel Taping

Dimension Configuration: Figure 4.0

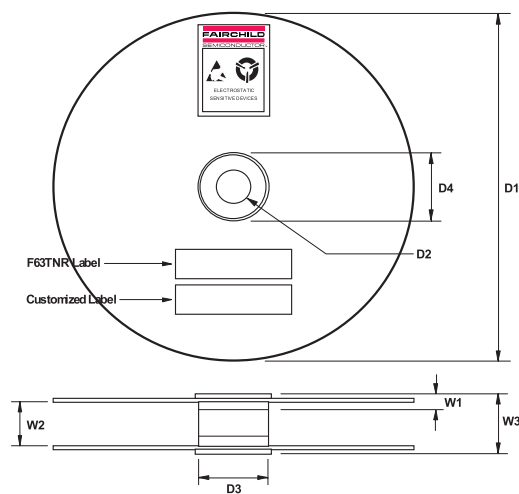


ITEM DESCRIPTION	SYMBOL	DIMENSION
Base of Package to Lead Bend	b	0.098 (max)
Component Height	Hb	1.078 (+/- 0.050)
Lead Clinch Height	HO	0.630 (+/- 0.020)
Component Base Height	H1	0.748 (+/- 0.020)
Component Alignment (side/side)	Pd	0.040 (max)
Component Alignment (front/back)	Hd	0.031 (max)
Component Pitch	P	0.500 (+/- 0.020)
Feed Hole Pitch	PO	0.500 (+/- 0.008)
Hole Center to First Lead	P1	0.150 (+0.009, -0.010)
Hole Center to Component Center	P2	0.247 (+/- 0.007)
Lead Spread	F1/F2	0.104 (+/- 0.010)
Lead Thickness	d	0.018 (+0.002, -0.003)
Out Lead Length	L	0.429 (max)
Taped Lead Length	L1	0.209 (+0.051, -0.052)
Taped Lead Thickness	t	0.032 (+/- 0.006)
Carrier Tape Thickness	t1	0.021 (+/- 0.006)
Carrier Tape Width	W	0.708 (+0.020, -0.019)
Hold - down Tape Width	W0	0.236 (+/- 0.012)
Hold - down Tape position	W1	0.035 (max)
Feed Hole Position	W2	0.360 (+/- 0.025)
Sprocket Hole Diameter	DO	0.157 (+0.008, -0.007)
Lead Spring Out	S	0.004 (max)

Note: All dimensions are in inches.

TO-226AE Reel

Configuration: Figure 5.0



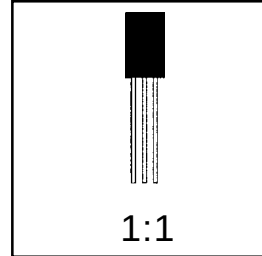
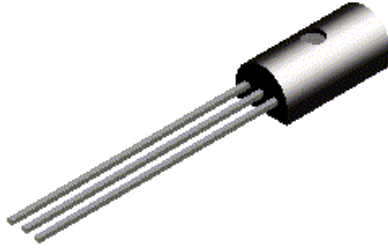
ITEM DESCRIPTION	SYMBOL	MINIMUM	MAXIMUM
Reel Diameter	D1	1.3975	14.025
Arbor Hole Diameter (Standard)	D2	1.160	1.200
(Small Hole)	D2	0.650	0.700
Core Diameter	D3	3.100	3.300
Hub Recess Inner Diameter	D4	2.700	3.100
Hub Recess Depth	W1	0.370	0.570
Flange to Flange Inner Width	W2	1.630	1.690
Hub to Hub Center Width	W3		2.090

Note: All dimensions are in inches

TO-226AE Package Dimensions



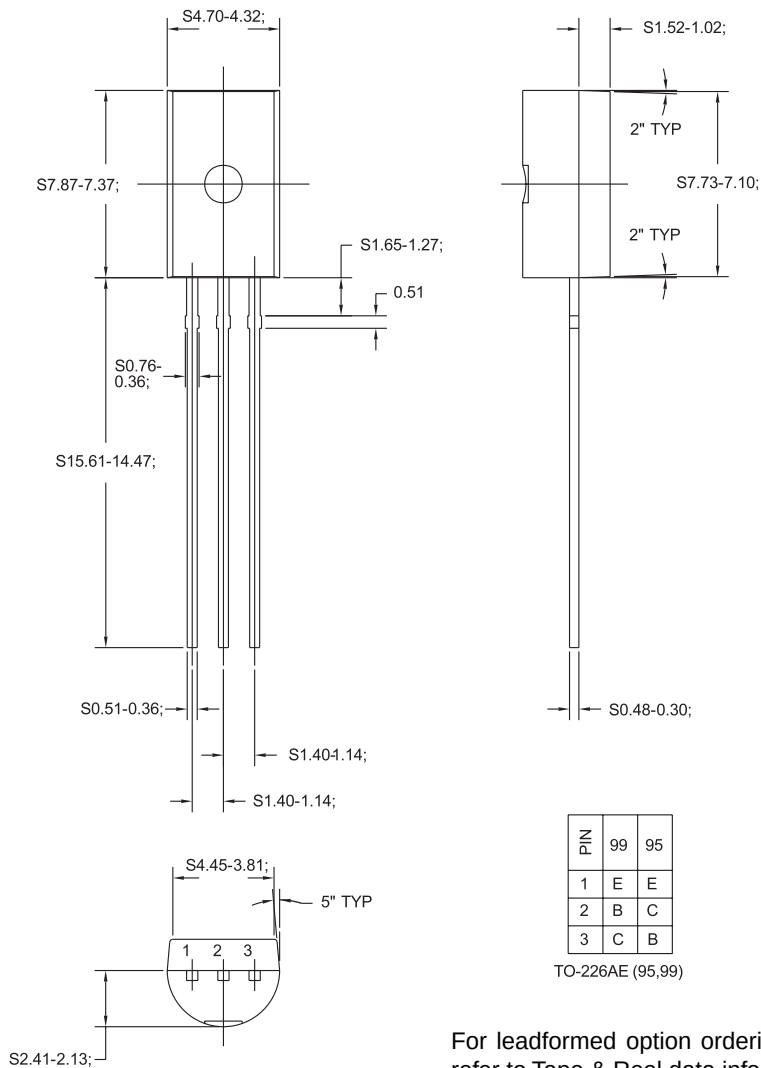
TO-226AE (FS PKG Code 95, 99)



Scale 1:1 on letter size paper

Dimensions shown below are in:
inches [millimeters]

Part Weight per unit (gram): 0.300



For leadformed option ordering,
refer to Tape & Reel data information.

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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