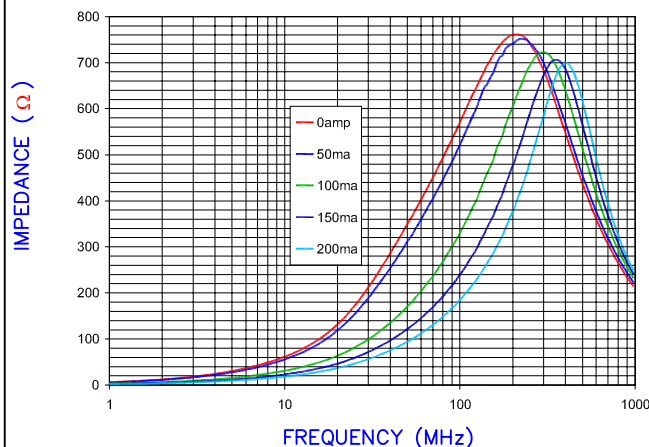


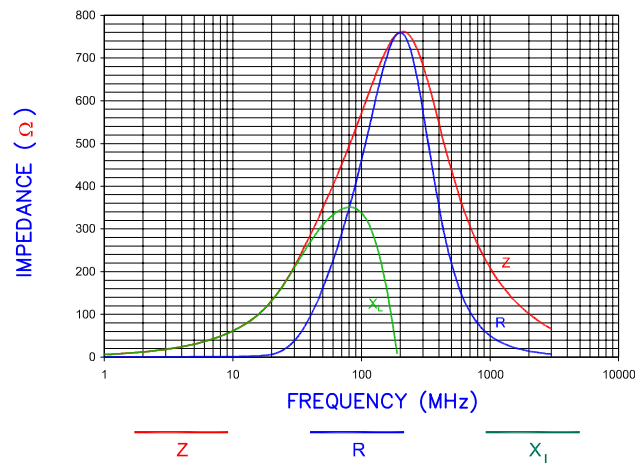
PHYSICAL DIMENSIONS:

A	3.20 [.126]	± 0.20 [.008]
B	1.60 [.063]	± 0.20 [.008]
C	0.80 [.031]	± 0.20 [.008]
D	0.40 [.016]	± 0.15 [.006]
D1	0.30 [.012]	± 0.20 [.008]
E	0.80 [.031]	± 0.10 [.004]

Z vs FREQUENCY
IMPEDANCE UNDER DC BIAS



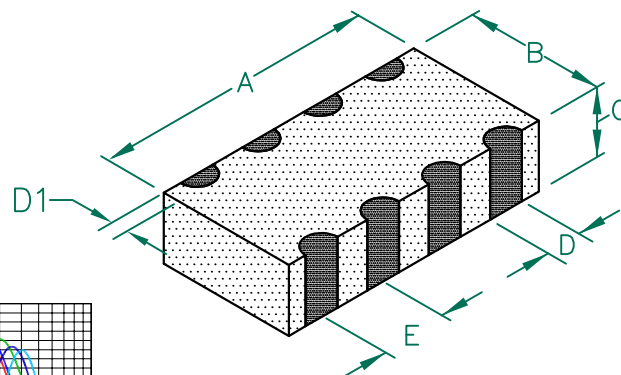
|Z|, R, AND X vs. FREQUENCY



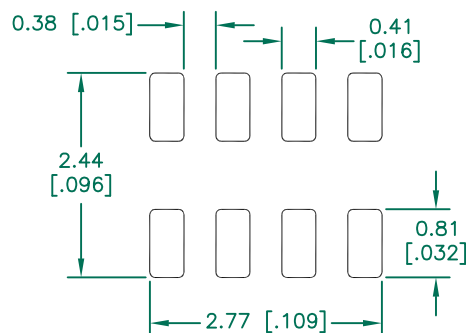
AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 3236

DA1206B601R-10

**UNCONTROLLED
DOCUMENT**



LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762
(0.030) to this dimension)

ELECTRICAL CHARACTERISTICS:

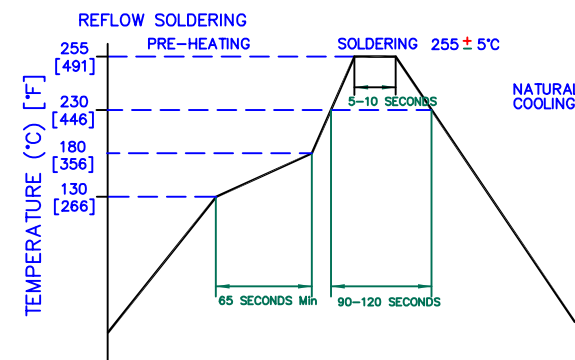
Z @ 100MHz (Ω)	DCR (Ω)	Rated Current
Nominal	600	
Minimum	450	
Maximum	750	200 mA

LINE TO LINE INSULATION RESISTANCE
>100 M Ω AT 75 VOLTS.

NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 3000 PCS/REEL EMBOSSED PLASTIC TAPE.
2. TERMINATION FINISH IS 100% TIN.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. OPERATING TEMPERATURE TEMP: $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$ (INCLUDING SELF-HEATING)

RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES].

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E	OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	PROJECT/PART NUMBER: DA1206B601R-10	
D	UPDATE COMPANY LOGO	05/20/09	JRK	REV	PART TYPE:
C	D1 dim chgd from 0.008 $\pm$ 0.004 to 0.012 $\pm$ 0.008. UPDATE COMPANY LOGO	10/30/07	JRK	E	CO-FIRE
B	CHANGE DCR, MODIFY NOTE 1 ADD ROHS	11/30/06	JRK	DATE:	DRAWN BY:
A	ORIGINAL DRAFT	03/30/04	TMB	03/30/04	TMB
REV	DESCRIPTION	DATE	INT	SCALE: NTS	SHEET:
CAD # DA1206B601R-10-E				TOOL # -	2 of 2

