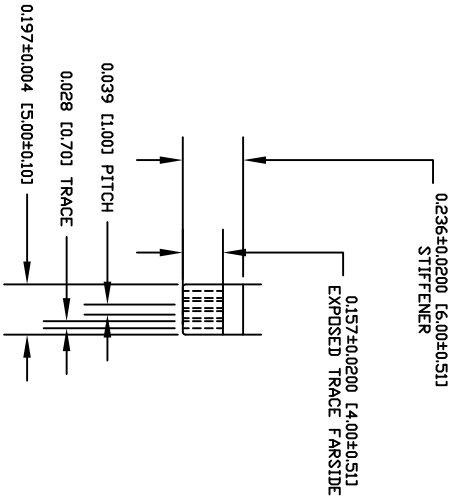


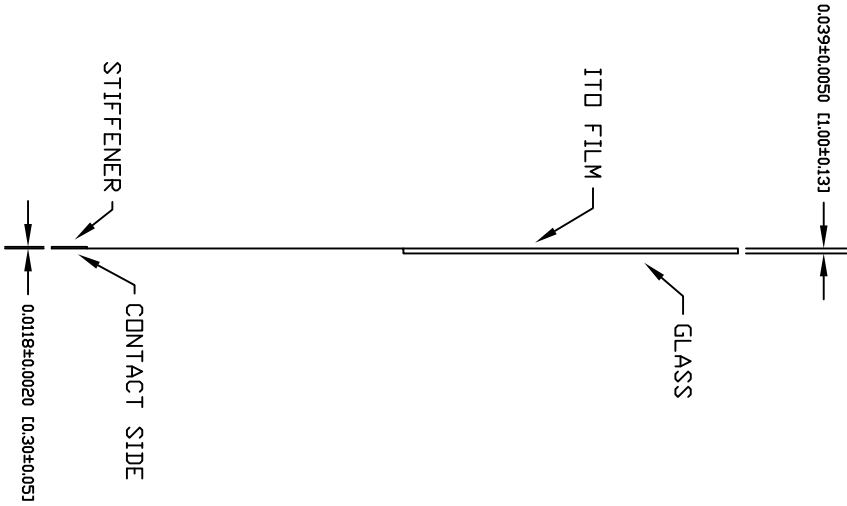
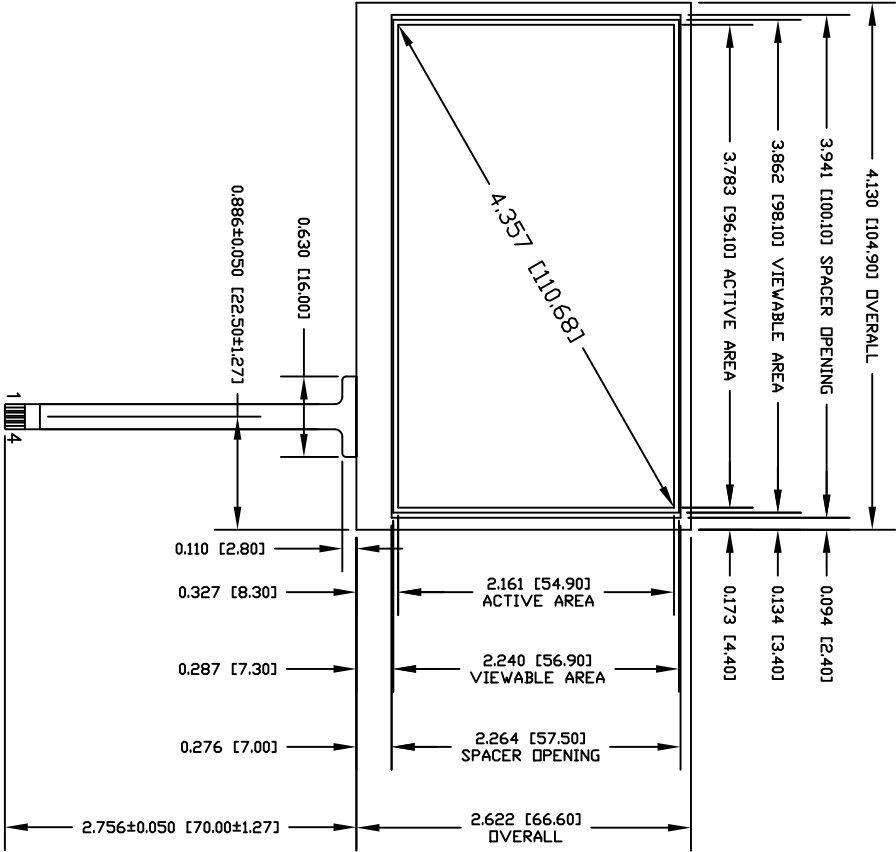
REV	INITIALS	DATE	DESCRIPTION
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DETAIL A
SCALE 2.0



PIN DIAGRAM



- NOTES:
1. TOLERANCE ±0.020 UNLESS NOTED OTHERWISE.
 2. TOP CONDUCTIVE LAYER TO HAVE AN CLEAR HARDCOATED SURFACE..
 3. PIN ONE IS DESIGNATED BY THE NUMERICAL NUMBER 1 BY THE CONNECTOR END.
 4. PINOUT TO BE AS INDICATED.

- PIN 1 - LEFT.
PIN 2 - BOTTOM.
PIN 3 - RIGHT.
PIN 4 - TOP.
5. TAIL TYPE: IMMERSION GOLD PLATED PFC, ZIF

BERGQUIST TOUGH PRODUCTS				
301 WASHINGTON STREET CANNON FALLS, MN 55605				
TITLE: 4.36 DIAGONAL 4-WIRE				
DRN: MLR	DATE: 6/4/08	TOL: ±0.020	DWG # 9551	
CHKD:	DATE:		PART # 400437	REV
APVD:	DATE:			