

PMEG2010AEK

1 A very low V_F MEGA Schottky barrier rectifier

Rev. 01 — 30 March 2007

Product data sheet

1. Product profile

1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOT346 (SC-59A/TO-236) small Surface-Mounted Device (SMD) plastic package.

1.2 Features

- Forward current: $I_F \leq 1$ A
- Reverse voltage: $V_R \leq 20$ V
- Very low forward voltage
- Small SMD plastic package

1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications

1.4 Quick reference data

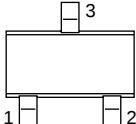
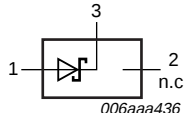
Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I_F	forward current	$T_{sp} \leq 55$ °C	-	-	1	A
V_R	reverse voltage		-	-	20	V
V_F	forward voltage	$I_F = 1$ A	^[1] -	400	450	mV

[1] Pulse test: $t_p \leq 300$ μ s; $\delta \leq 0.02$.

2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Symbol
1	anode		
2	not connected		
3	cathode		

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG2010AEK	SC-59A	plastic surface-mounted package; 3 leads	SOT346

4. Marking

Table 4. Marking codes

Type number	Marking code
PMEG2010AEK	H1

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_R	reverse voltage		-	20	V
I_F	forward current	$T_{sp} \leq 55\text{ °C}$	-	1	A
I_{FRM}	repetitive peak forward current	$t_p \leq 1\text{ ms}$; $\delta \leq 0.25$	-	3	A
I_{FSM}	non-repetitive peak forward current	square wave; $t_p = 8\text{ ms}$	-	4.5	A
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ °C}$	[1] -	250	mW
			[2] -	350	mW
T_j	junction temperature		-	150	°C
T_{amb}	ambient temperature		-65	+150	°C
T_{stg}	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]			
			[2]	-	-	500 K/W
			[3]	-	-	360 K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[4]	-	-	150 K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[4] Soldering point of cathode tab.

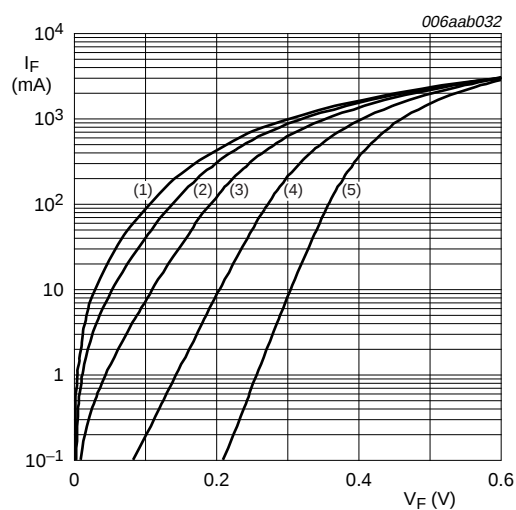
7. Characteristics

Table 7. Characteristics

$T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified.

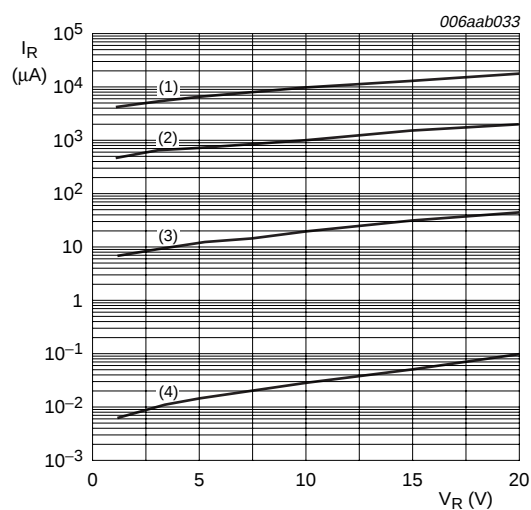
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_F	forward voltage		[1]			
		$I_F = 10\text{ mA}$	-	200	220	mV
		$I_F = 100\text{ mA}$	-	265	290	mV
		$I_F = 1\text{ A}$	-	400	450	mV
I_R	reverse current	$V_R = 5\text{ V}$	-	15	20	μA
		$V_R = 10\text{ V}$	-	20	80	μA
		$V_R = 20\text{ V}$	-	50	200	μA
C_d	diode capacitance	$V_R = 5\text{ V}; f = 1\text{ MHz}$	-	55	70	pF

[1] Pulse test: $t_p \leq 300\text{ }\mu\text{s}$; $\delta \leq 0.02$.



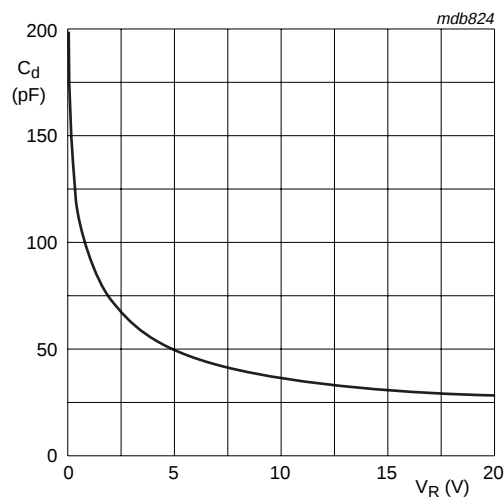
- (1) $T_{amb} = 150\text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 125\text{ }^{\circ}\text{C}$
- (3) $T_{amb} = 85\text{ }^{\circ}\text{C}$
- (4) $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (5) $T_{amb} = -40\text{ }^{\circ}\text{C}$

Fig 1. Forward current as a function of forward voltage; typical values



- (1) $T_{amb} = 125\text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 85\text{ }^{\circ}\text{C}$
- (3) $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (4) $T_{amb} = -40\text{ }^{\circ}\text{C}$

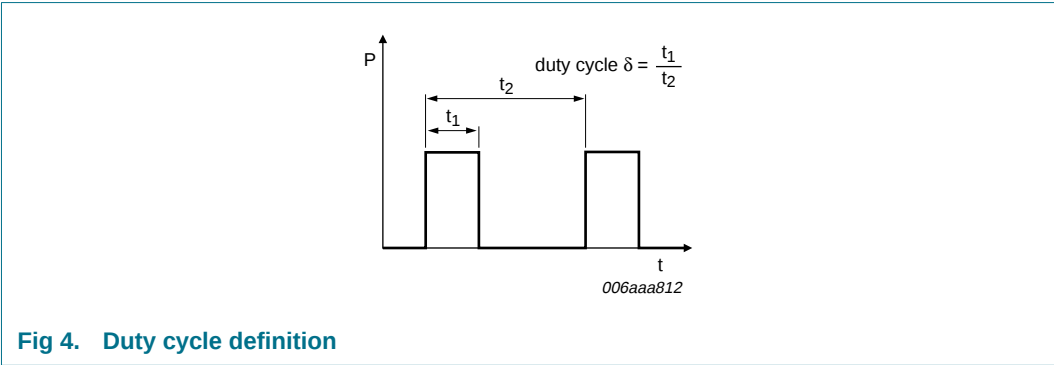
Fig 2. Reverse current as a function of reverse voltage; typical values



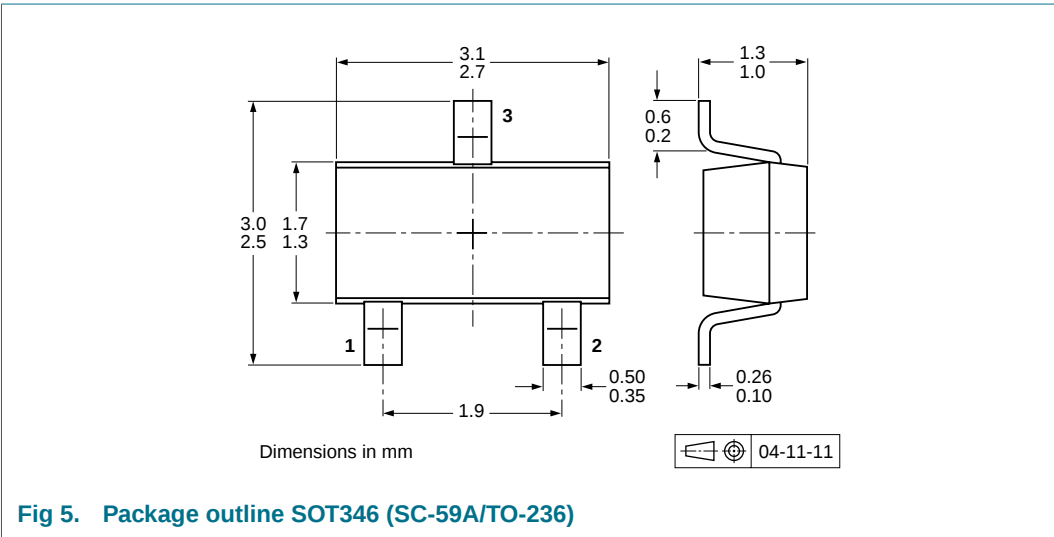
$f = 1\text{ MHz}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$

Fig 3. Diode capacitance as a function of reverse voltage; typical values

8. Test information



9. Package outline

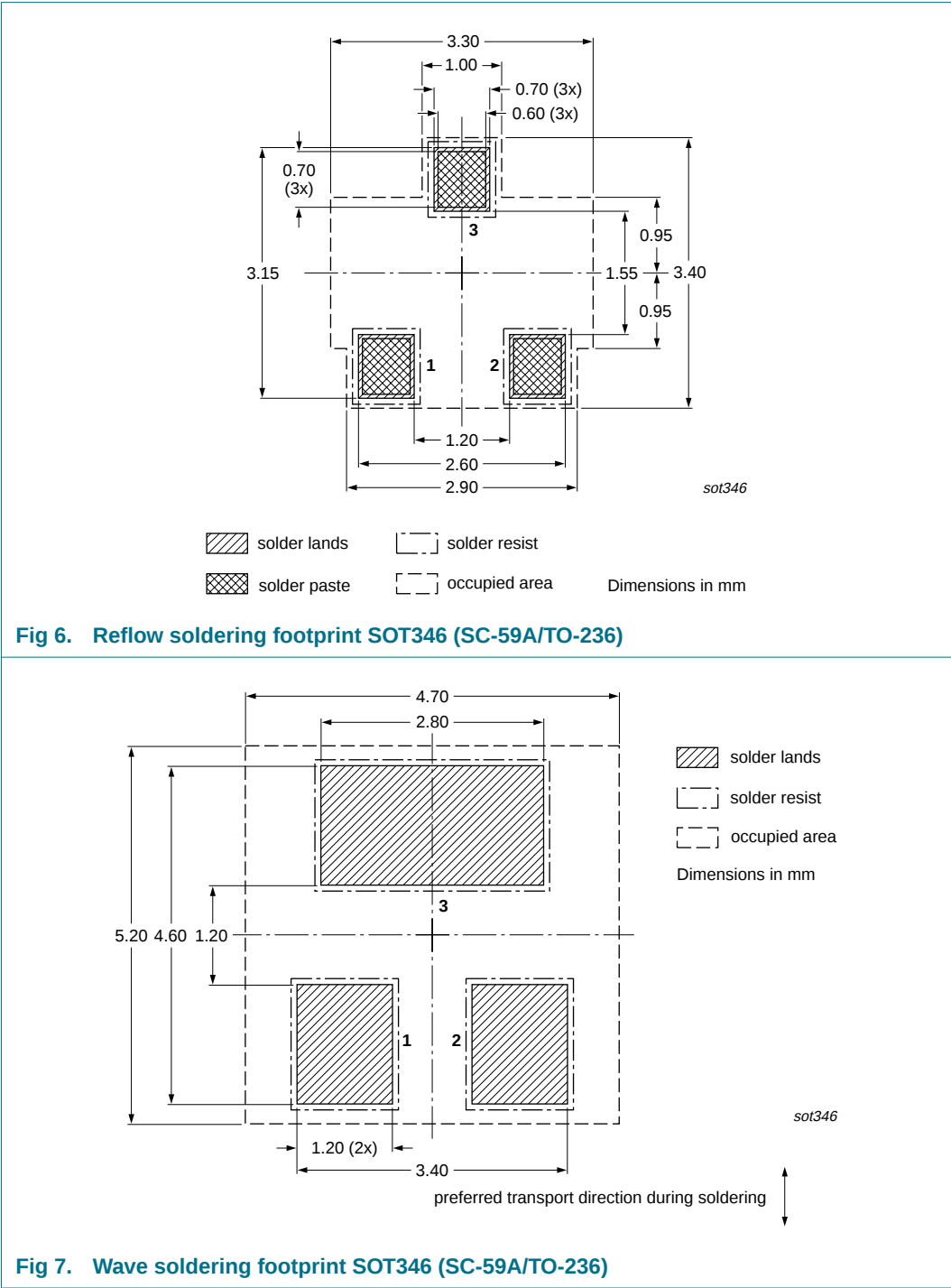


10. Packing information

Type number	Package	Description	Packing quantity	
			3000	10000
PMEG2010AEK	SOT346	4 mm pitch, 8 mm tape and reel	-115	-135

[1] For further information and the availability of packing methods, see [Section 14](#).

11. Soldering



12. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG2010AEK_1	20070330	Product data sheet	-	-

13. Legal information

13.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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15. Contents

1 Product profile 1

1.1 General description..... 1

1.2 Features 1

1.3 Applications 1

1.4 Quick reference data..... 1

2 Pinning information..... 2

3 Ordering information..... 2

4 Marking..... 2

5 Limiting values..... 2

6 Thermal characteristics..... 3

7 Characteristics..... 3

8 Test information..... 5

9 Package outline 5

10 Packing information..... 5

11 Soldering 6

12 Revision history..... 7

13 Legal information..... 8

13.1 Data sheet status 8

13.2 Definitions..... 8

13.3 Disclaimers..... 8

13.4 Trademarks..... 8

14 Contact information..... 8

15 Contents 9



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