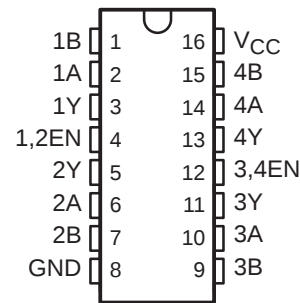


# SN75ALS175 QUADRUPLE DIFFERENTIAL LINE RECEIVER

SLLS131C – SEPTEMBER 1991 – REVISED MAY 1995

- Meets or Exceeds the Requirements of ANSI EIA/TIA-422-B, EIA/TIA-423-B, and RS-485
- Meets ITU Recommendations V.10, V.11, X.26, and X.27
- Designed for Multipoint Bus Transmission on Long Bus Lines in Noisy Environments
- Low Supply Current Requirement  
27 mA Max
- Common-Mode Input Voltage Range of  
–12 V to 12 V
- Input Sensitivity . . .  $\pm 200$  mV
- Input Hysteresis . . . 50 mV Typ
- High Input Impedance . . . 12 k $\Omega$  Min
- Operates From Single 5-V Supply

N OR NS<sup>†</sup> PACKAGE  
(TOP VIEW)



<sup>†</sup> The NS package is only available left-ended taped and reeled (order device SN75ALS175NSLE).

## description

The SN75ALS175 is a monolithic quadruple differential line receiver with 3-state outputs. It is designed to meet the requirements of ANSI Standards EIA/TIA-422-B, EIA/TIA-423-B, and RS-485 and several ITU recommendations. Advanced low-power Schottky technology provides high speed without the usual power penalty. Each of the two pairs of receivers has a common active-high enable. The device features high input impedance, input hysteresis for increased noise immunity, and input sensitivity of  $\pm 200$  mV over a common-mode input voltage range of –12 V to 12 V.

The SN75ALS175 is characterized for operation from 0°C to 70°C.

FUNCTION TABLE  
(each receiver)

| DIFFERENTIAL INPUTS<br>A – B | ENABLES<br>EN | OUTPUT<br>Y |
|------------------------------|---------------|-------------|
| $V_{ID} \geq 0.2$ V          | H             | H           |
| $-0.2$ V $< V_{ID} < 0.2$ V  | H             | ?           |
| $V_{ID} \leq -0.2$ V         | H             | L           |
| X                            | L             | Z           |
| Open Circuit                 | H             | H           |

H = high level, L = low level, ? = indeterminate,  
X = irrelevant, Z = high impedance (off)



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

 **TEXAS  
INSTRUMENTS**

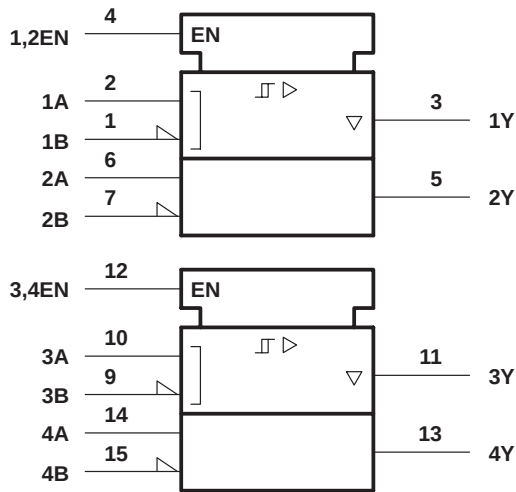
POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

Copyright © 1995, Texas Instruments Incorporated

# SN75ALS175 QUADRUPLE DIFFERENTIAL LINE RECEIVER

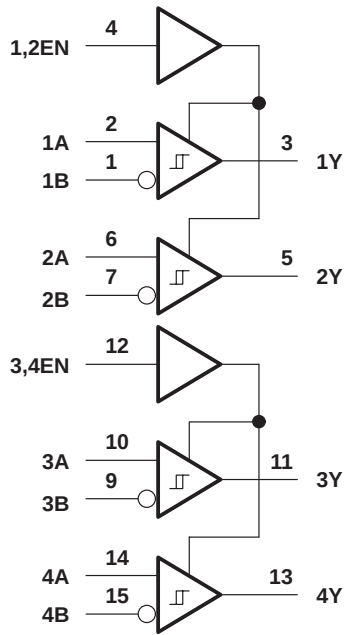
SLLS131C – SEPTEMBER 1991 – REVISED MAY 1995

logic symbol†

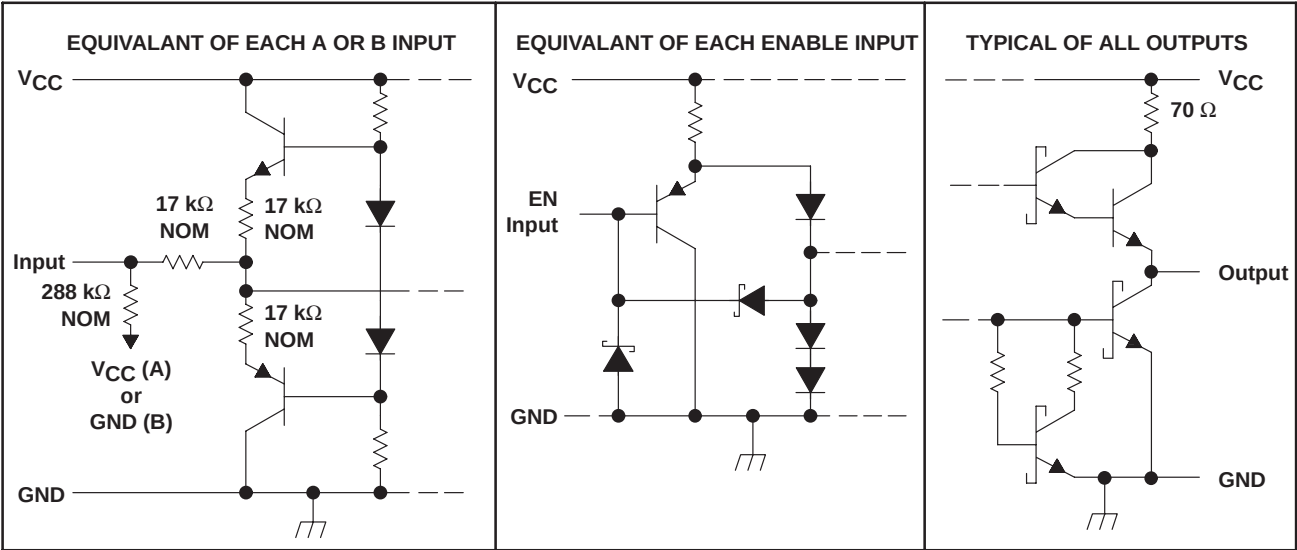


† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

logic diagram (positive logic)



schematics of inputs and outputs



**absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>**

|                                                              |                              |
|--------------------------------------------------------------|------------------------------|
| Supply voltage, $V_{CC}$ (see Note 1)                        | 7 V                          |
| Input voltage, $V_I$ (A or B inputs)                         | $\pm 14$ V                   |
| Differential input voltage, $V_{ID}$ (see Note 2)            | $\pm 14$ V                   |
| Enable input voltage, $V_I$                                  | 7 V                          |
| Low-level output current, $I_{OL}$                           | 50 mA                        |
| Continuous total dissipation                                 | See Dissipation Rating Table |
| Operating free-air temperature range, $T_A$                  | 0°C to 70°C                  |
| Storage temperature range, $T_{stg}$                         | –65°C to 150°C               |
| Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds | 260°C                        |

<sup>†</sup> Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltage values, except differential input voltage, are with respect to network ground terminal.  
2. Differential-input voltage is measured at the noninverting input with respect to the corresponding inverting input.

**DISSIPATION RATING TABLE**

| PACKAGE | $T_A \leq 25^\circ\text{C}$<br>POWER RATING | DERATING FACTOR<br>ABOVE $T_A = 25^\circ\text{C}$ | $T_A = 70^\circ\text{C}$<br>POWER RATING |
|---------|---------------------------------------------|---------------------------------------------------|------------------------------------------|
| N       | 1150 mW                                     | 9.2 mW/°C                                         | 736 mW                                   |
| NS      | 625 mW                                      | 5.0 mW/°C                                         | 400 mW                                   |

**recommended operating conditions**

|                                           | MIN  | NOM | MAX      | UNIT |
|-------------------------------------------|------|-----|----------|------|
| Supply voltage, $V_{CC}$                  | 4.75 | 5   | 5.25     | V    |
| Common-mode input voltage, $V_{IC}$       |      |     | $\pm 12$ | V    |
| Differential input voltage, $V_{ID}$      |      |     | $\pm 12$ | V    |
| High-level enable-input voltage, $V_{IH}$ | 2    |     |          | V    |
| Low-level enable-input voltage, $V_{IL}$  |      |     | 0.8      | V    |
| High-level output current, $I_{OH}$       |      |     | –400     | μA   |
| Low-level output current, $I_{OL}$        |      |     | 8        | mA   |
| Operating free-air temperature, $T_A$     | 0    |     | 70       | °C   |

# SN75ALS175

## QUADRUPLE DIFFERENTIAL LINE RECEIVER

SLLS131C – SEPTEMBER 1991 – REVISED MAY 1995

**electrical characteristics over recommended ranges of common-mode input voltage, supply voltage and operating free-air temperature (unless otherwise noted) (see Note 3)**

| PARAMETER                                            | TEST CONDITIONS                                          | MIN               | TYP <sup>†</sup> | MAX          | UNIT       |
|------------------------------------------------------|----------------------------------------------------------|-------------------|------------------|--------------|------------|
| $V_{IT+}$ Positive-going input threshold voltage     |                                                          |                   |                  | 200          | mV         |
| $V_{IT-}$ Negative-going input threshold voltage     |                                                          | -200 <sup>‡</sup> |                  |              | mV         |
| $V_{hys}$ Hysteresis voltage ( $V_{IT+} - V_{IT-}$ ) |                                                          |                   | 50               |              | mV         |
| $V_{IK}$ Enable-input clamp voltage                  | $I_I = -18$ mA                                           |                   |                  | -1.5         | V          |
| $V_{OH}$ High-level output voltage                   | $V_{ID} = 200$ mV, $I_{OH} = -400$ $\mu$ A, See Figure 1 | 2.7               |                  |              | V          |
| $V_{OL}$ Low-level output voltage                    | $V_{ID} = -200$ mV, $I_{OL} = 8$ mA, See Figure 1        |                   |                  | 0.45         | V          |
| $I_{OZ}$ High-impedance-state output current         | $V_O = 0.4$ V to 2.4 V                                   |                   |                  | $\pm 20$     | $\mu$ A    |
| $I_I$ Line input current                             | Other input at 0 V, See Note 3                           |                   |                  | $V_I = 12$ V | 1          |
|                                                      |                                                          |                   |                  | $V_I = -7$ V | -0.8       |
| $I_{IH}$ High-level enable-input current             | $V_{IH}(E) = 2.7$ V                                      |                   |                  | 20           | $\mu$ A    |
| $I_{IL}$ Low-level enable-input current              | $V_{IL}(E) = 0.4$ V                                      |                   |                  | -100         | $\mu$ A    |
| $r_i$ Input resistance                               |                                                          | 12                |                  |              | k $\Omega$ |
| $I_{OS}$ Short-circuit output current                | $V_O = 0$                                                | -15               |                  | -85          | mA         |
| $I_{CC}$ Supply current (total package)              | No load, Outputs enabled                                 |                   | 16               | 24           | mA         |
|                                                      | No load, Outputs disabled                                |                   | 18               | 27           |            |

<sup>†</sup> All typical values are at  $V_{CC} = 5$  V,  $T_A = 25^\circ\text{C}$ .

<sup>‡</sup> The algebraic convention, in which the less positive (more negative) limit is designated as minimum, is used in this data sheet for threshold voltage levels only.

NOTE 3: Refer to ANSI Standard RS-485 for exact conditions.

### switching characteristics, $V_{CC} = 5$ V, $T_A = 25^\circ\text{C}$

| PARAMETER                                                   | TEST CONDITIONS             | MIN | TYP <sup>†</sup> | MAX | UNIT |
|-------------------------------------------------------------|-----------------------------|-----|------------------|-----|------|
| $t_{PHL}$ Propagation delay time, high- to low-level output | $V_{ID} = -2.5$ V to 2.5 V, | 9   | 18               | 27  | ns   |
| $t_{PLH}$ Propagation delay time, low- to high-level output | $C_L = 15$ pF, See Figure 2 | 9   | 18               | 27  | ns   |
| $t_{PZH}$ Output enable time to high level                  | $C_L = 15$ pF, See Figure 3 | 4   | 12               | 18  | ns   |
| $t_{PZL}$ Output enable time to low level                   |                             | 6   | 13               | 21  | ns   |
| $t_{PHZ}$ Output disable time from high level               | $C_L = 15$ pF, See Figure 3 | 10  | 21               | 27  | ns   |
| $t_{PLZ}$ Output disable time from low level                |                             | 8   | 15               | 25  | ns   |

<sup>†</sup> All typical values are at  $V_{CC} = 5$  V,  $T_A = 25^\circ\text{C}$ .

## PARAMETER MEASUREMENT INFORMATION

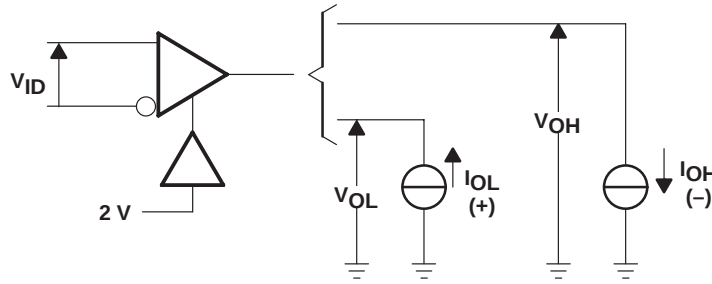
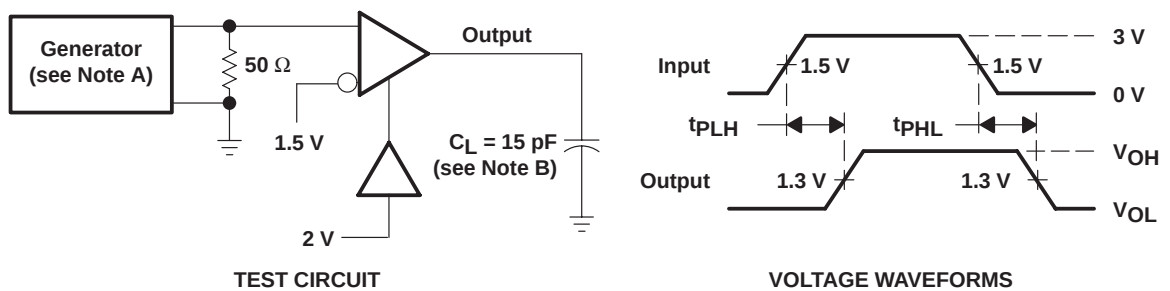


Figure 1.  $V_{OH}$ ,  $V_{OL}$



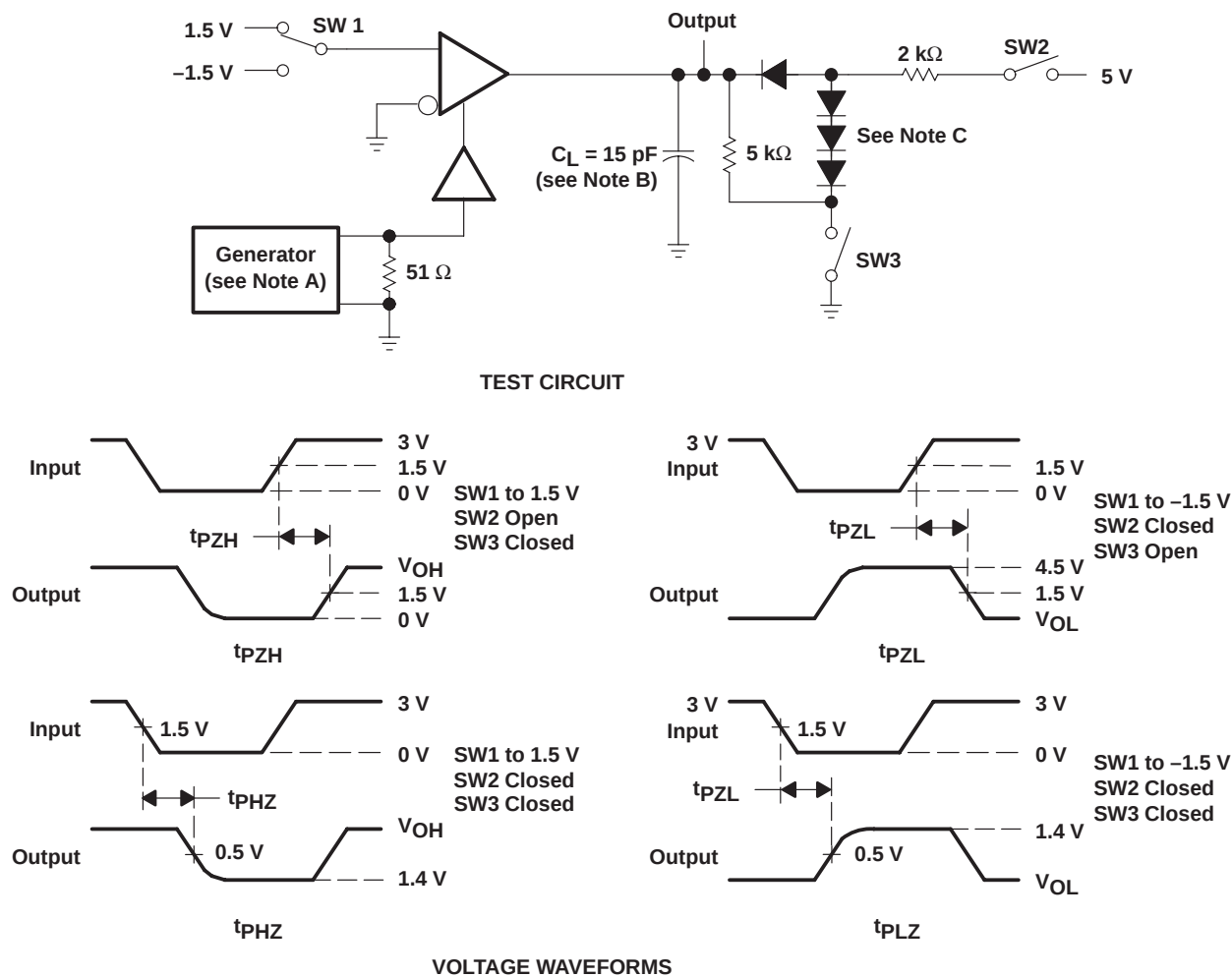
- NOTES: A. The input pulse is supplied by a generator having the following characteristics: PRR = 1 MHz, duty cycle = 50%,  $t_r = t_f = 6$  ns.  
B.  $C_L$  includes probe and jig capacitance.

Figure 2. Propagation Delay Times

# SN75ALS175 QUADRUPLE DIFFERENTIAL LINE RECEIVER

SLLS131C – SEPTEMBER 1991 – REVISED MAY 1995

## PARAMETER MEASUREMENT INFORMATION



- NOTES: A. The input pulse is supplied by a generator having the following characteristics: PRR = 1 MHz, duty cycle = 50%,  $t_r = t_f = 6$  ns.  
B.  $C_L$  includes probe and jig capacitance.  
C. All diodes are 1N916 or equivalent.

Figure 3. Enable and Disable Times

**PACKAGING INFORMATION**

| Orderable Device | Status <sup>(1)</sup> | Package Type | Package Drawing | Pins | Package Qty | Eco Plan <sup>(2)</sup> | Lead/Ball Finish | MSL Peak Temp <sup>(3)</sup> |
|------------------|-----------------------|--------------|-----------------|------|-------------|-------------------------|------------------|------------------------------|
| SN75ALS175N      | ACTIVE                | PDIP         | N               | 16   | 25          | Pb-Free (RoHS)          | CU NIPDAU        | N / A for Pkg Type           |
| SN75ALS175NE4    | ACTIVE                | PDIP         | N               | 16   | 25          | Pb-Free (RoHS)          | CU NIPDAU        | N / A for Pkg Type           |
| SN75ALS175NSLE   | OBSOLETE              | SO           | NS              | 16   |             | TBD                     | Call TI          | Call TI                      |
| SN75ALS175NSR    | ACTIVE                | SO           | NS              | 16   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN75ALS175NSRE4  | ACTIVE                | SO           | NS              | 16   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN75ALS175NSRG4  | ACTIVE                | SO           | NS              | 16   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

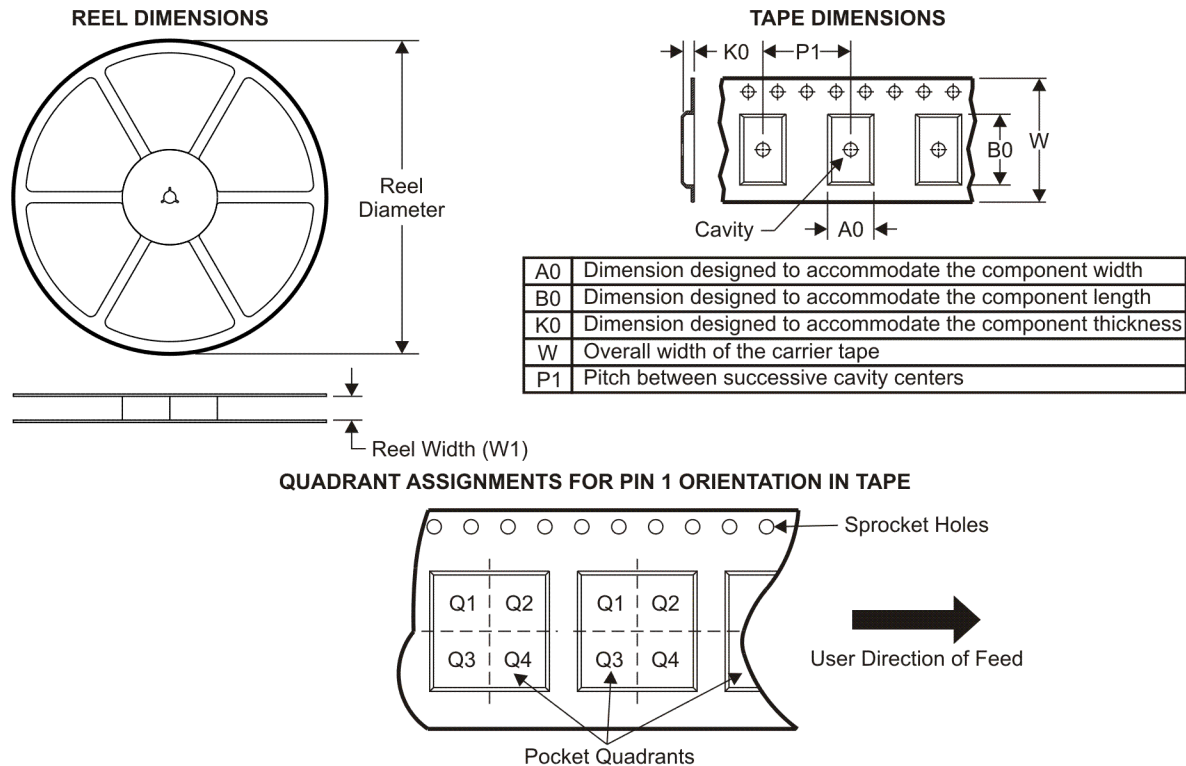
**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

**TAPE AND REEL INFORMATION**



\*All dimensions are nominal

| Device        | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|---------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| SN75ALS175NSR | SO           | NS              | 16   | 2000 | 330.0              | 16.4               | 8.2     | 10.5    | 2.5     | 12.0    | 16.0   | Q1            |

## TAPE AND REEL BOX DIMENSIONS



\*All dimensions are nominal

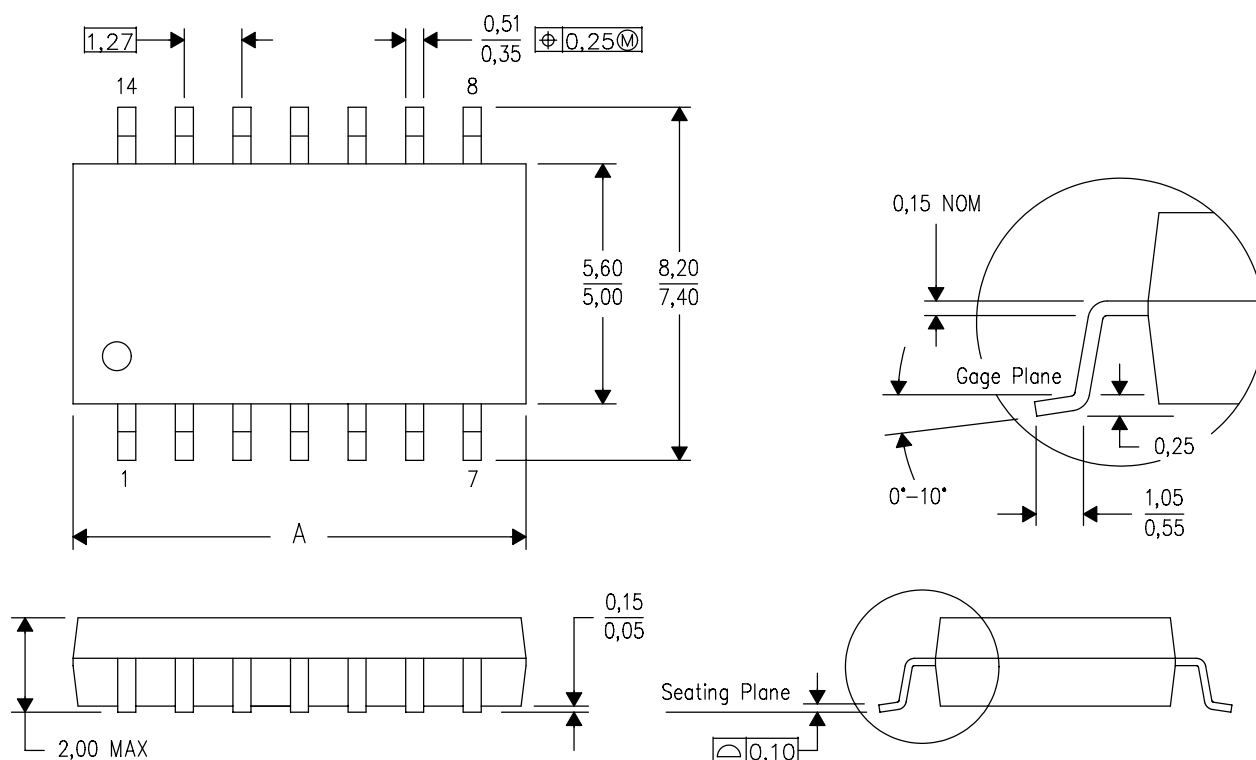
| Device        | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|---------------|--------------|-----------------|------|------|-------------|------------|-------------|
| SN75ALS175NSR | SO           | NS              | 16   | 2000 | 346.0       | 346.0      | 33.0        |

# MECHANICAL DATA

NS (R-PDSO-G\*\*)

PLASTIC SMALL-OUTLINE PACKAGE

14-PINS SHOWN



| DIM \ PINS ** | 14    | 16    | 20    | 24    |
|---------------|-------|-------|-------|-------|
| A MAX         | 10,50 | 10,50 | 12,90 | 15,30 |
| A MIN         | 9,90  | 9,90  | 12,30 | 14,70 |

4040062/C 03/03

- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

N (R-PDIP-T\*\*)

16 PINS SHOWN

## PLASTIC DUAL-IN-LINE PACKAGE



| PINS **<br>DIM      | 14               | 16               | 18               | 20               |
|---------------------|------------------|------------------|------------------|------------------|
| A MAX               | 0.775<br>(19,69) | 0.775<br>(19,69) | 0.920<br>(23,37) | 1.060<br>(26,92) |
| A MIN               | 0.745<br>(18,92) | 0.745<br>(18,92) | 0.850<br>(21,59) | 0.940<br>(23,88) |
| MS-001<br>VARIATION | AA               | BB               | AC               | AD               |



4040049/E 12/2002

NOTES:

- A. All linear dimensions are in inches (millimeters).  
B. This drawing is subject to change without notice.
-  Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).  
 The 20 pin end lead shoulder width is a vendor option, either half or full width.

## IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

TI products are not authorized for use in safety-critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, unless officers of the parties have executed an agreement specifically governing such use. Buyers represent that they have all necessary expertise in the safety and regulatory ramifications of their applications, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of TI products in such safety-critical applications, notwithstanding any applications-related information or support that may be provided by TI. Further, Buyers must fully indemnify TI and its representatives against any damages arising out of the use of TI products in such safety-critical applications.

TI products are neither designed nor intended for use in military/aerospace applications or environments unless the TI products are specifically designated by TI as military-grade or "enhanced plastic." Only products designated by TI as military-grade meet military specifications. Buyers acknowledge and agree that any such use of TI products which TI has not designated as military-grade is solely at the Buyer's risk, and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI products are neither designed nor intended for use in automotive applications or environments unless the specific TI products are designated by TI as compliant with ISO/TS 16949 requirements. Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, TI will not be responsible for any failure to meet such requirements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

### Products

|                             |                                                                    |
|-----------------------------|--------------------------------------------------------------------|
| Amplifiers                  | <a href="http://amplifier.ti.com">amplifier.ti.com</a>             |
| Data Converters             | <a href="http://dataconverter.ti.com">dataconverter.ti.com</a>     |
| DSP                         | <a href="http://dsp.ti.com">dsp.ti.com</a>                         |
| Clocks and Timers           | <a href="http://www.ti.com/clocks">www.ti.com/clocks</a>           |
| Interface                   | <a href="http://interface.ti.com">interface.ti.com</a>             |
| Logic                       | <a href="http://logic.ti.com">logic.ti.com</a>                     |
| Power Mgmt                  | <a href="http://power.ti.com">power.ti.com</a>                     |
| Microcontrollers            | <a href="http://microcontroller.ti.com">microcontroller.ti.com</a> |
| RFID                        | <a href="http://www.ti-rfid.com">www.ti-rfid.com</a>               |
| RF/IF and ZigBee® Solutions | <a href="http://www.ti.com/lprf">www.ti.com/lprf</a>               |

### Applications

|                    |                                                                          |
|--------------------|--------------------------------------------------------------------------|
| Audio              | <a href="http://www.ti.com/audio">www.ti.com/audio</a>                   |
| Automotive         | <a href="http://www.ti.com/automotive">www.ti.com/automotive</a>         |
| Broadband          | <a href="http://www.ti.com/broadband">www.ti.com/broadband</a>           |
| Digital Control    | <a href="http://www.ti.com/digitalcontrol">www.ti.com/digitalcontrol</a> |
| Medical            | <a href="http://www.ti.com/medical">www.ti.com/medical</a>               |
| Military           | <a href="http://www.ti.com/military">www.ti.com/military</a>             |
| Optical Networking | <a href="http://www.ti.com/opticalnetwork">www.ti.com/opticalnetwork</a> |
| Security           | <a href="http://www.ti.com/security">www.ti.com/security</a>             |
| Telephony          | <a href="http://www.ti.com/telephony">www.ti.com/telephony</a>           |
| Video & Imaging    | <a href="http://www.ti.com/video">www.ti.com/video</a>                   |
| Wireless           | <a href="http://www.ti.com/wireless">www.ti.com/wireless</a>             |

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265  
Copyright © 2008, Texas Instruments Incorporated