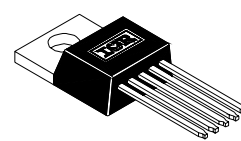


INTEGRATED SWITCHER

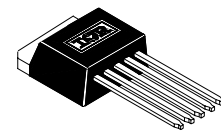
Features

- Primary current mode control, and secondary voltage mode control
- Vcc Over-voltage protection (latched)
- Over-current & over-temperature protection
- Quasi resonant, variable frequency operation
- 5 pin TO-220 and TO-262
- 3.9Ω Rds(on) max/ 650V MOSFET
- **Fully Characterized Avalanche Energy**

Packages



IRIS4011
5 Lead TO-220

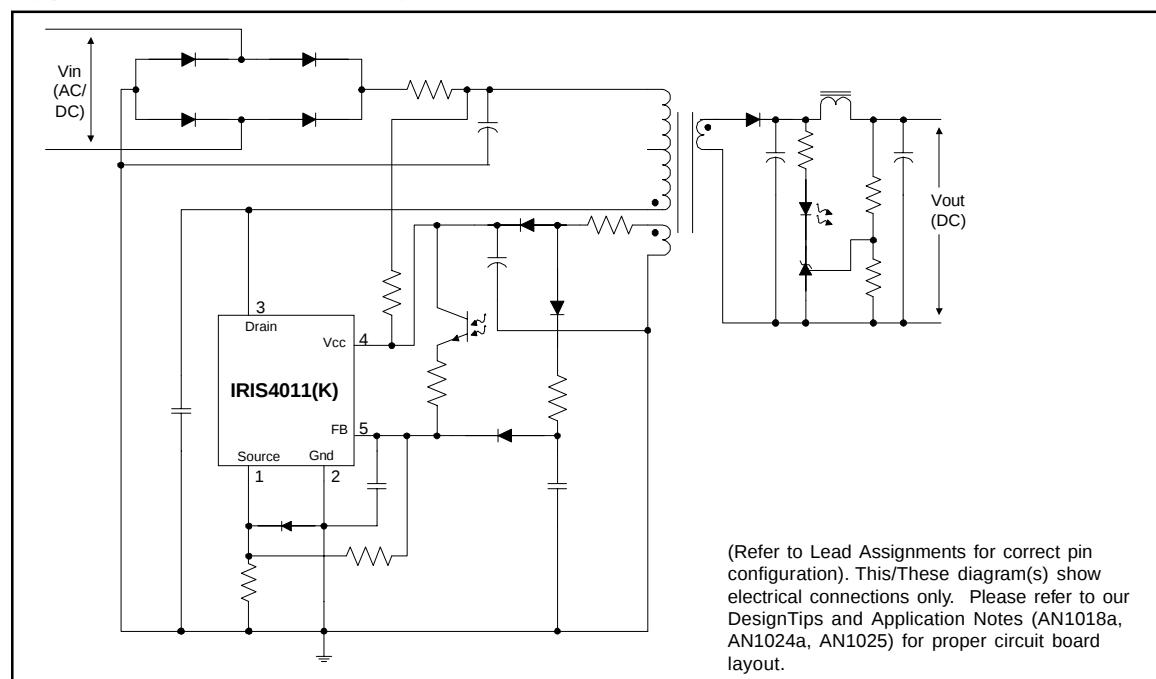


IRIS4011K
5 Lead TO-262

Description

The IRIS4011(K) is a dual mode voltage and current controller combined with a MOSFET in a single package. The IRIS4011(K) is designed for use in universal and single input AC/DC and DC/DC switching power supplies and is capable of powers up to 60W for a universal line input. The device can operate in either a quasi-resonant or Pulse Ratio Control (PRC) mode, and thereby variable frequency operation.

Typical Connection Diagram



Absolute Maximum Ratings

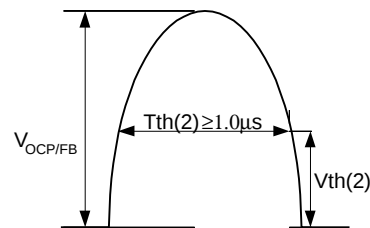
Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are absolute voltages referenced to terminals stated, all currents are defined positive into any lead. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions.

Symbol	Definition	Terminals	Max. Ratings	Units	Note
I_{Dpeak}	Peak drain current	3-1	6.8	A	Single pulse
I_{Dmax}	Maximum switching current	3-1	2.7		$V_{2-3} = 0.78V$ $T_c = 25^\circ C$
E_{AS}	Single pulse avalanche energy	3-1	92	mJ	$V_{dd} = 99V, L = 20mH,$ $G = 12V, I_{pk} = 2.8A$
V_{CC}	Power supply voltage	4-3	35	V	
V_{TH}	OCP/FB terminal voltage	5-2	6		
P_{D1}	Power dissipation for MOSFET	3-1	89	W	With infinite heatsink
			1.4		Without heatsink
P_{D2}	Power dissipation for control part (MIC)	4-2	0.8		Specified by $V_{IN} \times I_{IN}$
R_{thJC}	Thermal resistance, junction to case	—	1.4	$^\circ C/W$	
T_J	Junction temperature	—	-40-125	$^\circ C$	
T_S	Storage temperature	—	-40-125		
T_f	Internal frame temperature in operation	—	-20-125		Refer to recommended operating temperature
T_{OP}	Ambient operating temperature	—	-20-125		
T_L	Lead temp. (soldering, 10 seconds)	—	300		

Recommended Operating Conditions

Time for input of quasi resonant signals.

For the Quasi resonant signal inputted to the $V_{DCP/FB}$ terminal at the time of quasi resonant operation, the signal should be wider than $T_{th}(2)$



Electrical Characteristics (for Control IC)

$V_{CC} = 18V$, ($T_A = 25^\circ C$) unless otherwise specified.

Symbol	Definition	Min.	Typ.	Max.	Units	Test Conditions
V_{CCUV+}	V_{CC} supply undervoltage positive going threshold	14.4	16	17.6	V	
V_{CCUV-}	V_{CC} supply undervoltage negative going threshold	9	10	11		
I_{QCCUV}	UVLO mode quiescent current	—	—	100	μA	$V_{CC} < V_{CCUV+}$
I_{QCC}	Quiescent operating V_{CC} supply current	—	—	30	mA	
$T_{OFF(MAX)}$	Maximum OFF time	40	—	60	μsec	
$T_{TH(2)}$	Minimum input pulse width for quasi resonant signals	—	—	1.0		
$T_{OFF(MIN)}$	Minimum OFF time	—	—	1.5		
$V_{TH(1)}$	OCP/FB terminal threshold voltage 1	0.68	0.73	0.78	V	
$V_{TH(2)}$	OCP/FB terminal threshold voltage 2	1.3	1.45	1.6		
$I_{OCP/FB}$	OCP/FB terminal sink current	1.1	1.35	1.7	mA	
$V_{CC(OVP)}$	V_{CC} overvoltage protection limit	20.5	22.5	24.5	V	
$I_{CC(LA)}$	Latch circuit holding current	—	—	400	μA	
$V_{CC(LaOFF)}$	Latch circuit reset voltage	6.6	—	8.4	V	
$T_{J(TSD)}$	Thermal shutdown activation temperature	140	—	—	$^\circ C$	

Electrical Characteristics (for MOSFET)

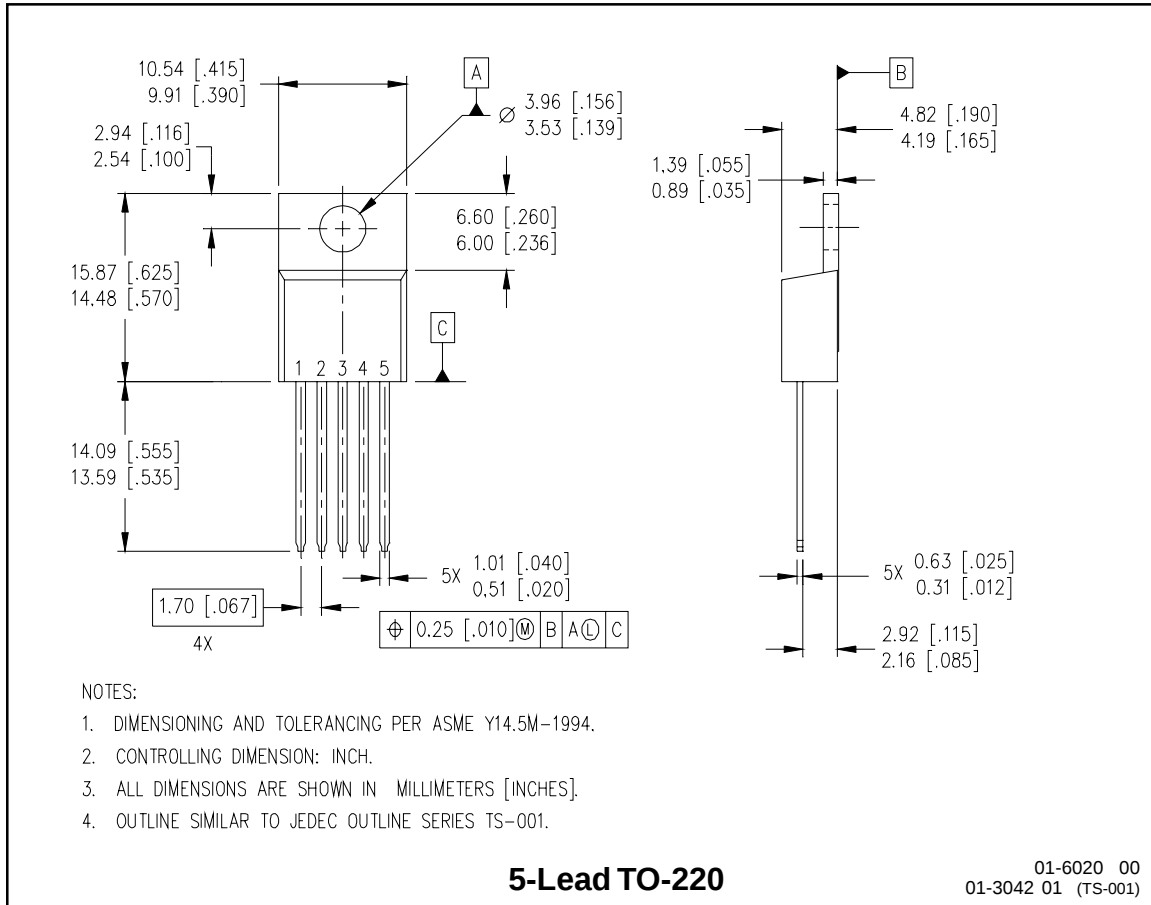
($T_A = 25^\circ C$) unless otherwise specified.

Symbol	Definition	Min.	Typ.	Max.	Units	Test Conditions
V_{DSS}	Drain-to-source breakdown voltage	650	—	—	V	
I_{DSS}	Drain leakage current	—	—	300	μA	$V_{ds}=520V, V_{CC}=0V$ $T_J=125^\circ C$
$R_{DS(ON)}$	On-resistance	—	—	3.9	Ω	$V_{3-1}=10V, I_D=0.9A$
t_r	Rise time (10% to 90%)	—	—	250	ns	
θ_{J-C}	Thermal resistance	—	—	1.4	$^\circ C/W$	Between junction and case

International
IOR Rectifier

O.V.P. – Overvoltage Protection Circuit
T.S.D. – Thermal Shutdown Circuit

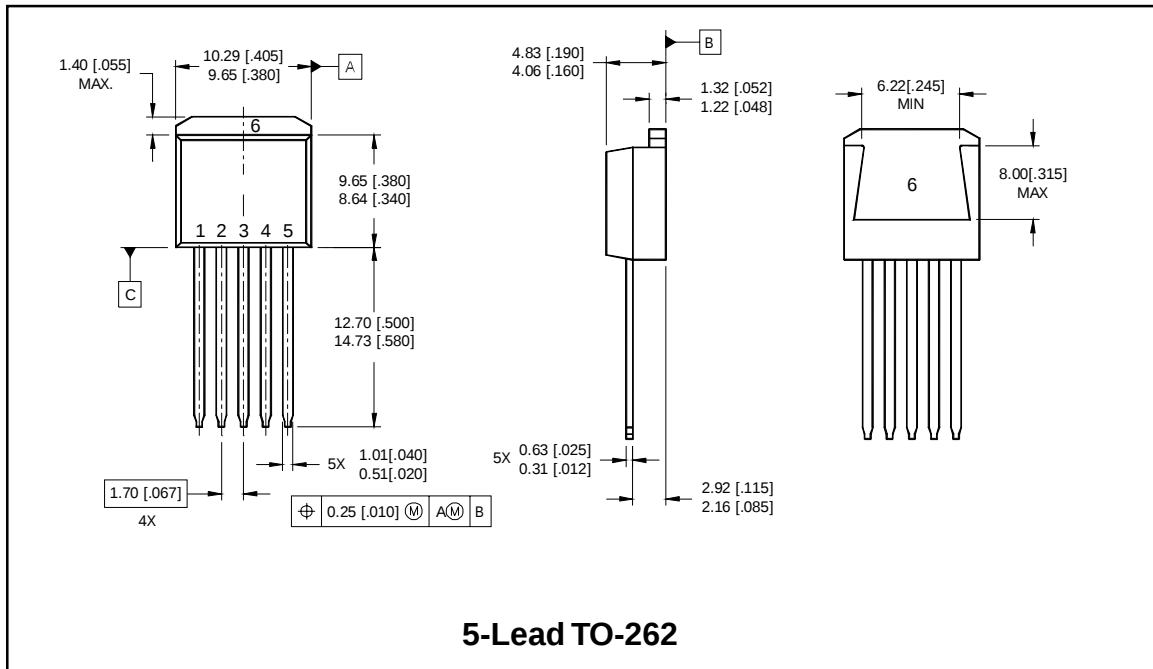
Case outline



IRIS4011(K)

International
IR Rectifier

Case outline



International
IR Rectifier

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Data and specifications subject to change without notice. 10/162001