

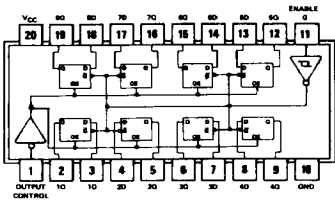
54/74 FAMILIES OF COMPATIBLE TTL CIRCUITS

PIN ASSIGNMENTS (TOP VIEWS)

OCTAL D-TYPE LATCHES

363 TRANSPARENT LATCH
3-STATE OUTPUTS
COMMON OUTPUT CONTROL
COMMON ENABLE

See page 7-467

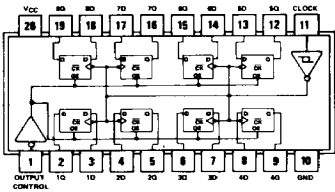


SN54LS363 (J) SN74LS363 (J, N)

OCTAL D-TYPE FLIP-FLOPS

364 COMMON CLOCK
COMMON OUTPUT CONTROL
3-STATE OUTPUTS

See page 7-467

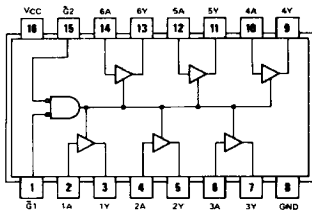


SN54LS364 (J) SN74LS364 (J, N)

HEX BUS DRIVERS

365 3-STATE OUTPUTS
NONINVERTED DATA OUTPUTS
GATED ENABLE INPUTS

See page 6-36

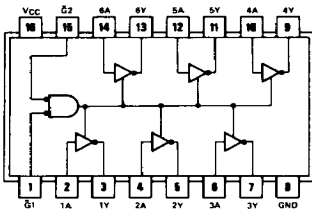


SN54365A (J, W) SN74365A (J, N)
SN54LS365A (J, W) SN74LS365A (J, N)

HEX BUS DRIVERS

366 INVERTED DATA OUTPUTS
GATED ENABLE INPUTS
3-STATE OUTPUTS

See page 6-36



SN54366A (J, W) SN74366A (J, N)
SN54LS366A (J, W) SN74LS366A (J, N)