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iCEblink40HX1K Evaluation Kit

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A low-cost platform for evaluating and developing with the low-power iCE40HX1K FPGA. The board provides access to general-purpose I/O and includes capacitive-touch buttons and LEDs. The board is powered and programmed via USB. An on-board microcontroller enables two-way communication with the iCE40HX1K FPGA.

The free iCEcube2™ development tool controls programming, accesses virtual I/O functions, and runs the included demos. iCEcube2 can be downloaded from the Lattice website.

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Features

- High-performance, low-power iCE40HX1K FPGA in a 100-pin TQFP package
- USB programming, debugging, virtual I/O functions and power supply
- Four user LEDs
- Four capacitive-touch buttons
- 3.3 MHz clock source
- 1Mbit SPI serial configuration PROM
- Supported by iCEcube2 design software
- 68 LVCMOS/LVTTL (3.3V) digital I/O connections on 0.1" through-hole connections
- Supports third-party I/O expansion boards and modules, including 3.3V Arduino Shield boards (requires additional sockets, not supplied)

[Kit Contents](#) [Board Photos](#) [Ordering Information](#) [Documentation](#) [Support](#)

Kit Contents

- iCEblink40 HX1K Board
- USB Mini Cable for power & programming
- QuickStart Guide

Board Photos

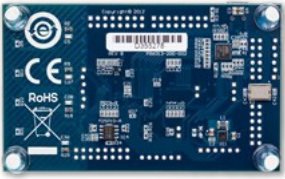


Top View

Click image to enlarge



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Bottom View

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Side View

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Ordering Information

- Ordering Part Number: ICE40HX1K-BLINK-EVN
- [Click here](#) to find an authorized Lattice distributor near you
- [Click here](#) to order now from the Lattice Online Store

Documentation

Quick Reference			Downloads			
User Manual			Design File			
		TITLE	NUMBER	VERSION	DATE	FORMAT
		CapSense Demo for iCEblink40 Evaluation Kits - User's Guide	UG64	1.0	10/17/2012	PDF
		iCEblink40-HX1K Evaluation Kit User's Guide	EB73	01.1	9/18/2012	PDF
Describes the features and functionality of the board, including schematics.						
		iCEblink40-HX1K QuickStart Guide	QS015		5/4/2012	PDF
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