

BIDIRECTIONAL THYRISTOR OVERVOLTAGE PROTECTORS



BOURNS®

TISP4500H3BJ Overvoltage Protector

Non-Conductive During K.20/21/45 Power Contact Test

- Off-State Voltage >245 V rms
- For Controlled Environment 0 °C to 70 °C

Ion-Implanted Breakdown Region

Precise and Stable Voltage

Low Voltage Overshoot under Surge

Device	V _{DRM} V @ 0 °C	V _(BO) V @ 70 °C
TISP4500H3BJ	350	500

SMBJ Package (Top View)



MD-SMB-004-a

Device Symbol



SD-TISP4xxx-001-a

Rated for International Surge Wave Shapes

Wave Shape	Standard	I _{PPSM} A
2/10	GR-1089-CORE	500
10/250	GR-1089-CORE	230
10/700	ITU-T K.20/21/45	200
10/1000	GR-1089-CORE	100

 UL Recognized Component

Description

This device is designed to limit overvoltages on the telephone line to ± 500 V over the temperature range of ± 350 V. It allows a.c. power contact voltages of up to 245 V rms to occur without clipping. The combination of these two voltages gives protection for components having ratings of 500 V or above and ensures the protector is non-conducting for the ITU-T recommendations K.20/21/45 230 V rms power cross test condition (test number 2.3.1).

The protector consists of a symmetrical voltage-triggered bidirectional thyristor. Overvoltages are initially clipped by breakdown clamping until the voltage rises to the breakdown level, which causes the device to crowbar into a low-voltage on state. This low-voltage on state causes the current resulting from the overvoltage to be safely diverted through the device. The high crowbar holding current prevents d.c. latchup as the diverted current subsides.

How To Order

Device	Package	Carrier	For Standard Termination Finish Order As	For Lead Free Termination Finish Order As	Marking Code	Std. Qty.
TISP4500H3BJ	SMB (DO-214AA)	Embossed Tape Reeled	TISP4500H3BJR	TISP4500H3BJR-S	4500H3	3000

*RoHS Directive 2002/95/EC Jan 27 2003 including Annex

APRIL 2001 - REVISED FEBRUARY 2005

Specifications are subject to change without notice.

Customers should verify actual device performance in their specific applications.

TISP4500H3BJ Overvoltage Protector

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Absolute Maximum Ratings, 0 °C ≤ TA ≤ 70 °C (Unless Otherwise Noted)

Rating	Symbol	Value	Unit
Repetitive peak off-state voltage	V _{DRM}	±350	V
Non-repetitive peak on-state pulse current (see Notes 1 and 2)	I _{PPSM}	500 230 200 100	A
2/10 (Telcordia GR-1089-CORE, 2/10 µs voltage wave shape)		TA = 25 °C	
10/250 (Telcordia GR-1089-CORE, 10/250 µs voltage wave shape)		TA = 25 °C	
10/700 (ITU-T K.20/21/45, 5/310 µs current wave shape)		TA = 25 °C	
10/1000 (Telcordia GR-1089-CORE, 10/1000 µs voltage wave shape)		TA = 25 °C	
Non-repetitive peak on-state current (see Notes 1, 2 and 3)	I _{TSM}	±55 ±2.0	A
50 Hz, 20 ms (1 cycle)			
50 Hz, 1000 s			
Junction temperature	T _J	-40 to +150	°C
Storage temperature range	T _{stg}	-65 to +150	°C

NOTES: 1. Initially the device must be in thermal equilibrium.
 2. The surge may be repeated after the device returns to its initial conditions.
 3. EIA/JESD51-2 environment and EIA/JESD51-3 PCB with standard footprint dimensions connected with 5 A rated printed wiring track widths.

Electrical Characteristics, 0 °C ≤ TA ≤ 70 °C (Unless Otherwise Noted)

Parameter	Test Conditions	Min	Typ	Max	Unit
I _{DRM} Repetitive peak off-state current	V _D = V _{DRM} TA = 25 °C TA = 70 °C			±5 ±10	µA
V _(BO) Breakover voltage	dv/dt = ±250 V/ms, R _{SOURCE} = 300 Ω			±500	V
V _(BO) Impulse breakover voltage	ITU-T recommendation K.44 (02/2000) Figure A.3-1/K.44 10/700 impulse generator Charge Voltage = ±4 kV			±500	V
I _(BO) Breakover current	dv/dt = ±250 V/ms, R _{SOURCE} = 300 Ω			±0.6	A
I _H Holding current	I _T = ±5 A, di/dt = -/+30 mA/ms	±0.15			A
I _D Off-state current	V _D = ±50 V TA = 70 °C			±10	µA
C _{off} Off-state capacitance	f = 1 MHz, V _d = 1 V rms, V _D = 0 f = 1 MHz, V _d = 1 V rms, V _D = -1 V f = 1 MHz, V _d = 1 V rms, V _D = -2 V f = 1 MHz, V _d = 1 V rms, V _D = -50 V			84 67 62 31	pF

Thermal Characteristics

Parameter	Test Conditions	Min	Typ	Max	Unit
R _{θJA} Junction to free air thermal resistance	EIA/JESD51-3 PCB, I _T = I _{TSM} (1000), TA = 25 °C, (see Note 5)			113	°C/W
	265 mm x 210 mm populated line card, 4-layer PCB, I _T = I _{TSM} (1000), TA = 25 °C		50		

NOTE 5: EIA/JESD51-2 environment and PCB has standard footprint dimensions connected with 5 A rated printed wiring track widths.

Parameter Measurement Information

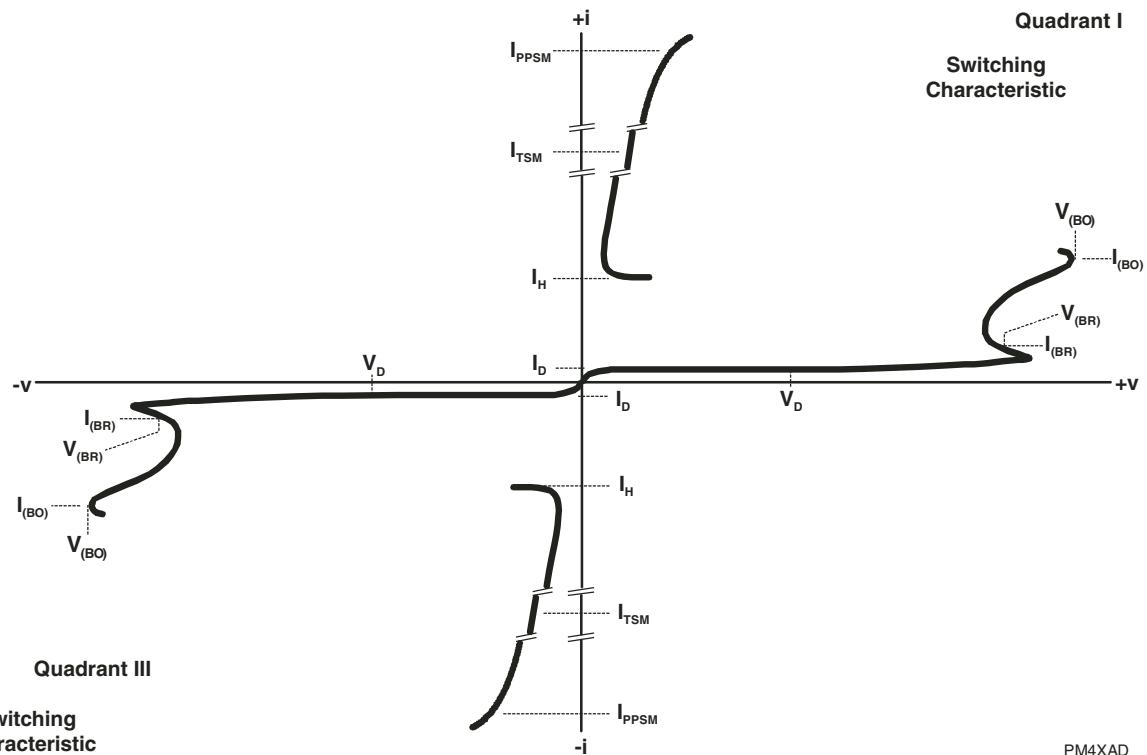
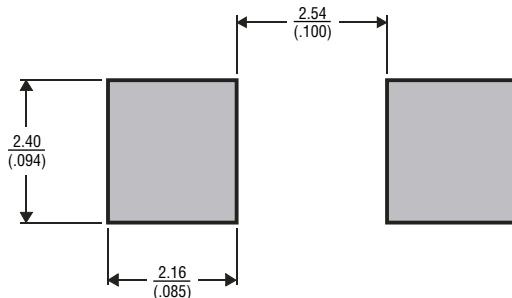


Figure 1. Voltage-current Characteristic for T and R Terminals

All Measurements are Referenced to the R Terminal

MECHANICAL DATA**Recommended Printed Wiring Land Pattern Dimensions****SMB Land Pattern**DIMENSIONS ARE: $\frac{\text{MM}}{(\text{INCHES})}$

MDXXBIB

Device Symbolization Code

Devices will be coded as below. As the device parameters are symmetrical, terminal 1 is not identified.

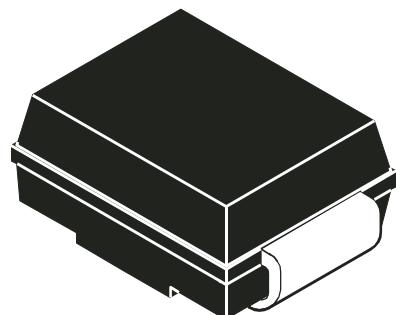
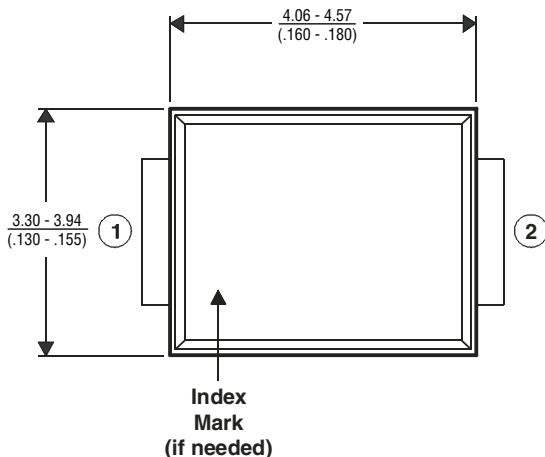
Device	Symbolization Code
TISP4500H3BJ	4500H3

MECHANICAL DATA

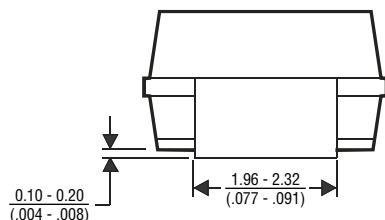
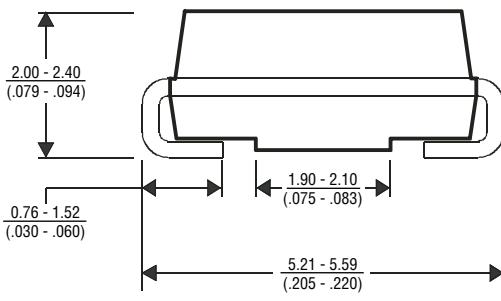
SMB (DO-214AA) Plastic Surface Mount Diode Package

This surface mount package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.

SMB



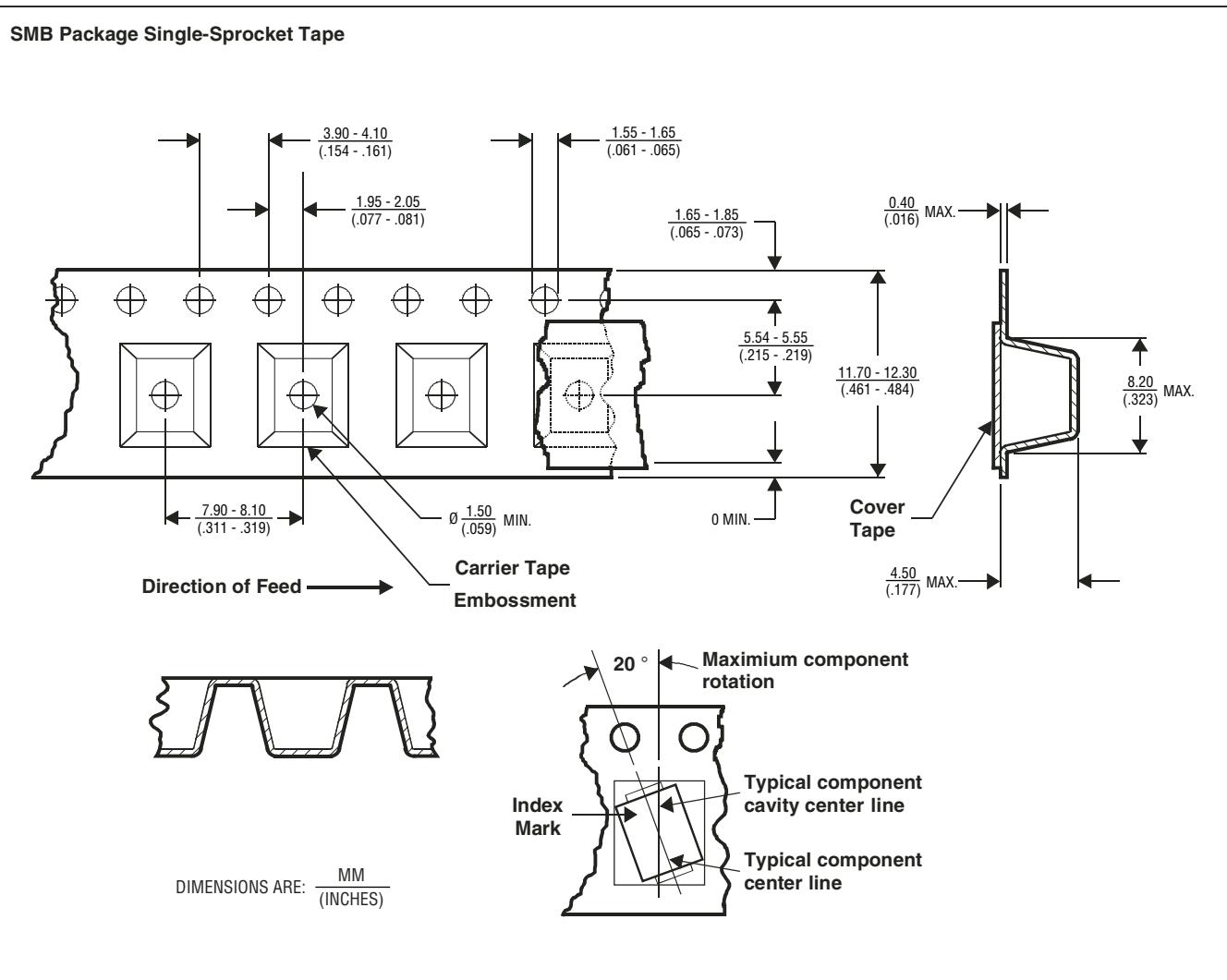
DIMENSIONS ARE: $\frac{\text{MM}}{(\text{INCHES})}$



MDXXBHB

MECHANICAL DATA

Tape Dimensions



NOTES: A. The clearance between the component and the cavity must be within 0.05 mm (.002 in.) MIN. to 0.65 mm (.026 in.) MAX. so that the component cannot rotate more than 20° within the determined cavity. MDXXBJA

B. Taped devices are supplied on a reel of the following dimensions:-

Reel diameter: 330 ± 3.0 mm ($12.99 \pm .118$ in.)
 Reel hub diameter: 75 mm (2.95 in.) MIN.
 Reel axial hole: 13.0 ± 0.5 mm (.512 ± .020 in.)

C. 3000 devices are on a reel.