LAN Components

TLA-3T105/109/110/112

10BASE-T

Pulse Transformers

FEATURES

- 16-pin SOP package(1.27mm=50mil pin pitch).
- TLA-3T105 is suitable for use with AMD's Am79C984A, 985, 988A.
- TLA-3T109 is suitable for use with LSI LOGIC's 80C24, 80C25, NS's DP83959, LEVEL ONE's LXT905, 908.
- TLA-3T110 is suitable for use with LEVEL ONE's LXT901A, 907A, 914, 915, 944, FUJITSU's MB86964, CIRRUS LOGIC's CS8900A(5V).
- TLA-3T112 is suitable for use with CIRRUS LOGIC's CS8900A(3.3V).
- Tape and reel packaging are available(TLA-3T1 □ -T: Tape and reel, TLA-3T1 □ : Bulk).
- ISO9001/QS9000 quality standards certification.

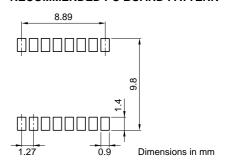
GENERAL SPECIFICATIONS

2
2
1
0 to +70°C
−40 to +85°C
Erms:2000V[60s]

SHAPES AND DIMENSIONS/CIRCUIT DIAGRAM

11.6max. 13 12 16 15 14 13 12 11 10 9 9.2 ± 0.2 TDK *** 6.5typ. Part No. Lot No. 2 3 4 5 6 7 8 N.C. pins: 4, 5, 12, 13 0.25typ ***: Country of origin 1.27typ. 0.4typ. Dimensions in mm

RECOMMENDED PC BOARD PATTERN



ELECTRICAL CHARACTERISTICS

Part No.	Turns ratio 1,2,3:16,15,14 6,7,8:11,10,9	Inductance (μH)min.[100kHz] 1-3 6-8	Leakage inductance (μH)max.[100kHz] 1-3[16-14 shorted] 6-8[11-9 shorted]	Inter-winding stray capacitance (pF)max.[100kHz] 1,3-16,14 6,8-11,9	DC resistance (Ω)max. 1-3 6-8
TLA-3T105(-T)	1CT:1CT	130	0.3	12	0.5
	1CT:1CT	130	0.3	12	0.5
TLA-3T109(-T)	2CT:1CT	130	0.35	12	0.5
	1CT:1CT	130	0.35	12	0.5
TLA-3T110(-T)	1CT:1CT	130	0.35	12	0.5
	1CT:1.41CT	130	0.35	12	0.5
TLA-3T112(-T)	1CT:1CT	130	0.35	12	0.85
	1CT:2.5CT	130	0.35	12	0.85

[•] Ta=25°C

LAN Components

TLA-3T105/109/110/112

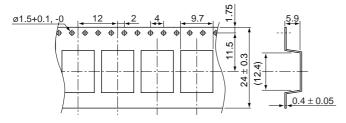
10BASE-T

Pulse Transformers

PACKAGING STYLES

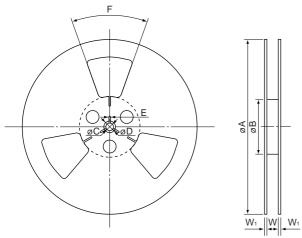
Quantity: 1000pieces/reel

TAPE DIMENSIONS



Dimensions in mm

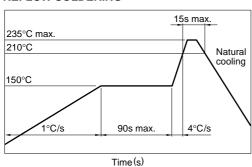
REEL DIMESIONS



 A
 B
 C
 D
 E
 F
 W
 W1

 ø330±2
 ø80±1
 ø13.5±0.2
 ø21±0.8
 2±0.5
 3-40°
 24.4+2, -0
 2±0.5

RECOMMENDED SOLDERING CONDITION REFLOW SOLDERING



- Temperature is measured on the top of products body(Using thermocoupler for measurement)
- This profile is reference data we recommend. Please check in your actual process.

D422_TLA3T 001227

