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ON Semiconductor®

FGD3325G2-F085

EcoSPARK®2 330mJ, 250V, N-Channel Ignition IGBT

Features

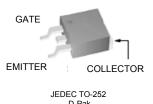
- SCIS Energy = 330mJ at T_J = 25°C
- Logic Level Gate Drive
- Qualified to AEC Q101
- RoHS Compliant

Applications

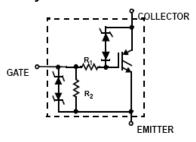
- Automotive Ignition Coil Driver Circuits
- Coil On Plug Applications



Package



Symbol



Absolute Maximum Ratings $T_A = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Ratings	Units	
BV _{CER}	Collector to Emitter Breakdown Voltage (I _C = 1mA)	250	V	
BV _{ECS}	Emitter to Collector Voltage - Reverse Battery Condition (I _C = 10m	nA)	28	V
E _{SCIS25}	I_{SCIS} = 14.8A, L = 3.0mHy, R_{GE} = 1K Ω	T _C = 25°C	330	mJ
	I_{SCIS} = 11.4A, L = 3.0mHy, R_{GE} = 1K Ω	T _C = 150°C	195	mJ
I _{C25}	Collector Current Continuous, at V _{GE} = 5.0V, T _C = 25°C	41	Α	
I _{C110}	Collector Current Continuous, at V _{GE} = 5.0V, T _C = 110°C	25	Α	
V_{GEM}	Gate to Emitter Voltage Continuous	±10	V	
Б	Power Dissipation Total, at T _C = 25°C	T _C = 25°C	150	W
P_D	Power Dissipation Derating, for T _C > 25°C	T _C > 25°C	1.0	W/°C
T_{J}	Operating Junction Temperature Range	-55 to +175	°C	
T _{STG}	Storage Junction Temperature Range		-55 to +175	°C
T_L	Max. Lead Temp. for Soldering (Leads at 1.6mm from case for 10	300	°C	
T _{PKG}	Reflow soldering according to JESD020C	260	°C	
ESD	HBM-Electrostatic Discharge Voltage at100pF, 1500Ω		4	kV
ESD	CDM-Electrostatic Discharge Voltage at 1Ω		2	kV

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FGD3325G2	FGD3325G2-F085	TO252	330mm	16mm	2500 units

Electrical Characteristics $T_A = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units

Off State Characteristics

BV _{CER}	Collector to Emitter Breakdown Voltage	$I_{CE} = 2mA, V_{GE} = 0,$ $R_{GE} = 1K\Omega,$ $T_{J} = -40 \text{ to } 150^{\circ}\text{C}$		225	1	275	V
BV _{CES}	Collector to Emitter Breakdown Voltage	$T_J = -40 \text{ to } 150^{\circ}\text{C}$		240	-	290	٧
BV _{ECS}	Emitter to Collector Breakdown Voltage	$I_{CE} = -75 \text{mA}, V_{GE} = 0 \text{V},$ $T_{J} = 25 ^{\circ}\text{C}$		28	-	-	V
BV_{GES}	Gate to Emitter Breakdown Voltage	I _{GES} = ±2mA		±12	±14	-	V
1	Collector to Emitter Leakage Current	V_{CE} = 175V, R_{GE} = 1K Ω	$T_J = 25^{\circ}C$	1	1	25	μΑ
ICER			$T_{\rm J} = 150^{\rm o}{\rm C}$	-	-	1	mA
	Emitter to Collector Leakage Current	V _{EC} = 24V,	$T_J = 25^{\circ}C$	-	-	1	m۸
I _{ECS}			$T_{\rm J} = 150^{\rm o}{\rm C}$	-	-	40	mA
R ₁	Series Gate Resistance		•	-	120	-	Ω
R ₂	Gate to Emitter Resistance			10K	-	30K	Ω

On State Characteristics

$V_{CE(SAT)}$	Collector to Emitter Saturation Voltage	$I_{CE} = 6A, V_{GE} = 4V,$	$T_{J} = 25^{\circ}C$	-	1.15	1.25	V
V _{CE(SAT)}	Collector to Emitter Saturation Voltage	I_{CE} = 10A, V_{GE} = 4.5V,	$T_{\rm J} = 150^{\rm o}{\rm C}$	-	1.35	1.50	V
V _{CE(SAT)}	Collector to Emitter Saturation Voltage	$I_{CE} = 15A, V_{GE} = 4.5V,$	$T_J = 150^{\circ}C$	-	1.68	1.85	V

Max Units

Min

Electrical Characteristics $T_A = 25^{\circ}C$ unless otherwise noted

Parameter

טynam	Dynamic Characteristics						
Q _{G(ON)}	Gate Charge	I _{CE} = 10A, V _{CE} = 12V, V _{GE} = 5V		-	21	-	nC
V	Gate to Emitter Threshold Voltage	I _{CE} = 1mA, V _{CE} = V _{GE}	$T_{J} = 25^{\circ}C$	1.3	1.5	2.2	V
V _{GE(TH)} Gate to Emitter Threshold Voltage	Gate to Emitter Threshold Voltage	ICE - IIIIA, VCE - VGE,	$T_{\rm J} = 150^{\rm o}{\rm C}$	0.75	1.1	1.8	v
V_{GEP}	Gate to Emitter Plateau Voltage	V _{CE} = 12V, I _{CE} = 10A		-	2.7	-	V

Test Conditions

Switching Characteristics

Symbol

t _{d(ON)R}	Current Turn-On Delay Time-Resistive	02 . 2	-	8.0	4	μS
t_{rR}	Current Rise Time-Resistive	$V_{GE} = 5V, R_G = 1K\Omega$ $T_J = 25^{\circ}C,$,	1.2	7	μS
t _{d(OFF)L}	Current Turn-Off Delay Time-Inductive			5.1	15	μS
t _{fL}	Current Fall Time-Inductive	$V_{GE} = 5V, R_{G} = 1K\Omega$ $I_{CE} = 6.5A, T_{J} = 25^{\circ}C,$	-	2.2	15	μS

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance Junction to Case	-	-	1	°C/W	

Typical Performance Curves

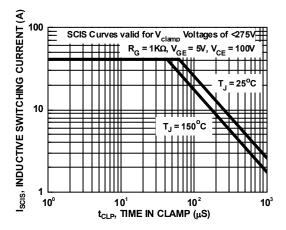


Figure 1. Self Clamped Inductive Switching Current vs. Time in Clamp

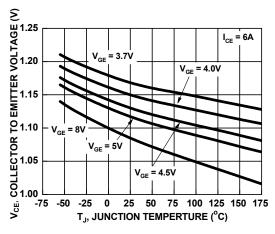


Figure 3. Collector to Emitter On-State Voltage vs. Junction Temperature

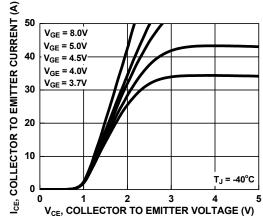


Figure 5. Collector to Emitter On-State Voltage vs. Collector Current

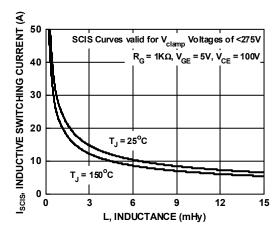


Figure 2. Self Clamped Inductive Switching Current vs. Inductance

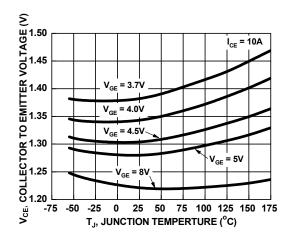


Figure 4. Collector to Emitter On-State Voltage vs. Junction Temperature

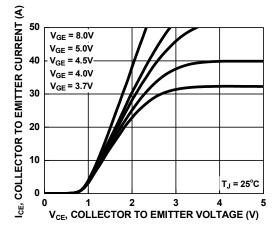


Figure 6. Collector to Emitter On-State Voltage vs. Collector Current

Typical Performance Curves (Continued)

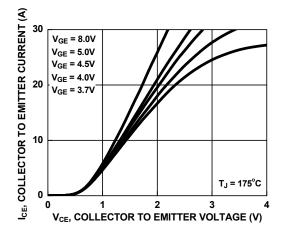


Figure 7. Collector to Emitter On-State Voltage vs. Collector Current

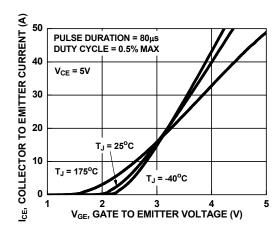


Figure 8. Transfer Characteristics

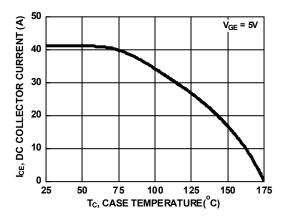


Figure 9. DC Collector Current vs. Case Temperature

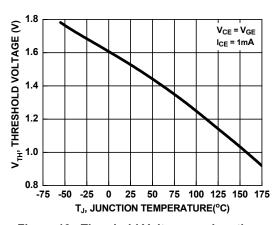


Figure 10. Threshold Voltage vs. Junction Temperature

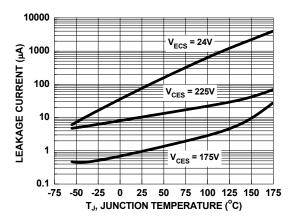


Figure 11. Leakage Current vs. Junction Temperature

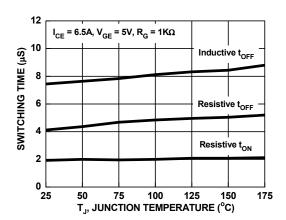


Figure 12. Switching Time vs. Junction Temperature

Typical Performance Curves (Continued)

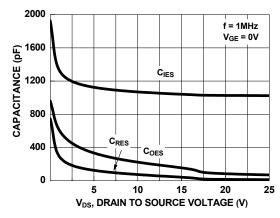


Figure 13. Capacitance vs. Collector to Emitter Voltage

Figure 14. Gate Charge

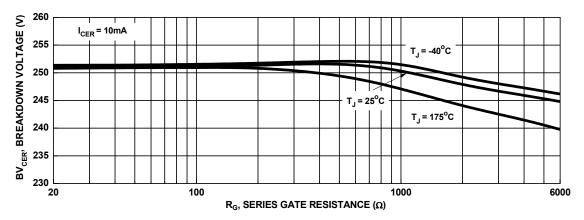


Figure 15. Break down Voltage vs. Series Gate Resistance

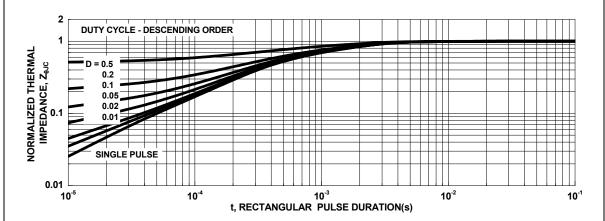


Figure 16. IGBT Normalized Transient Thermal Impedance, Junction to Case



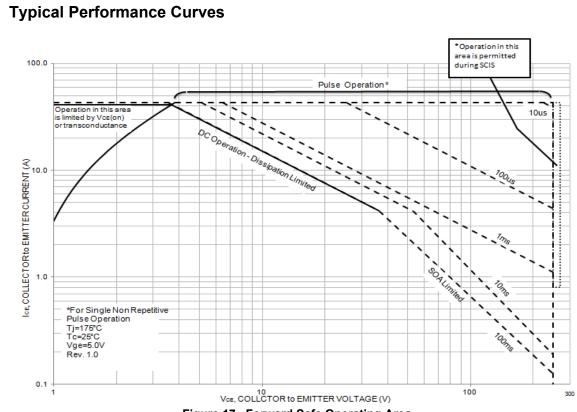


Figure 17. Forward Safe Operating Area

Test Circuit and Waveforms

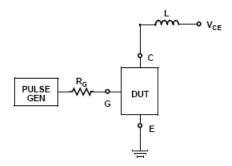


Figure 17. Inductive Switching Test Circuit

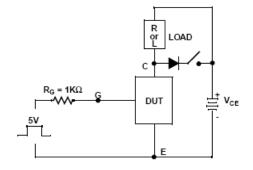


Figure 18. $t_{\mbox{\scriptsize ON}}$ and $t_{\mbox{\scriptsize OFF}}$ Switching Test Circuit

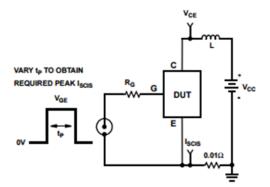


Figure 19. Energy Test Circuit

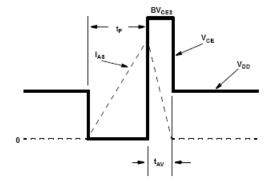
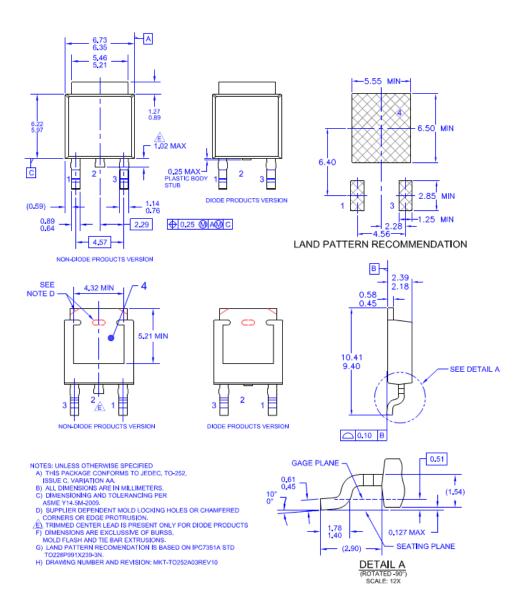


Figure 20. Energy Waveforms

Mechanical Dimensions

D-PAK



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