

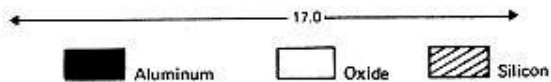
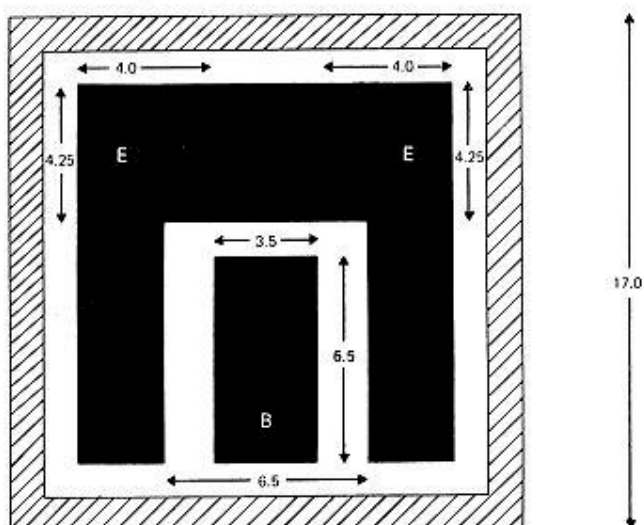
DIONICS INC.

65 RUSHMORE ST., WESTBURY, N.Y. 11590 516-997-7474



2N2222A 2N2221A
2N2219A 2N2218A
2N2222 2N2221
2N2219 2N2218

NPN SILICON
HIGH CURRENT (1.0 AMP) TRANSISTOR CHIPS
DESIGNED FOR HYBRID CIRCUIT APPLICATIONS.



- Chip Thickness=6 Mils $\pm$ 1 Mil
- Min. Dimension Across Bonding Pads=3.5 Mils
- Min. Separation Between Bonding Pads=1.5 Mils
- Distance from Bonding Pads to Edge of Chips=2.5 Mils

Detailed Specifications on Reverse Side

DIONICS INC.

65 RUSHMORE ST., WESTBURY, N.Y. 11590 516-897-7474

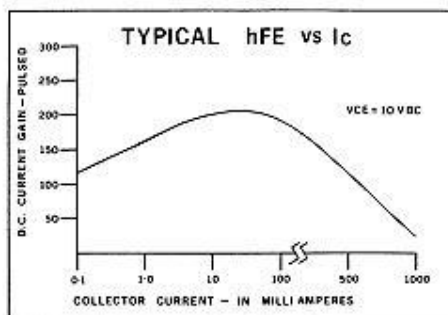


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NPN SILICON
HIGH CURRENT (1.0 AMP) TRANSISTOR CHIPS
DESIGNED FOR HYBRID CIRCUIT APPLICATIONS.

IMPROVED GAIN AT HIGH CURRENT LEVELS • LOW LEAKAGE CHARACTERISTICS •
OVERSIZED BONDING PADS • NO BETA DEGRADATION DURING PROLONGED HIGH
TEMPERATURE ASSEMBLY •

The high efficiency parallel emitter construction provides improved beta retention at high current levels. The large area bonding pads are positioned for maximum flexibility of substrate layout. Unique surface stabilization processing results in lower leakage currents and prevents the beta degradation frequently encountered during the extended high temperature assembly operations required for complex hybrid circuit construction. Chips are gold backed for eutectic die-attach, and have aluminum bonding pads for all conventional wire bonding techniques.



← 100% Probe Tested to These Parameters @ 25°C → Guaranteed →
(tested on sample basis)

h_{FE} @ $V_{CE} = 10V$				V_{CE0} Volts Min. @ $I_C = 10\mu A$ $I_E = 0$	V_{CE0} Volts Min. @ $I_C = 10mA$ $I_E = 0$	V_{CE0} Volts Min. @ $I_C = 10\mu A$ $I_E = 0$	I_{C0} nA Max. @ V_{CE} as below $I_E = 0$	V_{CE} (SAT.) Volts Max. @ $I_C = 150mA$ $I_E = 15mA$	C_{05} pF Max. @ $V_{CE} = 10V$ $I_C = 0$ $f = 100KHz$	f_t MHz Min. @ $I_C = 20mA$ $V_{CE} = 20V$ $f = 100MHz$
@ $I_C = 1mA$	@ $I_C = 10mA$	@ $I_C = 150mA$								
2N 2222A	50	75	100-300	70	40	6	10 @ $V_{CE} = 60V$	0.4	8	250
2N 2219A	MIN	MIN								
2N 2221A	25	35	40-120	60	30	5	10 @ $V_{CE} = 50V$	0.4	8	250
2N 2218A	MIN	MIN								
2N 2222	50	75	100-300	60	30	5	10 @ $V_{CE} = 50V$	0.4	8	250
2N 2219	MIN	MIN								
2N 2221	25	35	40-120	60	30	5	10 @ $V_{CE} = 50V$	0.4	8	250
2N 2218	MIN	MIN								

Dimensional Drawing on Reverse Side

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