



E = *early mate, late break* / voreilend
S = standard
L = *late mate, early break* / nacheilend

Leiterplatten-Lochbild / PCB drillhole pattern

2 x 2,54 - 5,08

2,8^{+0,1}

1 ±0,1

0,05

alle Löcher /
all holes

0,3

32 31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1

2,54

31 x 2,54 = 78,74

90

Missing information and dimensions per IEC 60 603-2

Consider protection memo from DIN 34	Information:			Tolerances		 All Dimensions in mm		Scale		2:1	
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						Messerleiste R 64 <i>Male Connector R 64</i>					
						334410					
	a		17.03.2006		ERNI www.ERNI.com				1		
Index		Date		H-00056615.S7A		STVR					