

TOSHIBA TRANSISTOR SILICON NPN TRIPLE DIFFUSED MESA TYPE

# 2SC5588

## HORIZONTAL DEFLECTION OUTPUT FOR SUPER HIGH RESOLUTION DISPLAY

COLOR TV FOR DIGITAL TV & HDTV

## HIGH SPEED SWITCHING APPLICATIONS

- High Voltage :  $V_{CBO} = 1700\text{ V}$
- Low Saturation Voltage :  $V_{CE(\text{sat})} = 3\text{ V (Max.)}$
- High Speed :  $t_f(2) = 0.1\mu\text{s (Typ.)}$

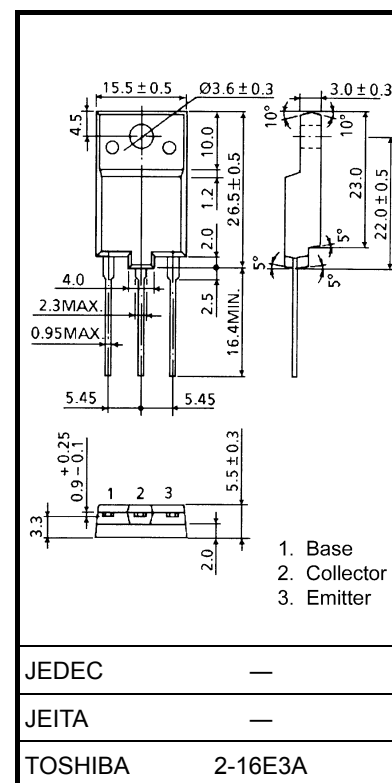
### ABSOLUTE MAXIMUM RATINGS (T<sub>c</sub> = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Collector–Base Voltage		$V_{CB0}$	1700	V
Collector–Emitter Voltage		$V_{CEO}$	800	V
Emitter–Base Voltage		$V_{EB0}$	5	V
Collector Current	DC	$I_C$	15	A
	Pulse	$I_{CP}$	30	
Base Current		$I_B$	7.5	A
Collector Power Dissipation		$P_C$	75	W
Junction Temperature		$T_j$	150	°C
Storage Temperature Range		$T_{stg}$	–55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Unit: mm

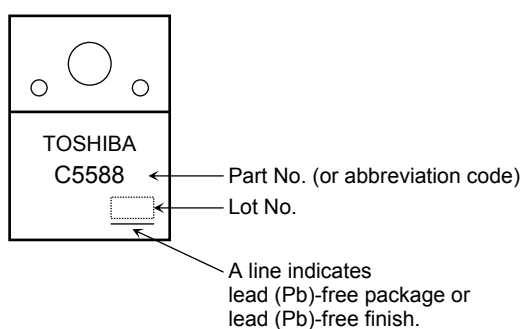


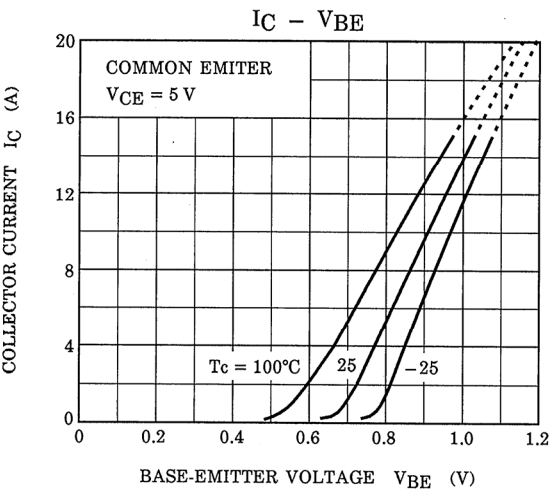
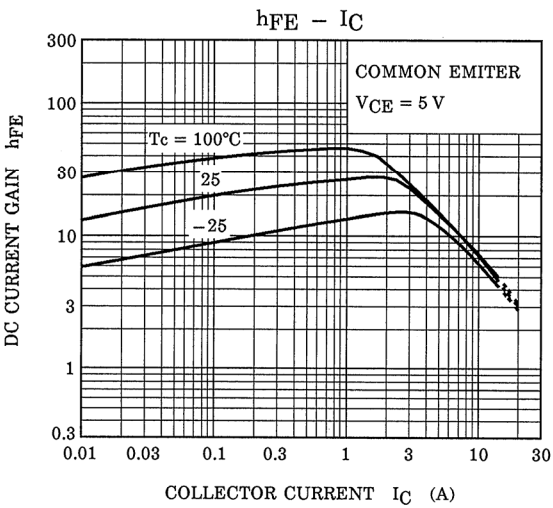
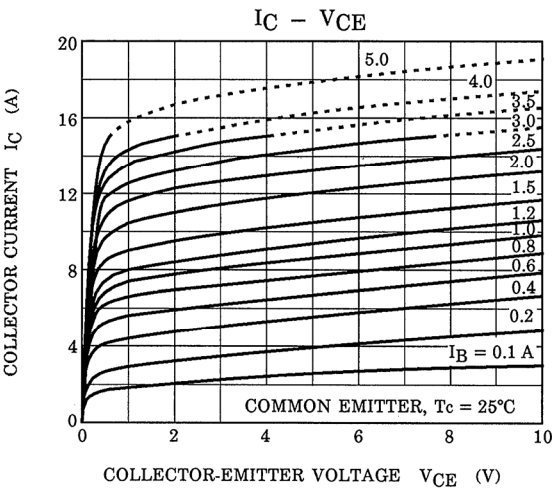
Weight: 5.5 g (typ.)

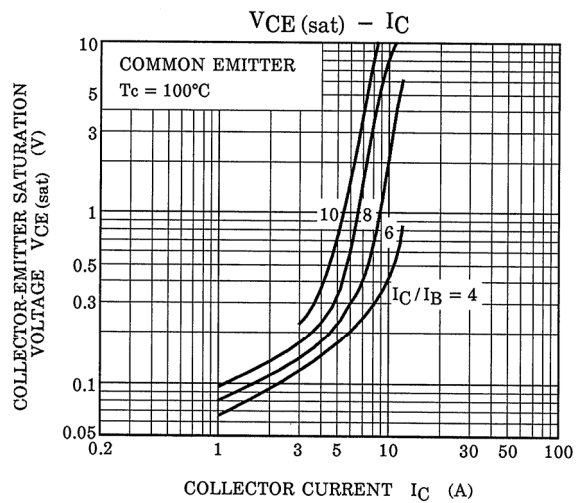
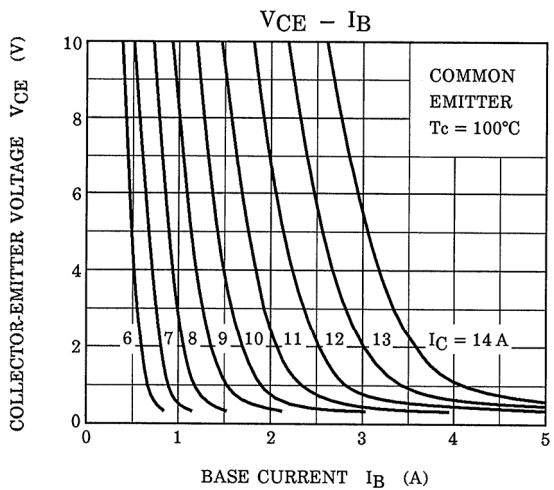
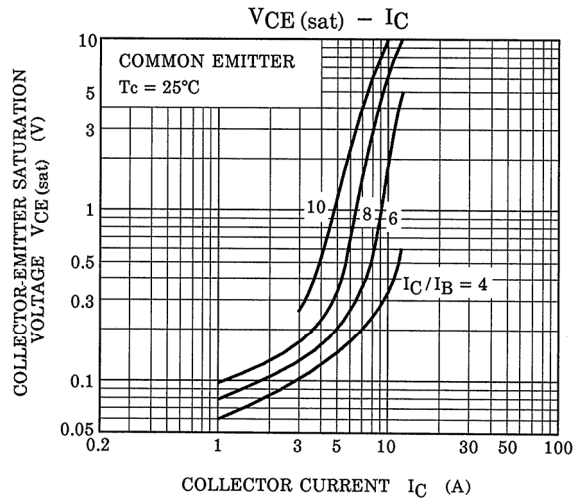
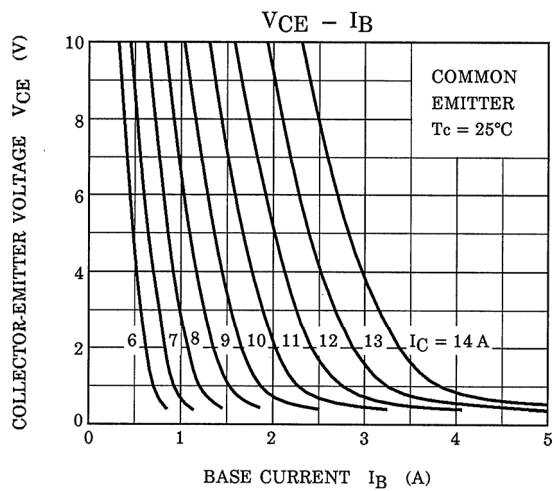
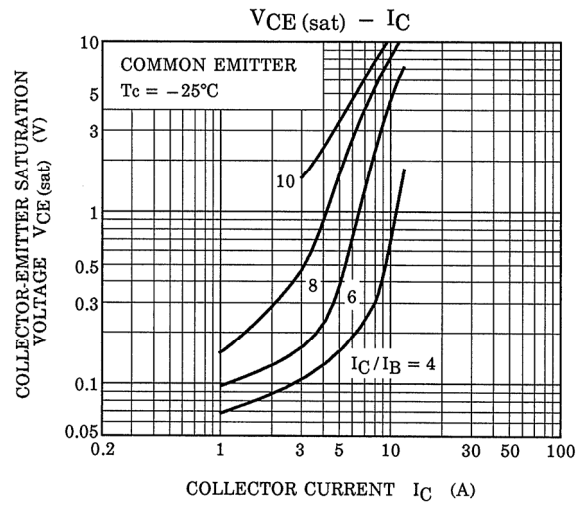
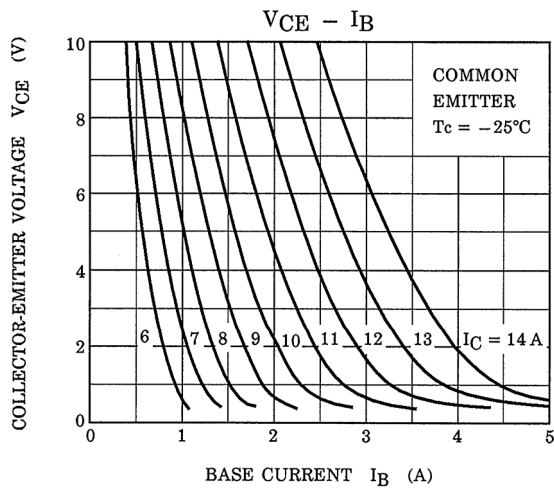
## ELECTRICAL CHARACTERISTICS (T<sub>c</sub> = 25°C)

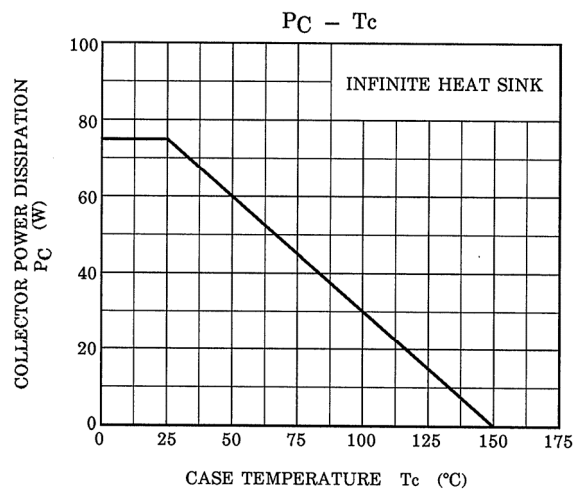
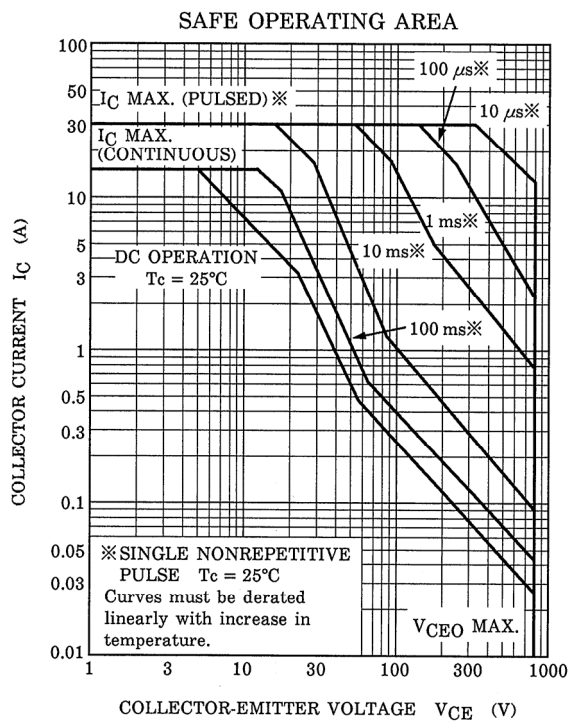
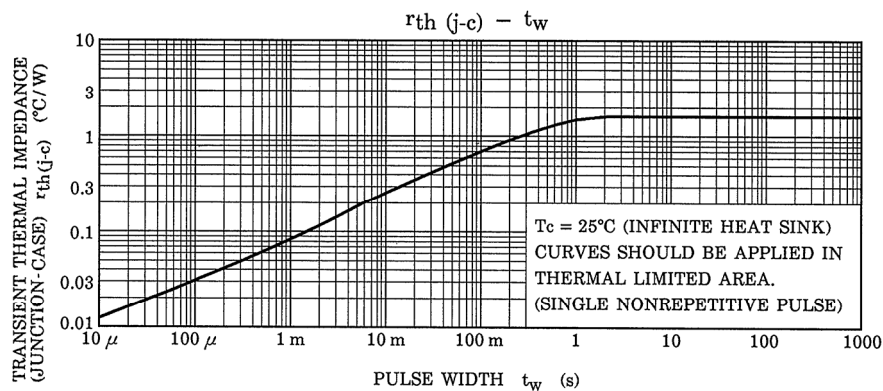
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Collector Cut-off Current		I <sub>CBO</sub>	V <sub>CB</sub> = 1700 V, I <sub>E</sub> = 0	—	—	1	mA
Emitter Cut-off Current		I <sub>EBO</sub>	V <sub>EB</sub> = 5 V, I <sub>C</sub> = 0	—	—	100	μA
Emitter-Base Breakdown Voltage		V <sub>(BR)</sub> CEO	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0	800	—	—	V
DC Current Gain		h <sub>FE</sub> (1)	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 2 A	22	—	45	—
		h <sub>FE</sub> (2)	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 9 A	6.5	—	12	
		h <sub>FE</sub> (3)	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 12 A	4.8	—	8.0	
Collector-Emitter Saturation Voltage		V <sub>CE</sub> (sat)	I <sub>C</sub> = 12 A, I <sub>B</sub> = 3 A	—	—	3	V
Base-Emitter Saturation Voltage		V <sub>BE</sub> (sat)	I <sub>C</sub> = 12 A, I <sub>B</sub> = 3 A	—	1.0	1.5	V
Transition Frequency		f <sub>T</sub>	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 0.1 A	—	2	—	MHz
Collector Output Capacitance		C <sub>ob</sub>	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz	—	240	—	pF
Switching Time	Storage Time	t <sub>stg</sub> (1)	I <sub>CP</sub> = 9 A, I <sub>B1</sub> (end) = 1.1 A f <sub>H</sub> = 32 kHz	—	3.5	4	μs
	Fall Time	t <sub>f</sub> (1)		—	0.25	0.35	
	Storage Time	t <sub>stg</sub> (2)	I <sub>CP</sub> = 6.5 A, I <sub>B1</sub> (end) = 1 A f <sub>H</sub> = 100 kHz	—	1.8	2	μs
	Fall Time	t <sub>f</sub> (2)		—	0.1	0.15	

## Marking









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