

- State-of-the-Art BiCMOS Design Significantly Reduces  $I_{CCZ}$
- 3-State Outputs Drive Bus Lines or Buffer Memory Address Registers
- ESD Protection Exceeds 2000 V per MIL-STD-883C, Method 3015
- Package Options Include Standard Plastic and Ceramic 300-mil DIPs

#### description

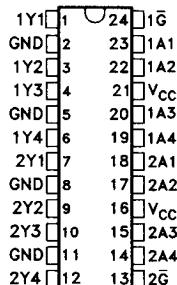
The 'BCT25244 is an octal noninverting buffer/line driver. The outputs are designed to source up to 80 mA and to sink up to 188 mA in order to facilitate incident-wave switching of transmission-line impedances down to  $25 \Omega$ .

When the output-enable inputs  $1\bar{G}$  and  $2\bar{G}$  are low, the device transmits data from the A inputs to the Y outputs. When  $1\bar{G}$  and  $2\bar{G}$  are high, the outputs are in the high-impedance state.

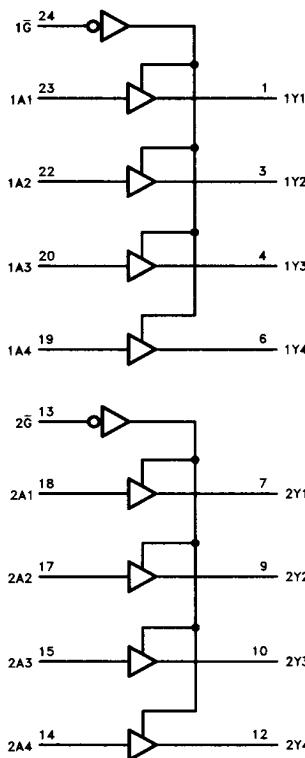
The distributed V<sub>CC</sub> and GND pins of the 'BCT25244 reduce switching noise for more reliable system operation.

The SN54BCT25244 is characterized for operation over the full military temperature range of  $-55^\circ\text{C}$  to  $125^\circ\text{C}$ . The SN74BCT25244 is characterized for operation from  $0^\circ\text{C}$  to  $70^\circ\text{C}$ .

SN54BCT25244 ... JT PACKAGE  
SN74BCT25244 ... NT PACKAGE  
(TOP VIEW)



#### logic diagram (positive logic)



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