

Product Features

- DC – 6000 MHz
- +17.5 dBm P1dB at 900 MHz
- +31 dBm OIP3 at 900 MHz
- 21 dB Gain at 900 MHz
- Single Voltage Supply
- SOT-89 SMT Package
- Internally matched to 50 Ω

Applications

- Mobile Infrastructure
- CATV / DBS
- W-LAN / ISM
- RFID
- Defense / Homeland Security
- Fixed Wireless

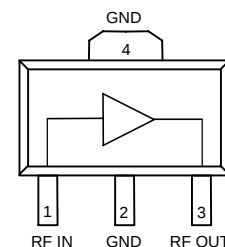
Product Description

The AG403-89 is a general-purpose buffer amplifier that offers high dynamic range in a low-cost surface-mount package. At 900 MHz, the AG403-89 typically provides 21 dB of gain, +31 dBm Output IP3, and +17.5 dBm P1dB. The device combines dependable performance with consistent quality to maintain MTTF values exceeding 100 years at mounting temperatures of +85°C and is housed in a SOT-89 industry-standard SMT package.

The AG403-89 consists of Darlington pair amplifiers using the high reliability InGaP/GaAs HBT technology process technology and only requires DC-blocking capacitors, a bias resistor, and an inductive RF choke for operation.

The broadband MMIC amplifier can be directly applied to various current and next generation wireless technologies such as GPRS, GSM, CDMA, and W-CDMA. In addition, the AG403-89 will work for other various applications within the DC to 6 GHz frequency range such as CATV and fixed wireless.

Functional Diagram



Function	Pin No.
Input	1
Output/Bias	3
Ground	2, 4

Specifications

Parameter	Units	Min	Typ	Max
Frequency Range	MHz	DC	900	6000
Gain (900 MHz)	dB		21.1	
Gain (1900 MHz)	dB	17.9	18.9	19.9
Input Return Loss	dB		13	
Output Return Loss	dB		15	
Output P1dB	dBm		+17.7	
Output IP3 (2)	dBm		+31.1	
Output IP2	dBm		+44	
Noise Figure	dB		3.1	
Device Voltage	V		4.91	
Device Current	mA		60	
Thermal Resistance	°C / W			207
Junction Temperature (3)	°C			177

Test conditions unless otherwise noted.

1. T = 25° C, Supply Voltage = +6 V, R_{bias} = 18.2 Ω , Frequency = 900 MHz, 50 Ω System.
2. 3OIP measured with two tones at an output power of 0 dBm/tone separated by 10 MHz. The suppression on the largest IM3 product is used to calculate the 3OIP using a 2:1 rule.
3. The junction temperature ensures a minimum MTTF rating of 1 million hours of usage.

Typical Performance

Parameter	Units	Typical			
Frequency	MHz	500	900	1900	2140
S21	dB	21.8	21.1	18.9	18.5
S11	dB	-14	-13	-14	-15
S22	dB	-24	-15	-10.4	-10.6
Output P1dB	dBm	+17.9	+17.7	+15.9	+15.3
Output IP3	dBm	+32.3	+31.1	+28.2	+27.6
Noise Figure	dB	2.9	3.1	3.3	3.3

Test conditions: T = 25° C, Supply Voltage = +6 V, R_{bias} = 18.2 Ω , 50 Ω System.

Absolute Maximum Rating

Parameter	Rating
Operating Case Temperature	-40 to +85 °C
Storage Temperature	-55 to +125 °C
DC Voltage	+6.2 V
RF Input Power (continuous)	+10 dBm
Junction Temperature	+250° C

Operation of this device above any of these parameters may cause permanent damage.

Ordering Information

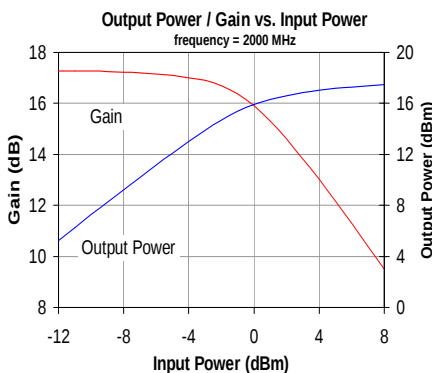
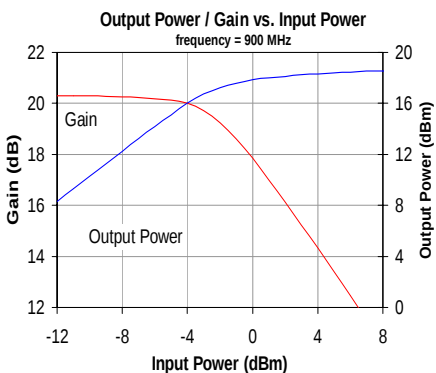
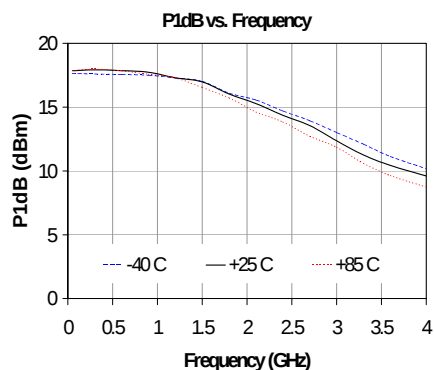
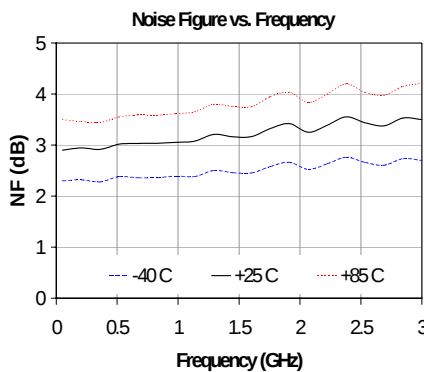
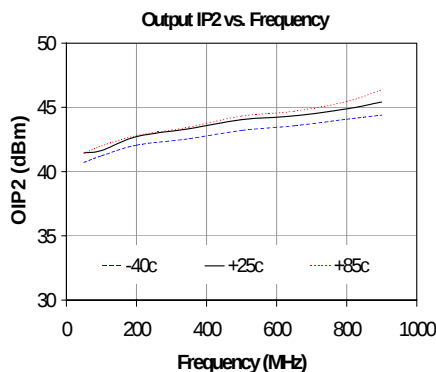
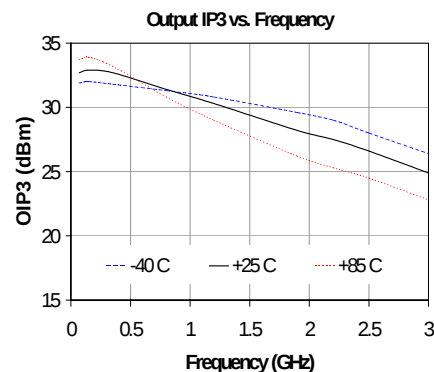
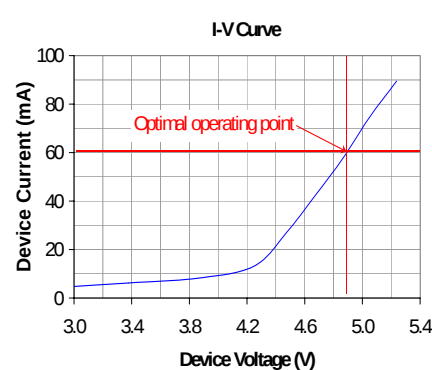
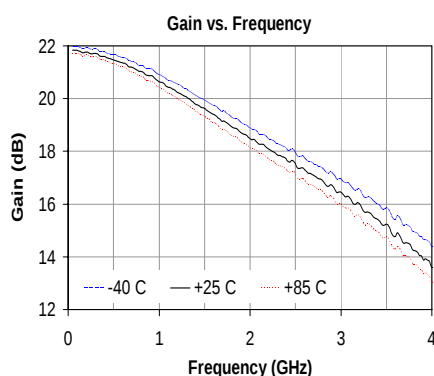
Part No.	Description
AG403-89	InGaP HBT Gain Block
AG403-89PCB	700 – 2400 MHz Fully Assembled Eval. Board

Typical Device RF Performance

Supply Bias = +6 V, $R_{bias} = 18.2 \Omega$, $I_{cc} = 60 \text{ mA}$

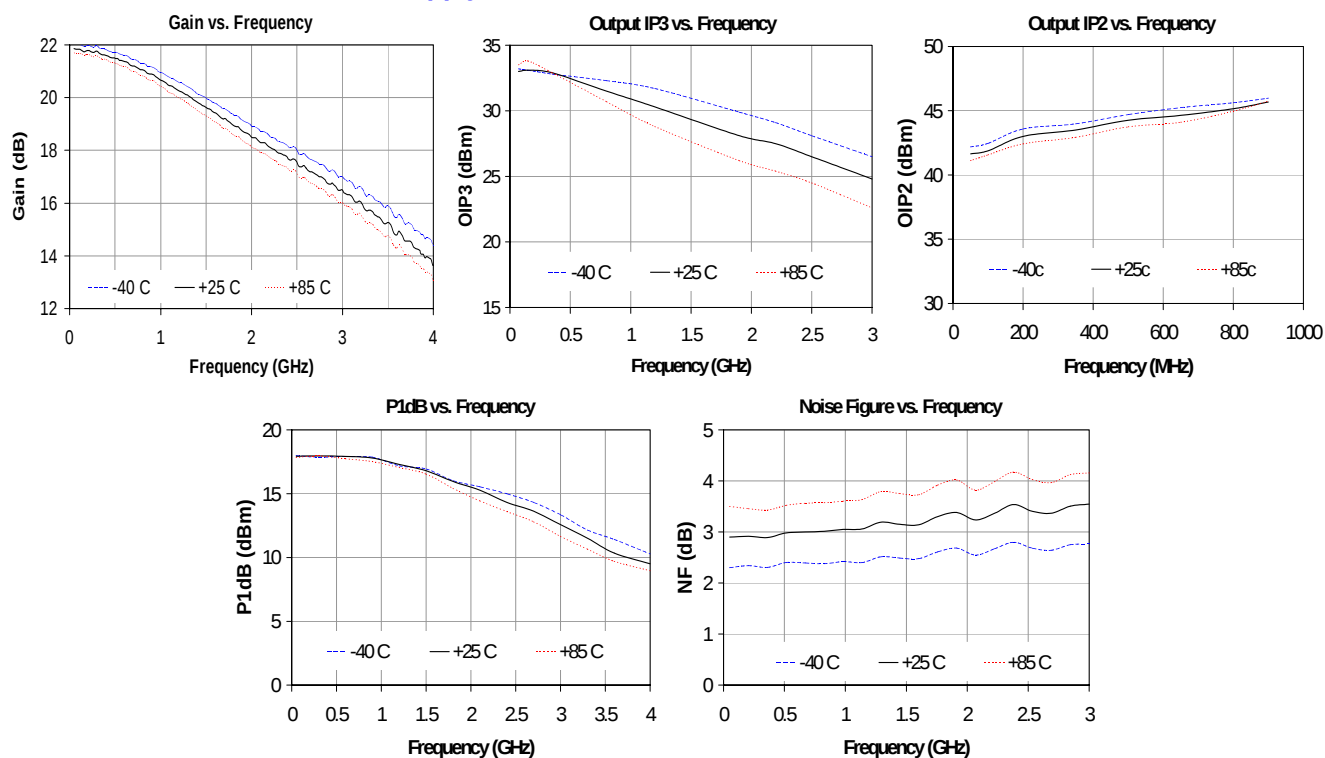
Frequency	MHz	100	500	900	1900	2140	2400	3500	5800
S21	dB	22.1	21.8	21.2	18.9	18.5	18.0	16.3	12.5
S11	dB	-18	-14	-13	-14	-15	-18	-28	-15
S22	dB	-25	-24	-15	-10.4	-10.6	-10.9	-14	-8
Output P1dB	dBm	+17.9	+17.9	+17.7	+15.9	+15.3	+14.4	+10.5	
Output IP3	dBm	+32.8	+32.3	+31.1	+28.2	+27.6	+26.9		
Noise Figure	dB	2.9	2.9	3.1	3.3	3.3	3.4		

1. Test conditions: $T = 25^\circ \text{C}$, Supply Voltage = +6 V, Device Voltage = 4.91 V, $R_{bias} = 18.2 \Omega$, $I_{cc} = 60 \text{ mA}$ typical, 50 Ω System.
2. 3OIP measured with two tones at an output power of 0 dBm/tones separated by 10MHz. The suppression on the largest IM3 product is used to calculate the 3OIP using a 2:1 rule.
3. Data is shown as device performance only. Actual implementation for the desired frequency band will be determined by external components shown in the application circuit.

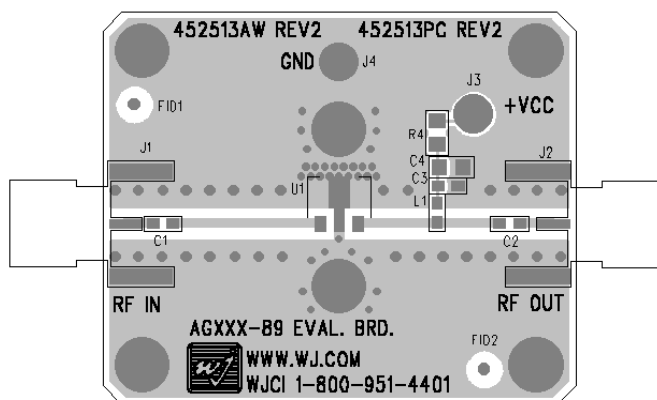
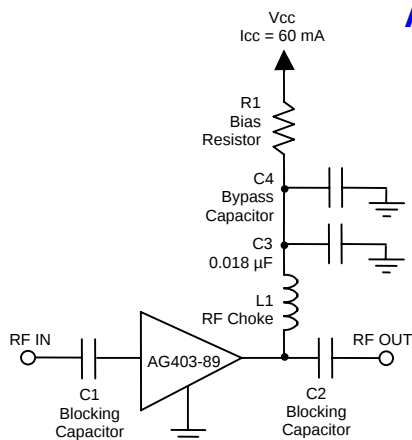


Typical Device RF Performance (cont'd)

Supply Bias = +8 V, $R_{bias} = 51 \Omega$, $I_{cc} = 60 \text{ mA}$



Application Circuit



Recommended Component Values

Reference Designator	Frequency (MHz)						
	50	500	900	1900	2200	2500	3500
L1	820 nH	220 nH	68 nH	27 nH	22 nH	18 nH	15 nH
C1, C2, C4	.018 µF	1000 pF	100 pF	68 pF	68 pF	56 pF	39 pF

- The proper values for the components are dependent upon the intended frequency of operation.
- The following values are contained on the evaluation board to achieve optimal broadband performance:

Ref. Desig.	Value / Type	Size
L1	39 nH wirewound inductor	0603
C1, C2	56 pF chip capacitor	0603
C3	0.018 µF chip capacitor	0603
C4	Do Not Place	
R1	18.2 Ω 1% tolerance	0805

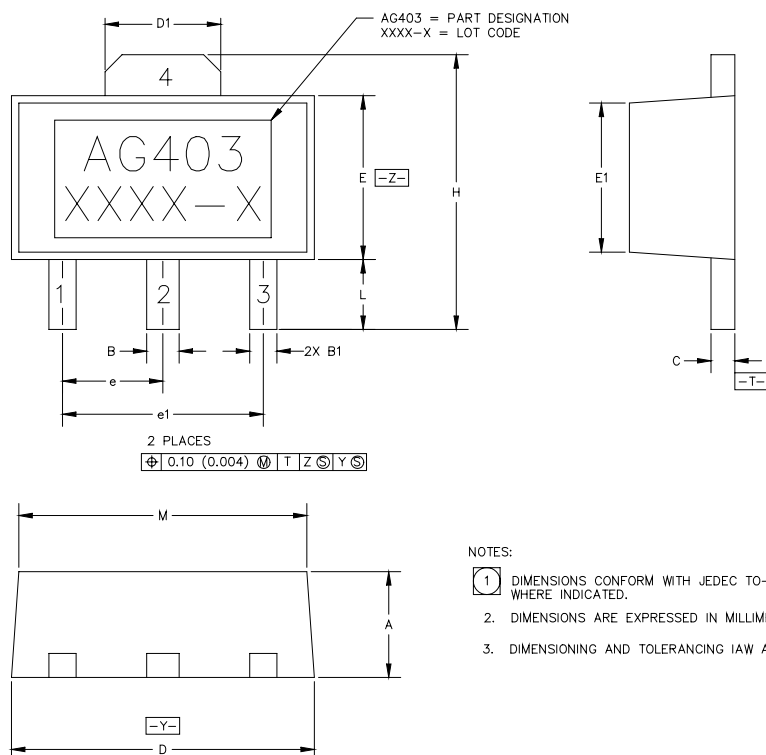
Recommended Bias Resistor Values

Supply Voltage	R1 value	Size
6 V	18.2 ohms	0805
7 V	34.8 ohms	1206
8 V	52 ohms	1210
9 V	68 ohms	1210
10 V	85 ohms	2010
12 V	118 ohms	2010

The proper value for R1 is dependent upon the supply voltage and allows for bias stability over temperature. WJ recommends a minimum supply bias of +6 V. A 1% tolerance resistor is recommended.

Specifications and information are subject to change without notice

Outline Drawing



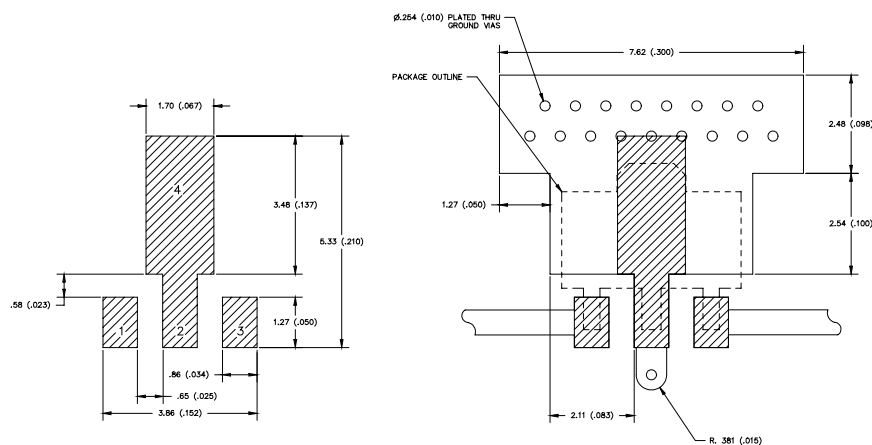
SYMBOL	MIN	MAX
A	1.40 (.055)	1.60 (.063)
B	.44 (.017)	.56 (.022)
B1	.36 (.014)	.48 (.019)
C	.35 (.014)	.44 (.017)
D	4.40 (.173)	4.60 (.181)
D1	1.62 (.064)	1.83 (.072)
E	2.29 (.090)	2.60 (.102)
E1	2.01 (.079)	2.29 (.090)
e	1.50 BSC (.059)	
e1	3.00 BSC (.118)	
H	3.94 (.155)	4.25 (.167)
L	.89 (.035)	1.20 (.047)
M	4.04 (.159)	4.19 (.165)

Product Marking

The component will be marked with an "AG403" designator with an alphanumeric lot code on the top surface of the package.

Tape and reel specifications for this part are located on the website in the "Application Notes" section.

Land Pattern



MSL / ESD Rating



Caution! ESD sensitive device.

ESD Rating: Class 1C
Value: Passes at 1000 V min.
Test: Human Body Model (HBM)
Standard: JEDEC Standard JESD22-A114

ESD Rating: Level 4
Value: Passes at 1000 V min.
Test: Charged Device Model (CDM)
Standard: JEDEC Standard JESD22-C101

MSL Rating: Level 3
Standard: JEDEC Standard J-STD-020A

Mounting Config. Notes

- GROUND/THERMAL VIAS ARE CRITICAL FOR THE PROPER PERFORMANCE OF THIS DEVICE. VIAS SHOULD USE A .35mm (#80/.0135") DIAMETER DRILL AND HAVE A FINAL, PLATED THRU DIAMETER OF .25mm (.010").
- ADD AS MUCH COPPER AS POSSIBLE TO INNER AND OUTER LAYERS NEAR THE PART TO ENSURE OPTIMAL THERMAL PERFORMANCE.
- ADD MOUNTING SCREWS NEAR THE PART TO FASTEN THE BOARD TO A HEATSINK. ENSURE THAT THE GROUND/THERMAL VIA REGION CONTACTS THE HEATSINK.
- DO NOT PUT SOLDER MASK ON THE BACK SIDE OF THE PC BOARD IN THE REGION WHERE THE BOARD CONTACTS THE HEATSINK.
- RF TRACE WIDTH DEPENDS UPON THE PC BOARD MATERIAL AND CONSTRUCTION.
- USE 1 OZ. COPPER MINIMUM.
- ALL DIMENSIONS ARE IN MILLIMETERS (INCHES). ANGLES ARE IN DEGREE



AG403-89

InGaP HBT Gain Block

The Communications Edge™

Product Information

Typical Device Data

S-Parameters ($V_{\text{device}} = +4.91 \text{ V}$, $I_{\text{CC}} = 60 \text{ mA}$, $T = 25^\circ \text{ C}$, calibrated to device leads)

Freq (MHz)	S11 (dB)	S11 (ang)	S21 (dB)	S21 (ang)	S12 (dB)	S12 (ang)	S22 (dB)	S22 (ang)
50	-18.06	179.79	21.71	176.98	-24.09	0.40	-24.87	-13.72
200	-18.00	162.07	21.60	168.63	-24.80	-2.09	-26.15	-31.34
400	-15.88	151.81	21.41	157.30	-25.08	-1.46	-26.28	-125.16
600	-14.16	144.50	21.18	146.55	-24.73	-4.98	-21.81	-150.37
800	-13.14	136.28	20.85	136.27	-25.01	-6.39	-17.43	-164.96
1000	-12.46	128.31	20.39	126.44	-24.95	-9.99	-14.78	-173.27
1200	-11.77	126.07	19.99	116.97	-25.18	-7.81	-12.79	-178.65
1400	-11.76	121.44	19.55	108.73	-24.83	-8.09	-11.66	177.17
1600	-12.19	119.08	19.07	100.30	-24.56	-8.70	-10.84	174.87
1800	-13.21	117.69	18.62	92.81	-24.54	-9.08	-10.49	173.17
2000	-14.50	113.06	18.22	85.14	-24.40	-10.37	-10.38	172.92
2200	-15.73	92.78	17.87	77.98	-24.46	-11.40	-10.62	-174.44
2400	-18.84	90.66	17.50	70.69	-24.05	-12.45	-10.94	-176.38
2600	-20.91	86.20	17.26	65.75	-23.99	-17.67	-11.43	-177.94
2800	-26.39	74.33	16.92	58.47	-23.40	-17.55	-12.05	178.15
3000	-33.53	31.90	16.59	50.79	-23.33	-18.82	-12.81	171.21
3200	-31.29	-6.38	16.24	43.76	-23.08	-19.04	-13.60	162.52
3400	-28.28	-12.01	15.92	36.24	-22.95	-22.98	-13.98	148.23
3600	-27.13	7.99	15.58	28.94	-22.73	-27.62	-13.93	130.33
3800	-24.00	29.15	15.23	21.76	-22.31	-31.86	-12.87	113.55
4000	-20.10	40.29	14.80	14.35	-22.29	-35.48	-11.51	99.23
4200	-17.35	45.76	14.36	7.42	-22.24	-38.20	-10.04	88.87
4400	-15.03	48.62	13.92	0.66	-21.92	-41.07	-8.65	81.23
4600	-13.62	48.88	13.48	-6.25	-22.10	-44.85	-7.89	75.64
4800	-12.84	49.53	13.08	-12.47	-22.05	-49.13	-7.36	70.54
5000	-12.39	49.21	12.75	-18.86	-21.74	-50.61	-6.98	68.07
5200	-12.68	47.67	12.49	-24.71	-21.66	-54.65	-7.06	64.15
5400	-13.14	46.17	12.23	-30.45	-21.22	-57.84	-7.34	60.90
5600	-13.88	44.73	12.17	-38.29	-20.74	-62.12	-10.48	-14.30
5800	-15.35	41.74	11.96	-43.15	-20.67	-64.22	-8.42	52.62
6000	-16.66	35.71	11.80	-49.72	-20.06	-69.98	-9.09	45.11

Device S-parameters are available for download off of the website at: <http://www.wj.com>