

MKP3V120, MKP3V240

Sidac High Voltage Bidirectional Triggers

Bidirectional devices designed for direct interface with the AC power line. Upon reaching the breakover voltage in each direction, the device switches from a blocking state to a low voltage on-state. Conduction will continue like a Triac until the main terminal current drops below the holding current. The plastic axial lead package provides high pulse current capability at low cost. Glass passivation insures reliable operation.

Features

- High Pressure Sodium Vapor Lighting
- Strokes and Flashers
- Ignitors
- High Voltage Regulators
- Pulse Generators
- Used to Trigger Gates of SCR's and Triacs
-  Indicates UL Registered – File #E210057
- These are Pb-Free Devices*

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Peak Repetitive Off-State Voltage (Sine Wave, 50 to 60 Hz, $T_J = -40$ to 125°C) MKP3V120 MKP3V240	V_{DRM} , V_{RRM}	± 90 ± 180	V
On-State RMS Current ($T_L = 80^\circ\text{C}$, Lead Length = $3/8"$, All Conduction Angles)	$I_{T(RMS)}$	± 1.0	A
Peak Non-Repetitive Surge Current (60 Hz One Cycle Sine Wave, Peak Value, $T_J = 125^\circ\text{C}$)	I_{TSM}	± 20	A
Operating Junction Temperature Range	T_J	-40 to $+125$	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-40 to $+150$	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Lead (Lead Length = $3/8"$)	$R_{\theta JL}$	15	$^\circ\text{C/W}$
Lead Solder Temperature (Lead Length $\geq 1/16"$ from Case, 10 s Max)	T_L	260	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

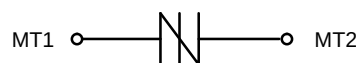
*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



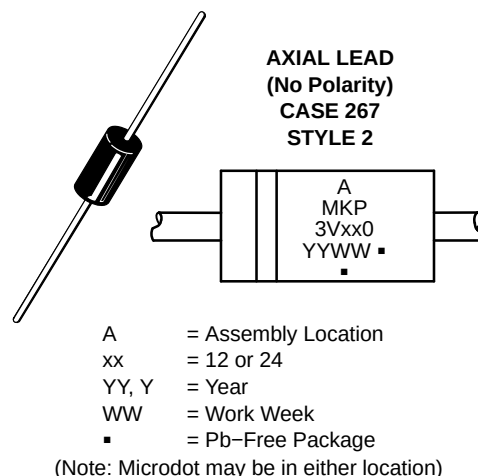
ON Semiconductor®

<http://onsemi.com>

SIDACS ()
1 AMPERE RMS
120 and 240 VOLTS



MARKING DIAGRAM



ORDERING INFORMATION

Device	Package	Shipping†
MKP3V120G	Axial Lead*	500 Units/Box
MKP3V120RLG	Axial Lead*	1500/Tape & Reel
MKP3V240G	Axial Lead*	500 Units/Box
MKP3V240RLG	Axial Lead*	1500/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MKP3V120, MKP3V240

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted; Electricals apply in both directions)

Characteristic	Symbol	Min	Typ	Max	Unit
----------------	--------	-----	-----	-----	------

OFF CHARACTERISTICS

Repetitive Peak Off-State Current (50 to 60 Hz Sine Wave) $V_{\text{DRM}} = 90\text{ V}$ $V_{\text{DRM}} = 180\text{ V}$	I_{DRM}	–	–	10	μA
	MKP3V120 MKP3V240				

ON CHARACTERISTICS

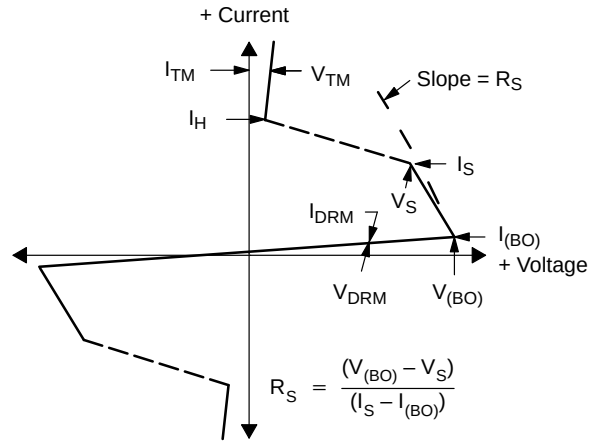
Breakover Voltage, $I_{\text{BO}} = 200\text{ }\mu\text{A}$	V_{BO}	110 220	– –	130 250	V
	MKP3V120 MKP3V240				
Breakover Current	I_{BO}	–	–	200	μA
Peak On-State Voltage ($I_{\text{TM}} = 1\text{ A Peak}$, Pulse Width $\leq 300\text{ }\mu\text{s}$, Duty Cycle $\leq 2\%$)	V_{TM}	–	1.1	1.5	V
Dynamic Holding Current (Sine Wave, 60 Hz, $R_L = 100\text{ }\Omega$)	I_{H}	–	–	100	mA
Switching Resistance (Sine Wave, 50 to 60 Hz)	R_S	0.1	–	–	$\text{k}\Omega$

DYNAMIC CHARACTERISTICS

Critical Rate-of-Rise of On-State Current, Critical Damped Waveform Circuit ($I_{\text{PK}} = 130\text{ }\Omega$, Pulse Width = $10\text{ }\mu\text{sec}$)	di/dt	–	120	–	$\text{A}/\mu\text{s}$
---	---------	---	-----	---	------------------------

Voltage Current Characteristic of SIDAC (Bidirectional Device)

Symbol	Parameter
I_{DRM}	Off State Leakage Current
V_{DRM}	Off State Repetitive Blocking Voltage
V_{BO}	Breakover Voltage
I_{BO}	Breakover Current
I_{H}	Holding Current
V_{TM}	On State Voltage
I_{TM}	Peak on State Current



MKP3V120, MKP3V240

CURRENT DERATING

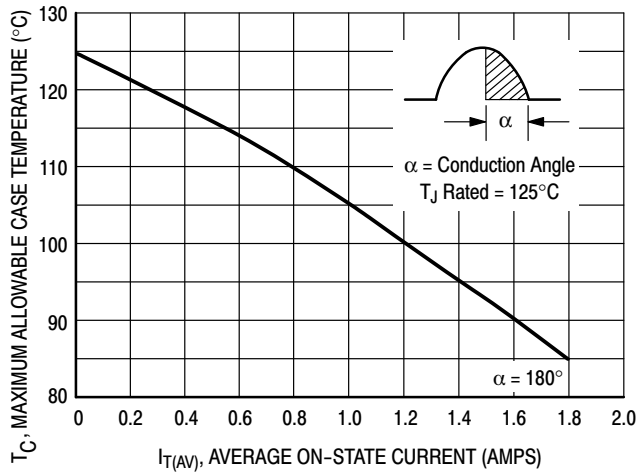


Figure 1. Maximum Case Temperature

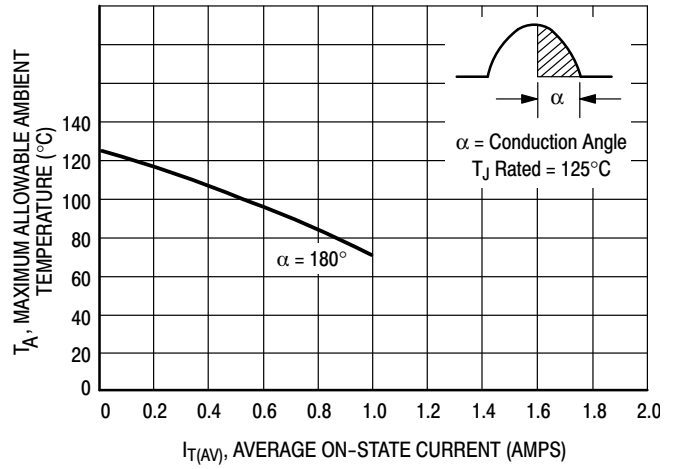


Figure 2. Maximum Ambient Temperature

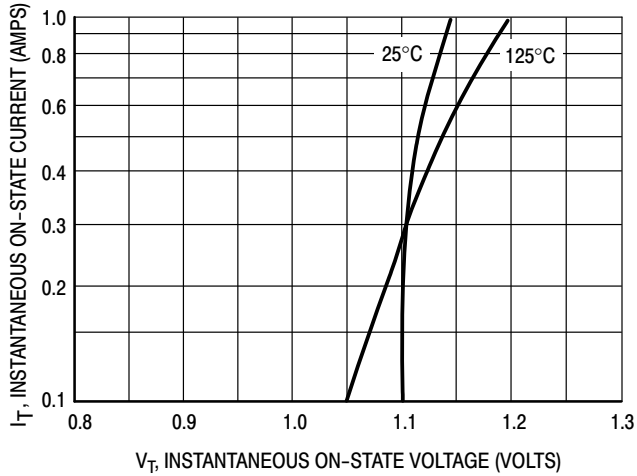


Figure 3. Typical Forward Voltage

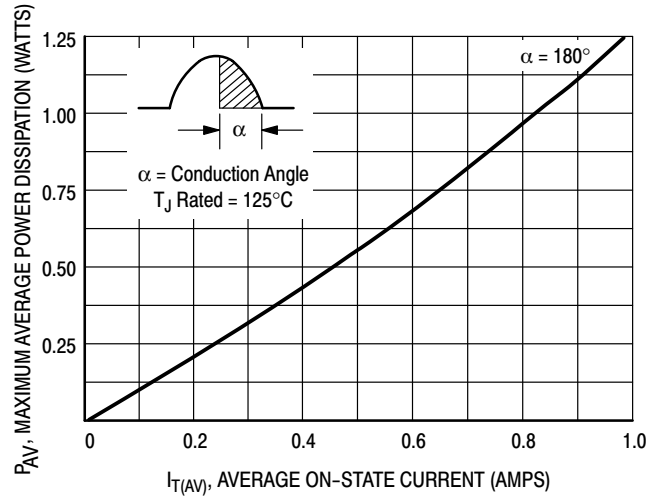


Figure 4. Typical Power Dissipation

MKP3V120, MKP3V240

THERMAL CHARACTERISTICS

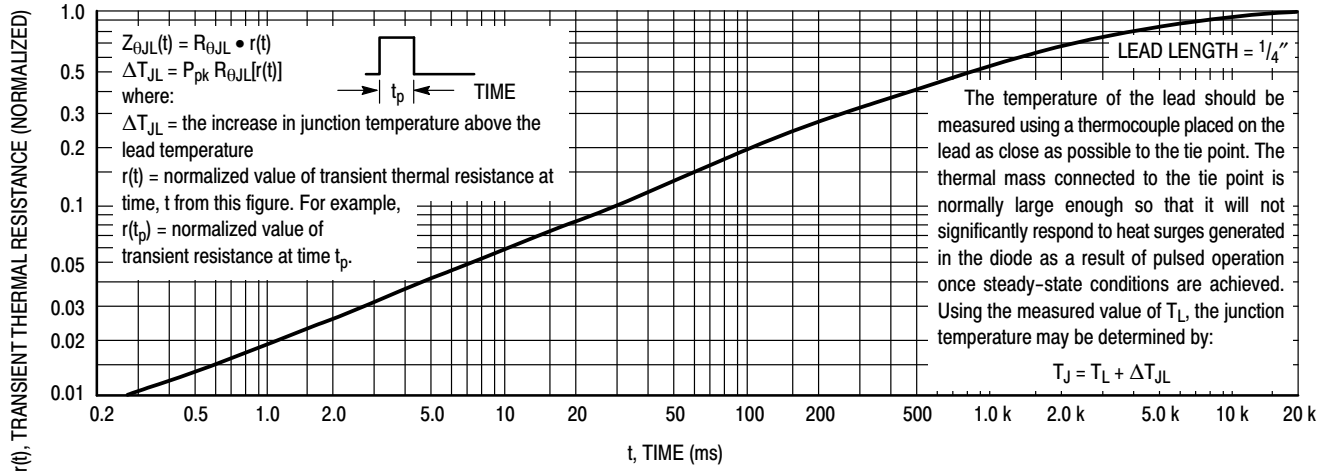


Figure 5. Thermal Response

TYPICAL CHARACTERISTICS

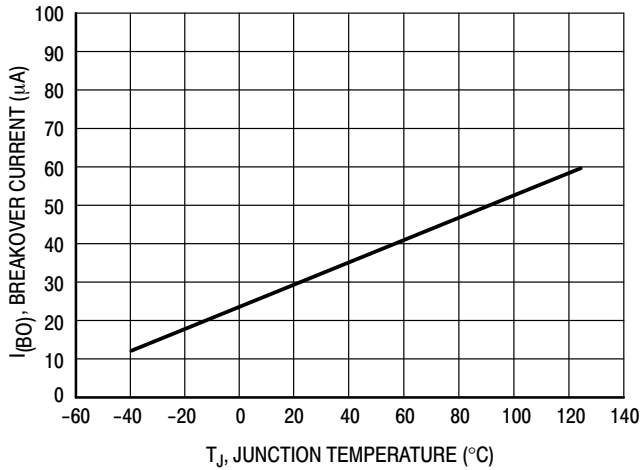


Figure 6. Typical Breakover Current

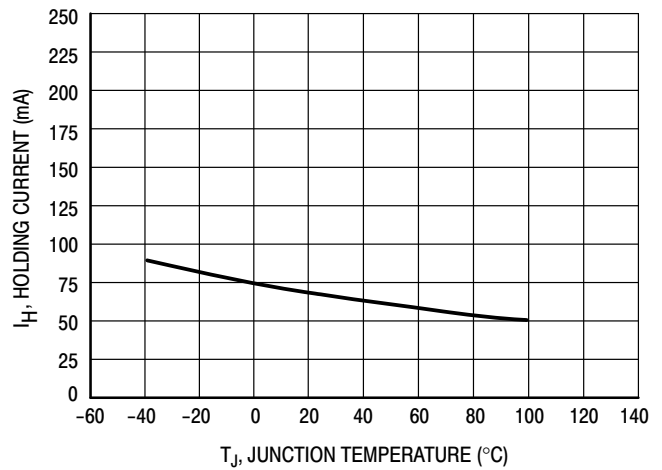
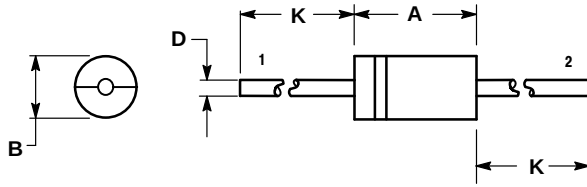


Figure 7. Typical Holding Current

MKP3V120, MKP3V240

PACKAGE DIMENSIONS

AXIAL LEAD CASE 267-05 ISSUE G



NOTES:

1. DIMENSIONS AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. 267-04 OBSOLETE, NEW STANDARD 267-05.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.287	0.374	7.30	9.50
B	0.189	0.209	4.80	5.30
D	0.047	0.051	1.20	1.30
K	1.000	---	25.40	---

STYLE 2:

NO POLARITY

ON Semiconductor and  are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor
P.O. Box 5163, Denver, Colorado 80217 USA
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: <http://www.onsemi.com/orderlit>

For additional information, please contact your local Sales Representative

MKP3V120/D