

Products > Clock ICs and Timing Solutions > Clock Generators, Synthesizers and Zero Delay Buffers > General Purpose Clock Generators, Synthesizers and Zero Delay Buffers > DB1900GS Frequency Gearing Clock for Intel CPU, PCIe Gen2 & Fully Buffered DIMM Clocks

Status: **Active**

Product Quick Links

- Specifications
- Product Options
- Documentation & Tools
- Technical Support
- Contact Sales

9FG1900-1
DB1900GS Frequency Gearing Clock for Intel CPU, PCIe Gen2 & Fully
Buffered DIMM Clocks

The ICS9FG1900B-1 follows the Intel DB1900G v0.96 Differential Buffer Specification. This buffer provides 19 output clocks for CPU Host Bus, PCI-Express, or Fully Buffered DIMM applications. The outputs are configured with two groups. Both groups, DIF_(16:0) and DIF_(18:17) can be equal to or have a gear ratio to the input clock. A differential CPU clock from a CK410B+ main clock generator, such as the ICS932S421, drives the ICS9FG1900B-1. The ICS9FG1900B-1 can provide outputs up to 400MHz.

Features

- Power up default is all outputs in 1:1 mode
- DIF_(16:0) can be “gear-shifted” from the input CPU Host Clock
- DIF_(18:17) can be “gear-shifted” from the input CPU Host Clock
- Spread spectrum compatible
- Supports output clock frequencies up to 400 MHz
- 8 Selectable SMBus addresses
- SMBus address determines PLL or Bypass mode
- VDDA controlled power down mode

Product Specification

App Jitter Compliance	Core Voltage (V)	Diff. Output Signaling	Input Freq (MHz)	Output Freq Range (MHz)	Output Voltage (V)	Power Typ (mW)	Supply Voltage (V)
FBD, PCIe Gen2, PCIe Gen1	3.3	HCSL	100.000000 - 133.330000	66.670000 - 400.000000	0.8		3.30

Product Options

Orderable Part ID	Part Status	Pkg. Code	Pkg. Type	Lead Count (#)	Temp. Grade	Pb (Lead) Free	Sample & Buy
9FG1900BK-1LF	Active	NLG72	VFQFPN	72	C	Yes	Check Availability

Documents

	Title	Type ▲	Format	File Size	Date
	PC Clocks Contact Info	Misc	PDF	62 KB	05/29/2007
	IDT PCI Express Solutions Overview	Overview	PDF	2.47 MB	09/14/2011
	PCN# : K1009-01 CHANGE OF NEW ORDERING PART FOR 9FG1900AK-1LF	Product Change Notice	PDF	62 KB	10/10/2010

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