

Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)} \text{ max}$	$I_D \text{ max}$ $T_A = +25^\circ\text{C}$
-20V	75mΩ @ $V_{GS} = -4.5\text{V}$	-3.3A
	140mΩ @ $V_{GS} = -1.8\text{V}$	-2.4A

Features and Benefits

- Low On-Resistance
- Very Low Gate Threshold Voltage $V_{GS(th)} \leq 1\text{V}$
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q 101 Standards for High Reliability**

Description and Applications

This MOSFET has been designed to minimize the on-state resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Battery Charging
- Power Management Functions
- DC-DC Converters
- Portable Power Adaptors

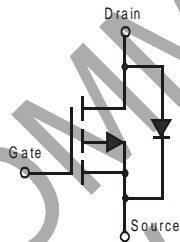
Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram Below
- Terminals: Finish - Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.008 grams (approximate)

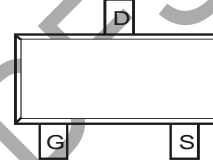
SOT23



Top View



Internal Schematic



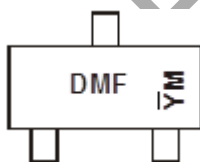
Top View

Ordering Information (Note 4)

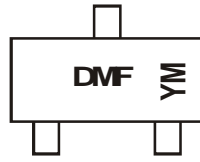
Part Number	Case	Packaging
DMP2160U-7	SOT23	3000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>

Marking Information



Chengdu A/T Site



Shanghai A/T Site

DMF = Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site)

Y or Y̅ = Year (ex: A = 2013)

M = Month (ex: 9 = September)

Date Code Key

Year	2008	2009	2010	2011	2012	2013	2014	2015
Code	V	W	X	Y	Z	A	B	C

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V _{DSS}	-20	V
Gate-Source Voltage	V _{GSS}	±12	V
Continuous Drain Current (Note 5) V _{GS} = -4.5V	I _D	-3.3 -2.6	A
Pulsed Drain Current	I _{DM}	-13	A

Thermal Characteristics

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P _D	1.4	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	90	°C/W
Thermal Resistance, Junction to Case (Note 5)	R _{θJC}	22	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	—	—	-1.0	μA	V _{DS} = -16V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100 ±800	nA	V _{GS} = ±8V, V _{DS} = 0V V _{GS} = ±12V, V _{DS} = 0V
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(th)}	-0.4	-0.6	-0.9	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	60	75	mΩ	V _{GS} = -4.5V, I _D = -1.5A
			73	96		V _{GS} = -2.5V, I _D = -1.2A
			92	140		V _{GS} = -1.8V, I _D = -1.2A
Forward Transconductance	g _{FS}	—	7	—	S	V _{DS} = -10V, I _D = -1.5A
Diode Forward Voltage (Note 5)	V _{SD}	—	—	-1.0	V	V _{GS} = 0V, I _S = -1.0A
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C _{iss}	—	627	—	pF	V _{DS} = -10V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	64	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	53	—	pF	
Gate Resistance	R _G	—	44.9	—	Ω	V _{GS} = 0V, V _{DS} = 0V, f = 1.0MHz
Total Gate Charge	Q _g	—	6.5	—	nC	V _{GS} = -4.5V, V _{DS} = -10V, I _D = -3A
Gate-Source Charge	Q _{gs}	—	0.9	—	nC	
Gate-Drain Charge	Q _{gd}	—	1.5	—	nC	
Turn-On Delay Time	t _{D(on)}	—	12.5	—	ns	V _{DS} = -10V, V _{GS} = -4.5V, R _L = 10Ω, R _G = 1.0Ω, I _D = -1A
Turn-On Rise Time	t _r	—	10.3	—	ns	
Turn-Off Delay Time	t _{D(off)}	—	46.5	—	ns	
Turn-Off Fall Time	t _f	—	22.2	—	ns	

- Notes:
- Device mounted on 1in² FR-4 PCB with 2 oz. Copper. t ≤ 10 sec.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

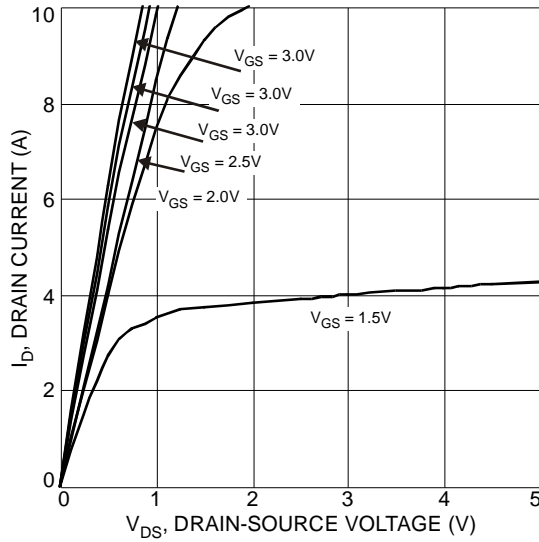


Fig. 1 Typical Output Characteristics

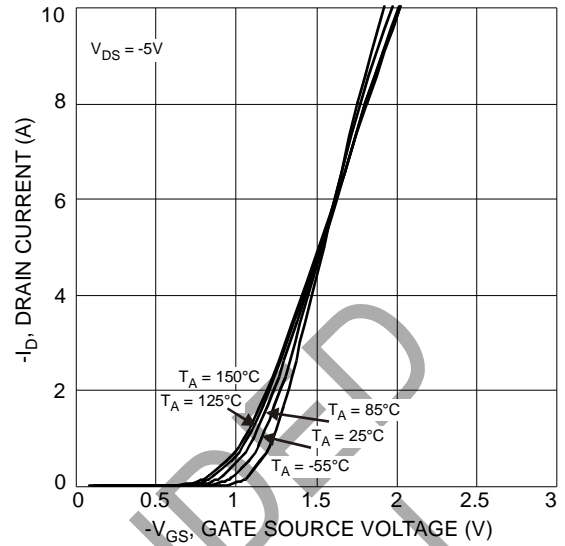


Fig. 2 Typical Transfer Characteristics

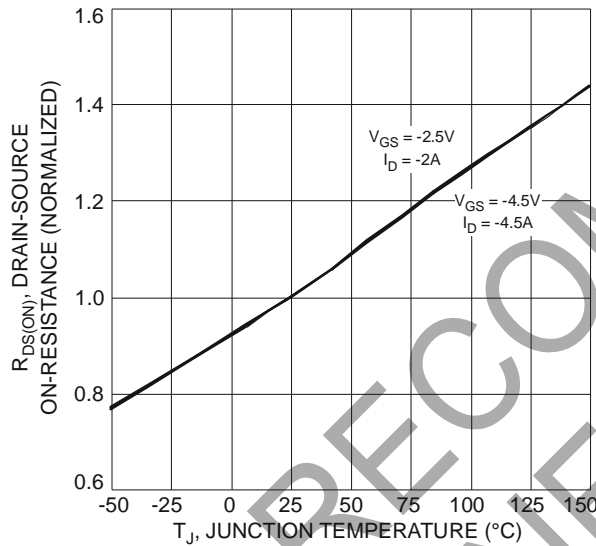


Fig. 3 On-Resistance Variation with Temperature

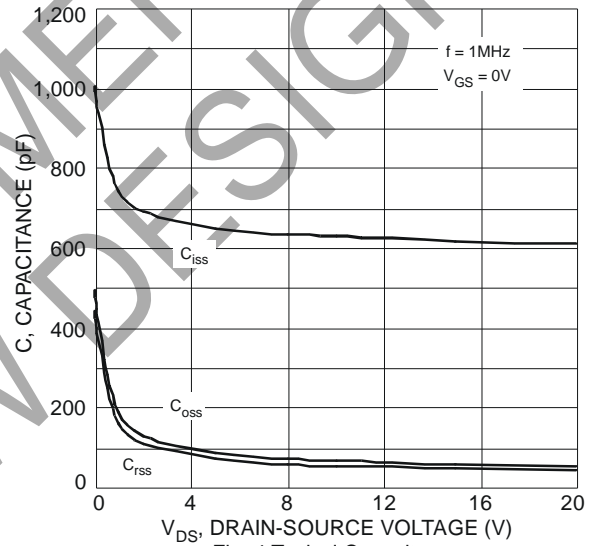


Fig. 4 Typical Capacitance

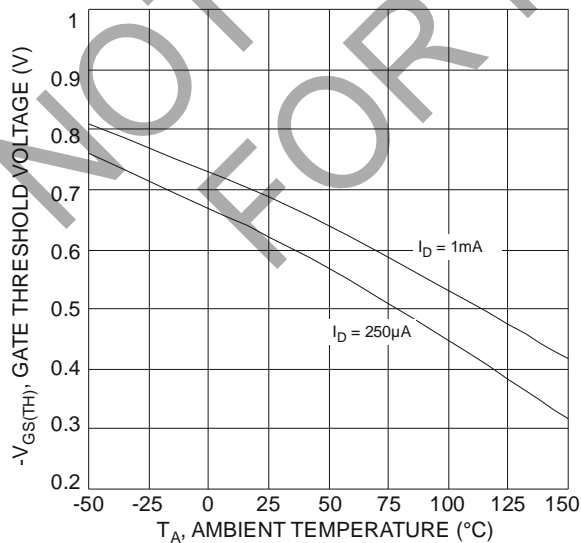


Fig. 5 Gate Threshold Variation vs. Ambient Temperature

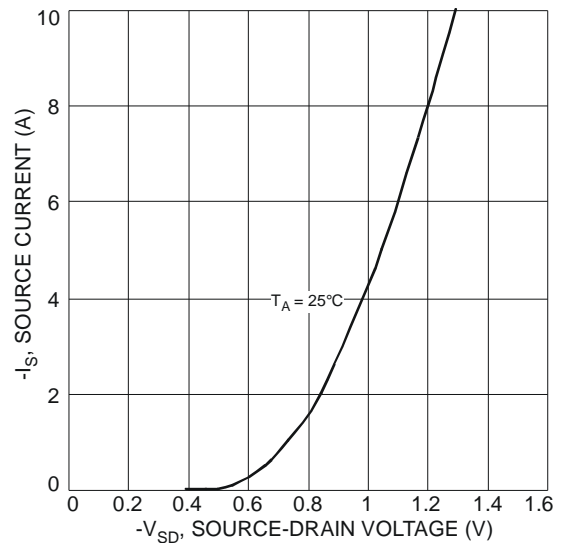


Fig. 6 Diode Forward Voltage vs. Current

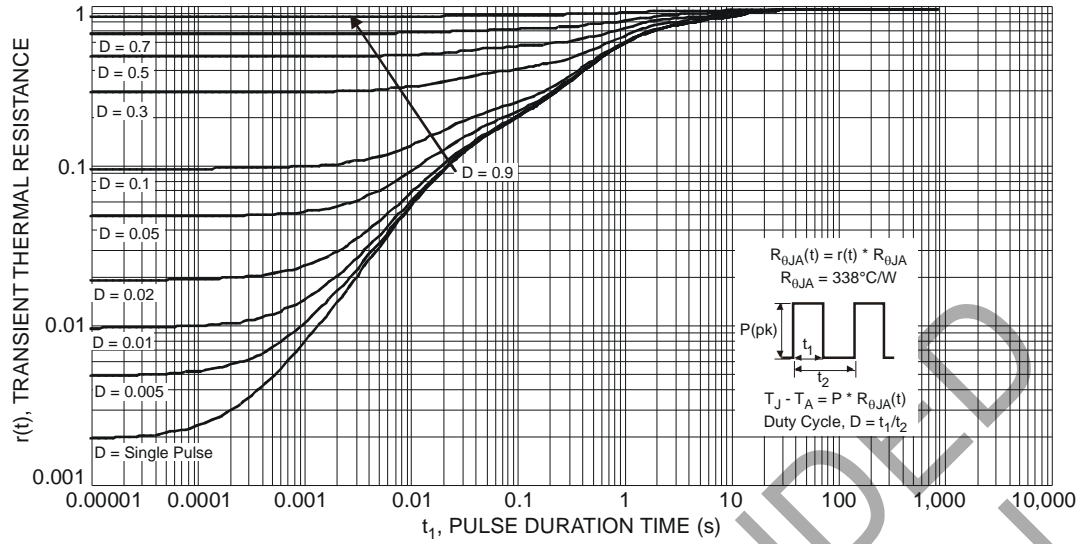
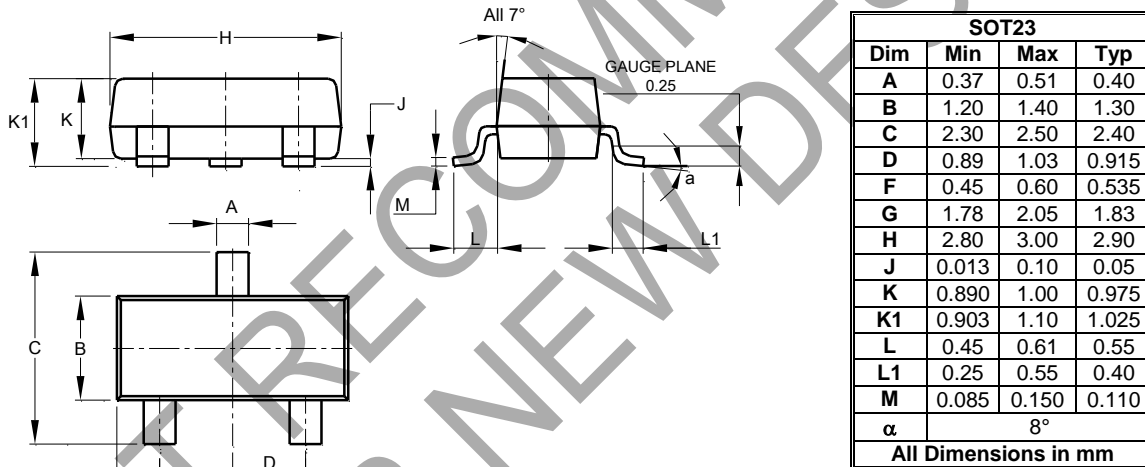


Fig. 7 Transient Thermal Response

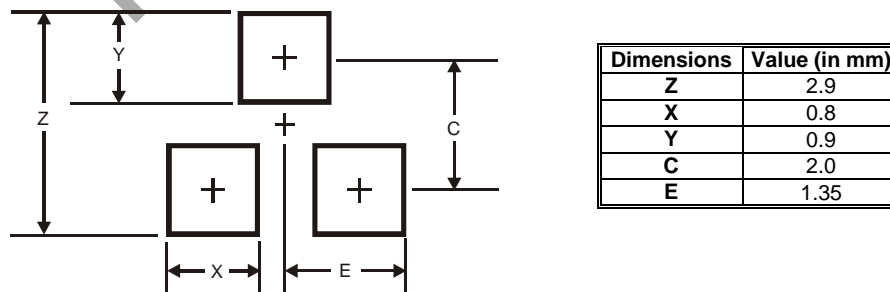
Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



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