

6

5

4

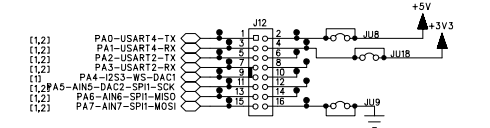
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2

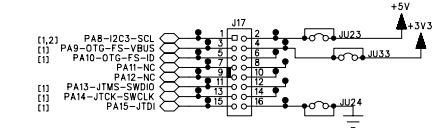
1

Port Expansion Connectors (for ribbon cables)
2x8 DIL Headers

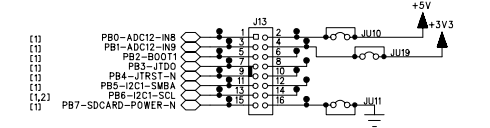
Port A0 to A7



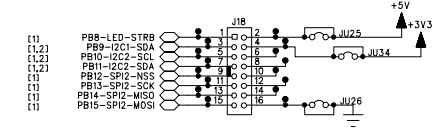
Port A8 to A15



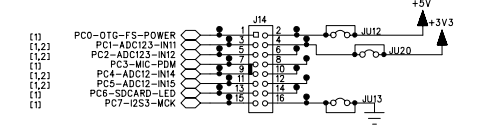
Port B0 to B7



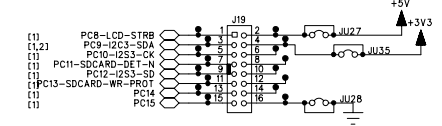
Port B8 to B15



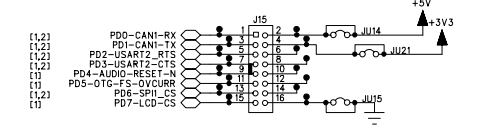
Port C0 to C7



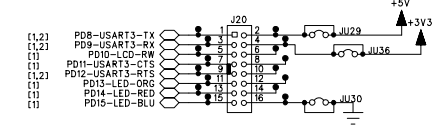
Port C8 to C15



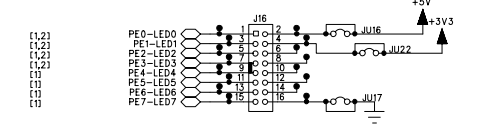
Port D0 to D7



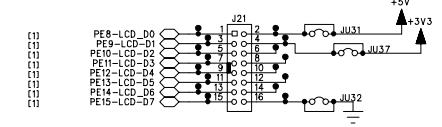
Port D8 to D15



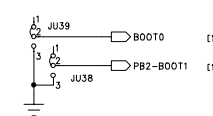
Port E0 to E7



Port E8 to E15



Boot Option Headers



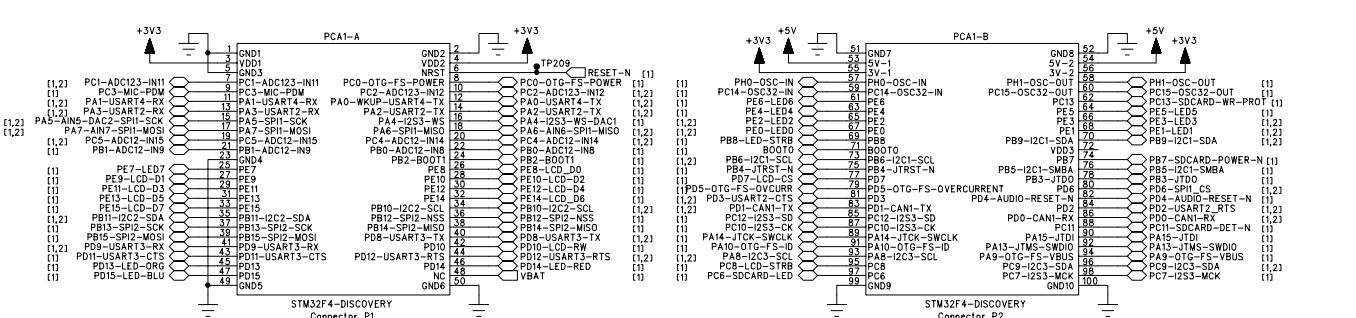
BOOT0 and BOOT1 have pullup resistors on the Discovery-VL board. The SB2 & SB16 bridges are installed by default on Discovery board, pulling BOOT0 & BOOT1 low (forcing user program to run). SB16 must be removed to use serial bootloader.

BOOT1	BOOT0	ACTION
X	LO	Run user program from Flash
LO	HI	Run USART1 Bootloader from ROM
HI	HI	Run SRAM program

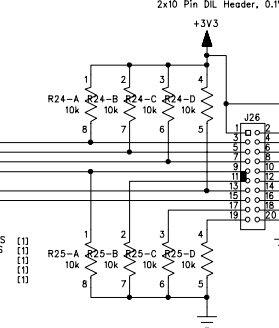
SOT-23 Pinout



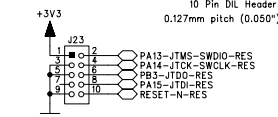
STM32F4 Discovery-F4 Board with STM32F407VCT6



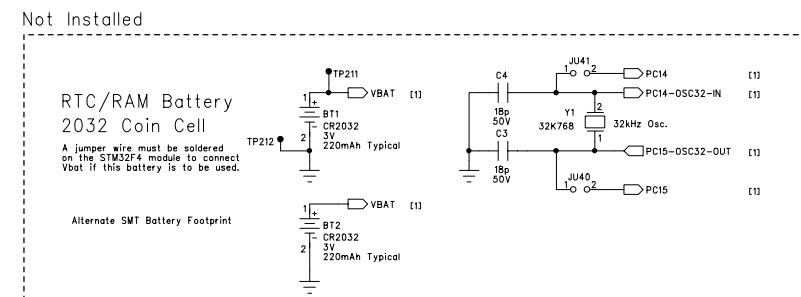
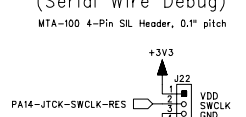
ARM 20-pin JTAG Header



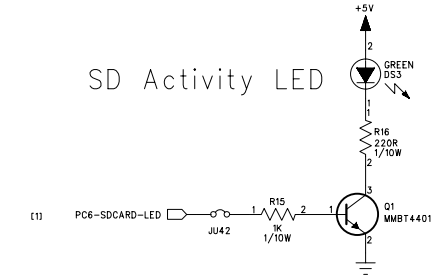
ARM 10-pin JTAG Header



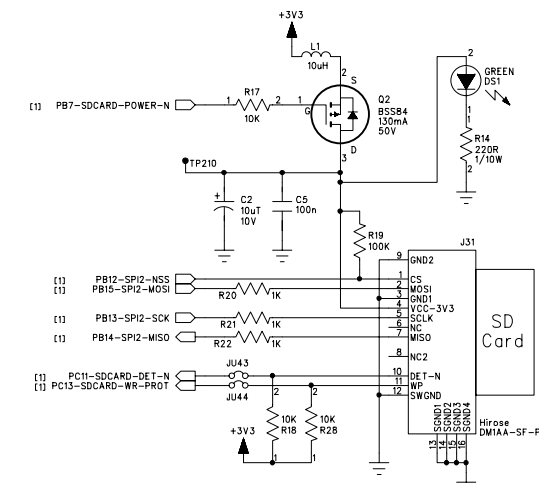
SWD Header (Serial Wire Debug)



SD Activity LED



Secure Digital Card



REV.	DATE	ECO NUMBER (DESCRIPTION)	APPR'D
1	Dec 2011		
2	Jun 2012		

DESIGNED	Scot Kornak	DATE	Dec 2011
DRAWN	Scot Kornak	DATE	Dec 2011
CHECKED	Scot Kornak	DATE	Jun 2012
APPROVED	Scot Kornak	DATE	Jun 2012

Kornak Technologies Inc.
www.Kornak.ca Calgary, Alberta, Canada

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CLASSIFICATION
RELEASED TO PUBLIC

TITLE
STM32F4 Discovery
BaseBoard F4B1

KTI DRAWING NUMBER - REVISION
SCH-(STM32-F4B1-1.00) Rev1.01

DRAWING IS FOR PART NUMBER - REVISION
PCB-STM32-F4B1-1.00
(PCB-640-1.00)

Designed by
Kornak Technologies
Calgary, Alberta
Contact: 403-567-9328
sales@kornak.ca www.kornak.ca

