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FDP6030L/FDB6030L

N-Channel Logic Level PowerTrench[®] MOSFET

General Description

This N-Channel Logic Level MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.

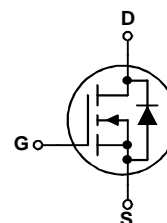
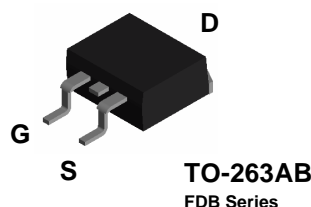
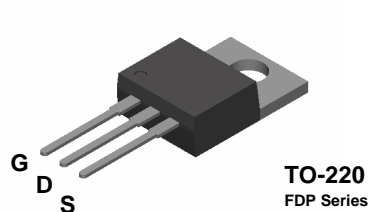
These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable $R_{DS(ON)}$ specifications.

The result is a MOSFET that is easy and safer to drive (even at very high frequencies), and DC/DC power supply designs with higher overall efficiency.

It has been optimized for low gate charge, low $R_{DS(ON)}$ and fast switching speed.

Features

- 48 A, 30 V $R_{DS(ON)} = 13 \text{ m}\Omega @ V_{GS} = 10 \text{ V}$
 $R_{DS(ON)} = 17 \text{ m}\Omega @ V_{GS} = 4.5 \text{ V}$
- Critical DC electrical parameters specified at elevated temperature
- High performance trench technology for extremely low $R_{DS(ON)}$
- 175°C maximum junction temperature rating



Absolute Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{DSS}	Drain-Source Voltage	30	V
V_{GSS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous (Note 1)	48	A
	– Pulsed	150	
P_D	Total Power Dissipation @ $T_C = 25^\circ\text{C}$	52	W
	Derate above 25°C	0.3	
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-65 to $+175$	$^\circ\text{C}$

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	2.9	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity
FDB6030L	FDB6030L	13"	24mm	800 units
FDP6030L	FDP6030L	Tube	n/a	45

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Drain-Source Avalanche Ratings (Note 1)						
E _{AS}	Single Pulse Drain-Source Avalanche Energy	V _{DD} = 15 V, I _D = 26 A			100	mJ
I _{AS}	Maximum Drain-Source Avalanche Current				26	A
Off Characteristics						
BV _{DSS}	Drain–Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	30			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		23		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 24 V, V _{GS} = 0 V			1	μA
I _{GSS}	Gate–Body Leakage	V _{GS} = ± 20 V, V _{DS} = 0 V			± 100	nA
On Characteristics (Note 2)						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	1	1.9	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		–5		mV/°C
R _{DS(on)}	Static Drain–Source On–Resistance	V _{GS} = 10 V, I _D = 26 A V _{GS} = 4.5 V, I _D = 21 A V _{GS} = 10 V, I _D = 26 A, T _J =125°C		7.9 10.2 13.0	13 17 20	mΩ
I _{D(on)}	On–State Drain Current	V _{GS} = 10 V, V _{DS} = 10 V	60			A
g _{FS}	Forward Transconductance	V _{DS} = 10V, I _D = 26 A		68		S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 15 V, V _{GS} = 0 V, f = 1.0 MHz		1250		pF
C _{oss}	Output Capacitance			330		pF
C _{rss}	Reverse Transfer Capacitance			155		pF
R _G	Gate Resistance	V _{GS} = 15 mV, f = 1.0 MHz		1.3		Ω
Switching Characteristics (Note 2)						
t _{d(on)}	Turn–On Delay Time	V _{DD} = 15V, I _D = 1 A, V _{GS} = 10 V, R _{GEN} = 6 Ω		11	20	ns
t _r	Turn–On Rise Time			12	22	ns
t _{d(off)}	Turn–Off Delay Time			29	46	ns
t _f	Turn–Off Fall Time			12	21	ns
Q _g	Total Gate Charge	V _{DS} = 15 V, I _D = 26 A, V _{GS} = 5 V		13	18	nC
Q _{gs}	Gate–Source Charge			3.9		nC
Q _{gd}	Gate–Drain Charge			5.2		nC
Drain–Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain–Source Diode Forward Current				48	A
V _{SD}	Drain–Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 26 A (Note 1)		0.92	1.3	V
t _{rr}	Diode Reverse Recovery Time	I _F = 26 A,		26		nS
Q _{rr}	Diode Reverse Recovery Charge	d _I F/d _t = 100 A/μs		15		nC

Notes:

1. Calculated continuous current based on maximum allowable junction temperature.
2. Pulse Test: Pulse Width < 300 μs , Duty Cycle < 2.0%

Typical Characteristics

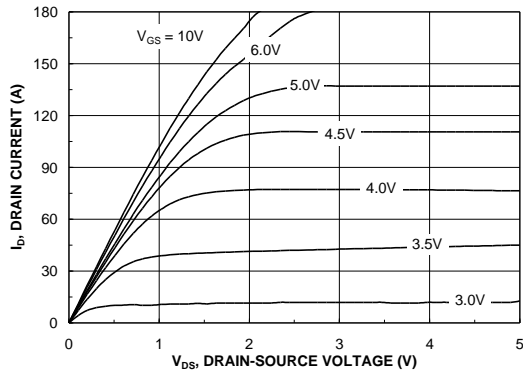


Figure 1. On-Region Characteristics.

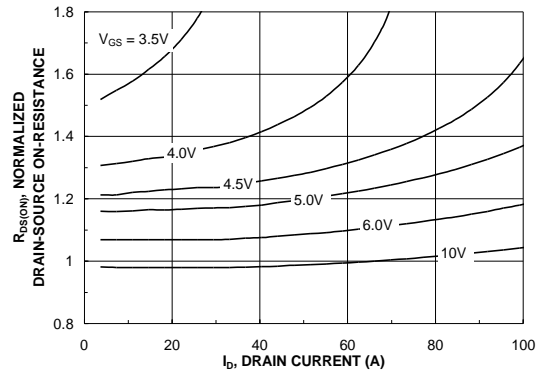


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

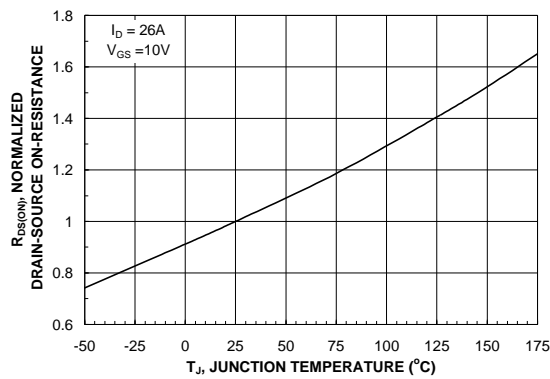


Figure 3. On-Resistance Variation with Temperature.

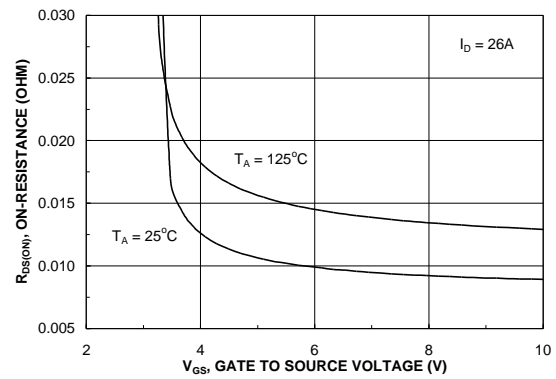


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

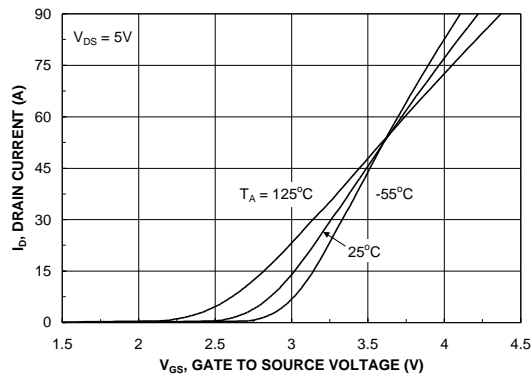


Figure 5. Transfer Characteristics.

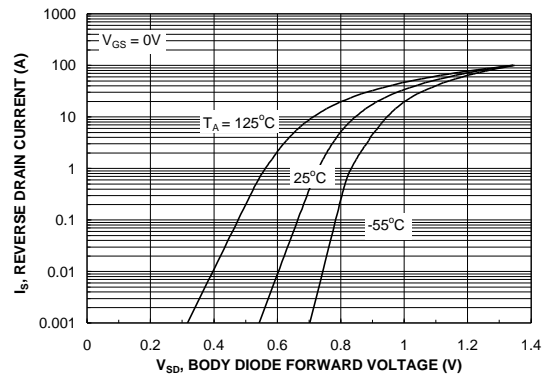


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

Typical Characteristics

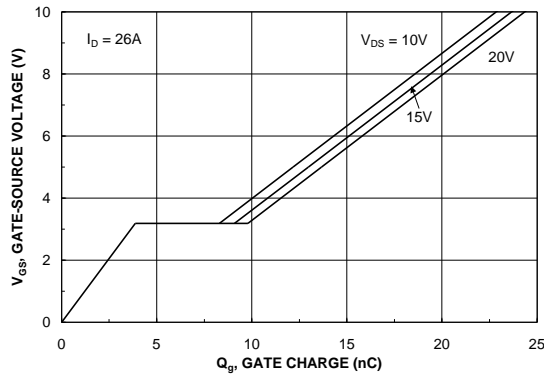


Figure 7. Gate Charge Characteristics.

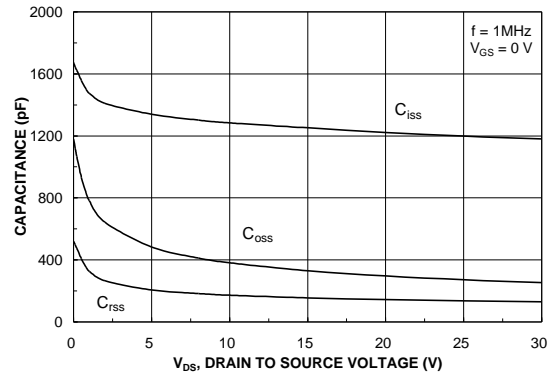


Figure 8. Capacitance Characteristics.

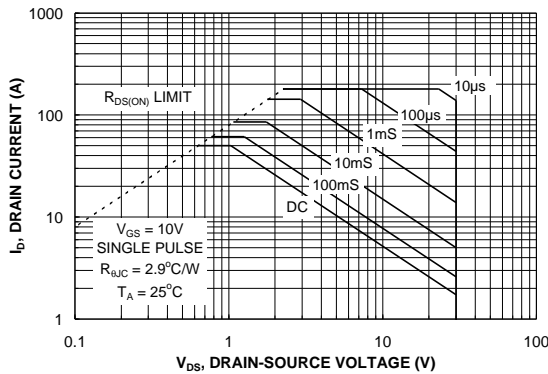


Figure 9. Maximum Safe Operating Area.

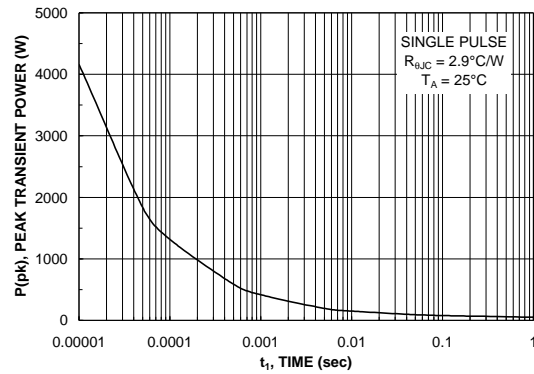


Figure 10. Single Pulse Maximum Power Dissipation.

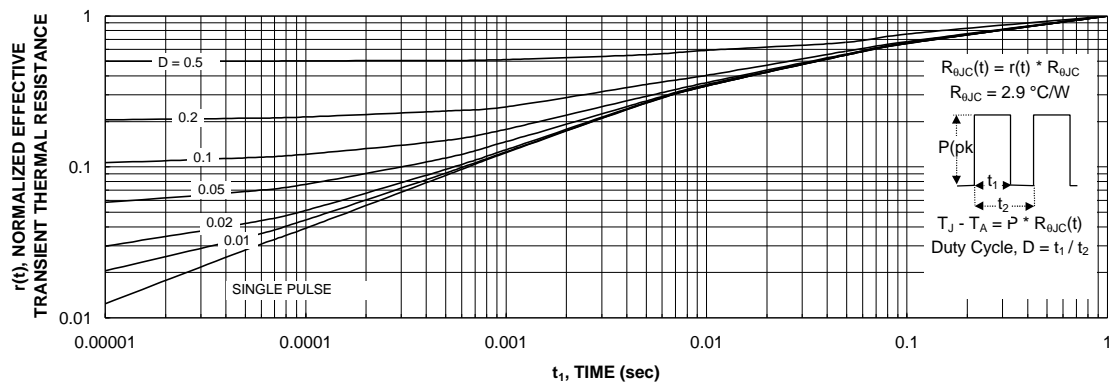


Figure 11. Transient Thermal Response Curve

Thermal characterization performed using the conditions described in Note 1b.
Transient thermal response will change depending on the circuit board design.

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