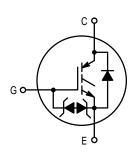
Designer's™ Data Sheet

Insulated Gate Bipolar Transistor

N-Channel Enhancement-Mode Silicon Gate

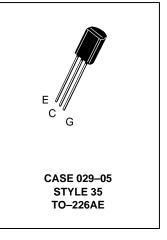
This IGBT contains a built–in free wheeling diode and a gate protection zener diodes. Fast switching characteristics result in efficient operation at higher frequencies. This device is ideally suited for high frequency electronic ballasts.

- Built-In Free Wheeling Diodes
- Built-In Gate Protection Zener Diode
- Industry Standard Package (TO92 1.0 Watt)
- High Speed E_{Off}: Typical 6.5 μ J @ I_C = 0.3 A; T_C = 125°C and dV/dt = 1000 V/ μ s
- Robust High Voltage Termination
- Robust Turn-Off SOA



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IGBT 0.5 A @ 25°C 600 V



MAXIMUM RATINGS ($T_C = 25^{\circ}C$ unless otherwise noted)

Parameters	Symbol	Value	Unit
Collector–Emitter Voltage	VCES	600	Vdc
Collector–Gate Voltage ($R_{GE} = 1.0 \text{ M}\Omega$)	^V CGR	600	Vdc
Gate-Emitter Voltage — Continuous	V _{GES}	±15	Vdc
Collector Current — Continuous @ T _C = 25°C — Continuous @ T _C = 90°C — Repetitive Pulsed Current (1)	I _{C25} I _{C90} I _{CM}	0.5 0.3 2.0	Adc
Total Power Dissipation	PD	1.0	Watt
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to 150	°C

THERMAL CHARACTERISTICS

Thermal Resistance — Junction to Case – IGBT — Junction to Ambient	R _{θJC} R _{θJA}	25 125	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 5 seconds	TL	260	°C

UNCLAMPED DRAIN-TO-SOURCE AVALANCHE CHARACTERISTICS (T_C ≤ 150°C)

Single Pulse Drain-to-Source Avalanche	E _{AS}		mJ
Energy – Starting @ T _C = 25°C		125	
$@ T_C = 125^{\circ}C$		40	
V_{CE} = 100 V, V_{GE} = 15 V, Peak I _L = 2.0 A, L = 3.0 mH, R _G = 25 Ω			

⁽¹⁾ Pulse width is limited by maximum junction temperature repetitive rating.

Designer's Data for "Worst Case" Conditions — The Designer's Data Sheet permits the design of most circuits entirely from the information presented. SOA Limit curves — representing boundaries on device characteristics — are given to facilitate "worst case" design.

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Cha	racteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Collector-to-Emitter Breakdown Vo (VGE = 0 Vdc, I _C = 250 μAdc) Temperature Coefficient (Positive		V(BR)CES	600 —	680 0.7	_	Vdc V/°C
Zero Gate Voltage Collector Current (VCE = 600 Vdc, VGE = 0 Vdc) (VCE = 600 Vdc, VGE = 0 Vdc, TC = 125°C)		ICES ICES	_ _	0.1 5.0	5.0 50	μAdc
Gate-Body Leakage Current (VGE	= ±15 Vdc, V _{CE} = 0 Vdc)	IGES	_	10	100	μAdc
ON CHARACTERISTICS						
Collector-to-Emitter On-State Volt (VGE = 15 Vdc, IC = 0.3 Adc) (VGE = 15 Vdc, IC = 0.3 Adc, TC	•	VCE(on)	_	1.6 1.5	2.0 —	Vdc
Gate Threshold Voltage (V _{CE} = V _{GE} , I _C = 250 μAdc) Threshold Temperature Coefficie	nt (Negative)	VGE(th)	3.5 —	— 6.0	6.0 —	Vdc mV/°C
Forward Transconductance (VCE =	= 10 Vdc, I _C = 0.5 Adc)	9fe	0.3	0.42	_	Mhos
DYNAMIC CHARACTERISTICS						
Input Capacitance		C _{ies}	_	75	100	pF
Output Capacitance	(V _{CE} = 20 Vdc, V _{GE} = 0 Vdc, f = 1.0 MHz)	C _{oes}	_	11	20	
Transfer Capacitance	,	C _{res}	_	1.6	5.0	
DIODE CHARACTERISTICS						
Diode Forward Voltage Drop (I _{EC} = 0.3 Adc) (I _{EC} = 0.3 Adc, T _C = 125°C) (I _{EC} = 0.1 Adc) (I _{EC} = 0.1 Adc, T _C = 125°C)		VFEC	 - - -	5.0 5.2 2.3 2.3	6.0 — 3.0 —	Vdc
Reverse Recovery Time	(I _F = 0.4 Adc, V _R = 300 Vdc,	t _{rr}	_	150	_	ns
Reverse Recovery Stored Charge	$dIF/dt = 10 A/\mu s$)	Q _{RR}	_	35	_	μC
SWITCHING CHARACTERISTICS (1)					
Turn-Off Delay Time	(V _{CC} = 300 Vdc, I _C = 0.4 Adc,	td(off)	_	28	_	ns
Fall Time	$V_{GE} = 15 \text{ Vdc}, L = 3.0 \text{ mH}, R_{G} = 25 \Omega,$ $dV/dt = 1000 \text{ V/}\mu\text{s})$	t _f	_	150	_]
Turn-Off Switching Loss	Energy losses include "tail"	E _{off}	_	3.25	4.25	μJ
Turn-Off Delay Time	(V _{CC} = 300 Vdc, I _C = 0.4 Adc,	^t d(off)	_	21	_	ns
Fall Time	V_{GE} = 15 Vdc, L = 3.0 mH, R _G = 25 Ω, T _C = 125°C, dV/dt = 1000 V/μs)	t _f	_	280	_]
Turn-Off Switching Loss	Energy losses include "tail"	E _{off}	_	8.0	10	μJ
Gate Charge	$V_{CC} = 300 \text{ Vdc}, I_{C} = 0.3 \text{ Adc}, V_{GE} = 15 \text{ Vdc}$	QT	_	6.4	_	nC

⁽¹⁾ Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

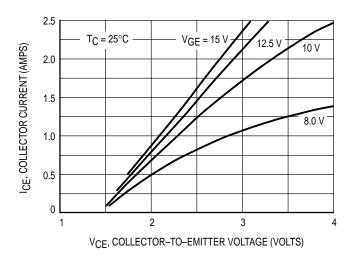


Figure 1. Saturation Characteristics

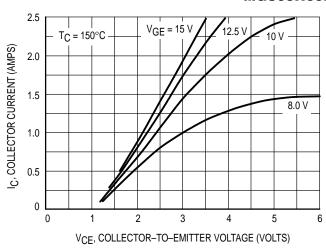


Figure 2. Saturation Characteristics

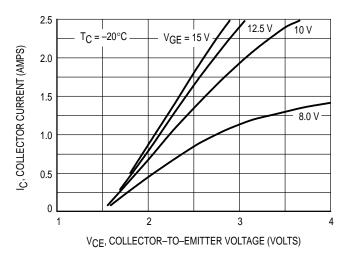


Figure 3. Saturation Characteristics

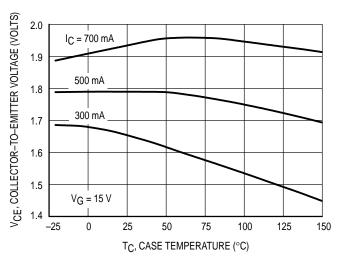


Figure 4. Collector–To–Emitter Saturation Voltage versus Case Temperature

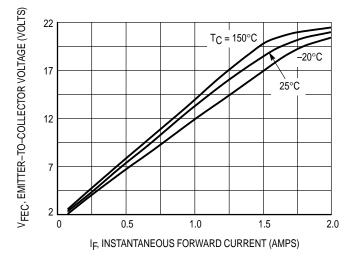


Figure 5. Diode Forward Voltage Drop

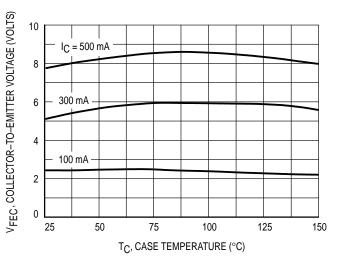


Figure 6. Diode Forward Voltage versus Case Temperature

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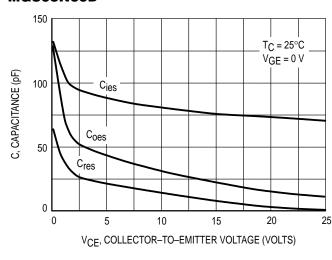


Figure 7. Capacitance Variation

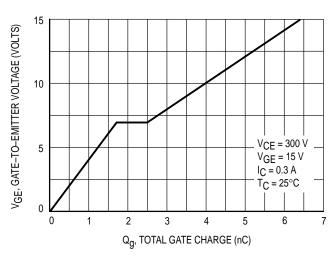


Figure 8. Gate-To-Emitter Voltage versus
Total Charge

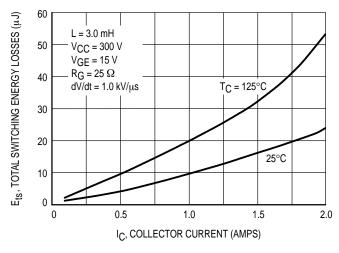


Figure 9. Total Switching Losses versus Collector Current

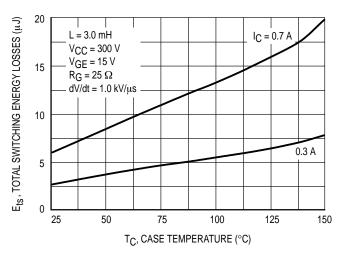


Figure 10. Total Switching Losses versus

Case Temperature

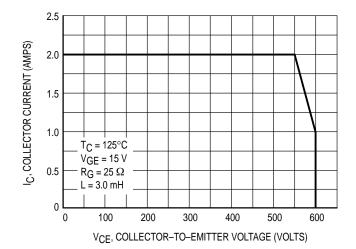


Figure 11. Minimum Turn-Off Safe Operating Area

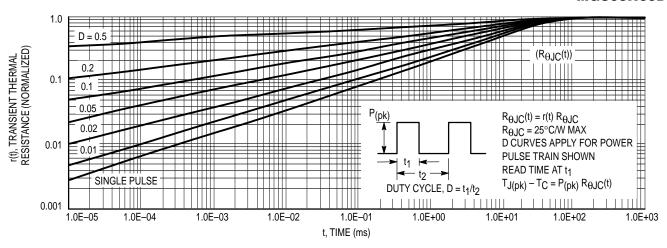
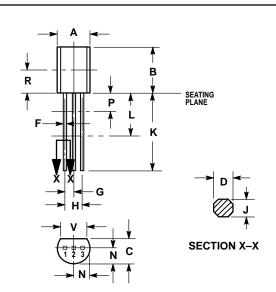


Figure 12. Typical Thermal Response

PACKAGE DIMENSIONS



CASE 029-05 TO-226AE ISSUE AD

NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M. 1982.
- 2. CONTROLLING DIMENSION: INCH.
- CONTOUR OF PACKAGE BEYOND DIMENSION R
 IS UNCONTROLLED.
- 4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSIONS D AND J APPLY BETWEEN L AND K MIMIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS				
DIM	MIN	MAX	MIN	MAX			
Α	0.175	0.205	4.44	5.21			
В	0.290	0.310	7.37	7.87			
С	0.125	0.165	3.18	4.19			
D	0.018	0.022	0.46	0.56			
F	0.016	0.019	0.41	0.48			
G	0.045	0.055	1.15	1.39			
Н	0.095	0.105	2.42	2.66			
J	0.018	0.024	0.46	0.61			
K	0.500		12.70				
L	0.250		6.35				
N	0.080	0.105	2.04	2.66			
Р		0.100		2.54			
R	0.135		3.43				
٧	0.135		3.43				

STYLE 35:

PIN 1. GATE

- 2. COLLECTOR
- 3. EMITTER

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