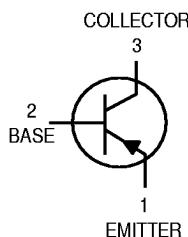


Amplifier Transistors

PNP Silicon

Boca Semiconductor Corp. (BSC)



MAXIMUM RATINGS

Rating	Symbol	2N5400	2N5401	Unit
Collector-Emitter Voltage	V_{CEO}	120	150	Vdc
Collector-Base Voltage	V_{CBO}	130	160	Vdc
Emitter-Base Voltage	V_{EBO}	5.0	—	Vdc
Collector Current — Continuous	I_C	600	—	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	625 5.0	—	mW mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	1.5 12	—	Watts mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	—55 to +150	—	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case	$R_{\theta JC}$	83.3	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
—	—	—	—	—

OFF CHARACTERISTICS

Collector-Emitter Breakdown Voltage ⁽¹⁾ ($I_C = 1.0$ mAdc, $I_B = 0$) 2N5400 2N5401	$V_{(BR)CEO}$	120 150	—	Vdc
Collector-Base Breakdown Voltage ($I_C = 100$ μ Adc, $I_E = 0$) 2N5400 2N5401	$V_{(BR)CBO}$	130 160	—	Vdc
Emitter-Base Breakdown Voltage ($I_E = 10$ μ Adc, $I_C = 0$)	$V_{(BR)EBO}$	5.0	—	Vdc
Collector Cutoff Current ($V_{CB} = 100$ Vdc, $I_E = 0$) ($V_{CB} = 120$ Vdc, $I_E = 0$) ($V_{CB} = 100$ Vdc, $I_E = 0$, $T_A = 100^\circ\text{C}$) ($V_{CB} = 120$ Vdc, $I_E = 0$, $T_A = 100^\circ\text{C}$) 2N5400 2N5401 2N5400 2N5401	I_{CBO}	— — — —	100 50 100 50	nAdc μ Adc
Emitter Cutoff Current ($V_{EB} = 3.0$ Vdc, $I_C = 0$)	I_{EBO}	—	50	nAdc

1. Pulse Test: Pulse Width = 300 μ s, Duty Cycle = 2.0%.

Preferred devices are Motorola recommended choices for future use and best overall value.

2N5400
2N5401*

*Motorola Preferred Device



CASE 29-04, STYLE 1
TO-92 (TO-226AA)

2N5400 2N5401**ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)**

Characteristic	Symbol	Min	Max	Unit
ON CHARACTERISTICS(1)				
DC Current Gain (I _C = 1.0 mAdc, V _{CE} = 5.0 Vdc)	2N5400 2N5401	h _{FE} 30 50	— —	—
(I _C = 10 mAdc, V _{CE} = 5.0 Vdc)	2N5400 2N5401	40 60	180 240	
(I _C = 50 mAdc, V _{CE} = 5.0 Vdc)	2N5400 2N5401	40 50	— —	
Collector-Emitter Saturation Voltage (I _C = 10 mAdc, I _B = 1.0 mAdc) (I _C = 50 mAdc, I _B = 5.0 mAdc)	V _{CE(sat)}	— —	0.2 0.5	Vdc
Base-Emitter Saturation Voltage (I _C = 10 mAdc, I _B = 1.0 mAdc) (I _C = 50 mAdc, I _B = 5.0 mAdc)	V _{BE(sat)}	— —	1.0 1.0	Vdc

SMALL-SIGNAL CHARACTERISTICS

Current-Gain — Bandwidth Product (I _C = 10 mAdc, V _{CE} = 10 Vdc, f = 100 MHz)	2N5400 2N5401	f _T 100 100	400 300	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)	C _{obo}	—	6.0	pF
Small-Signal Current Gain (I _C = 1.0 mAdc, V _{CE} = 10 Vdc, f = 1.0 kHz)	2N5400 2N5401	h _{fe} 30 40	200 200	—
Noise Figure (I _C = 250 µAdc, V _{CE} = 5.0 Vdc, R _S = 1.0 kΩ, f = 1.0 kHz)	NF	—	8.0	dB

1. Pulse Test: Pulse Width = 300 µs, Duty Cycle = 2.0%.

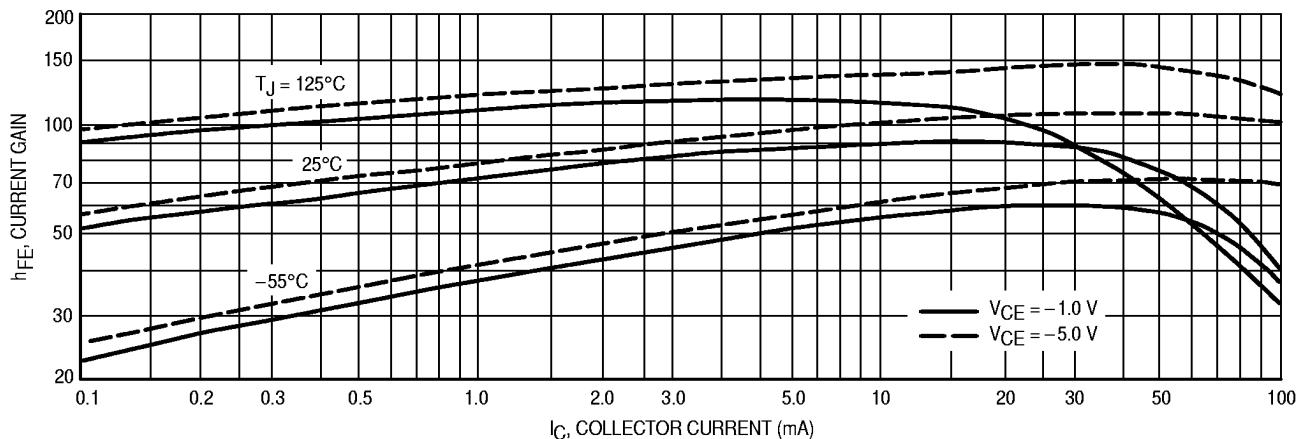


Figure 1. DC Current Gain

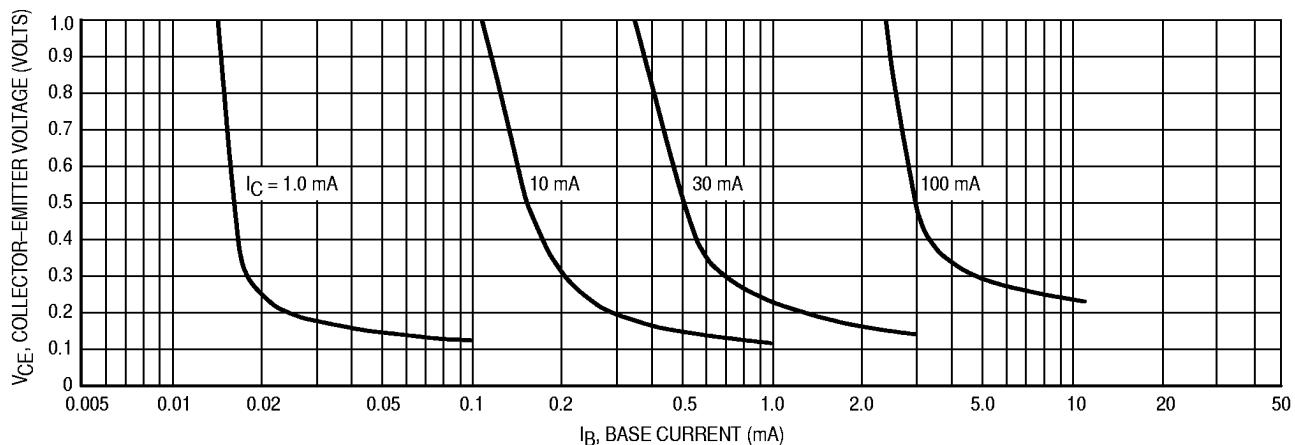


Figure 2. Collector Saturation Region

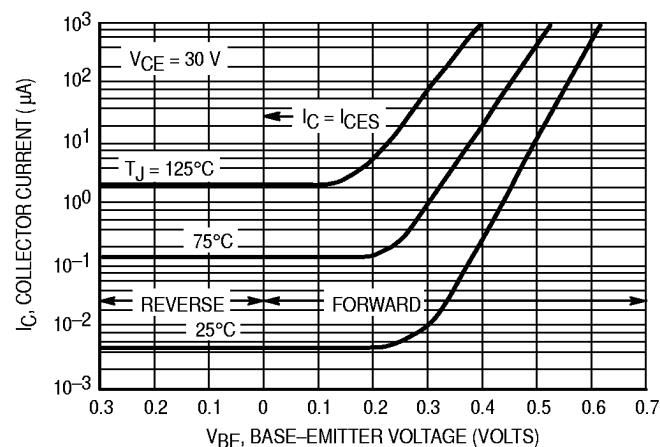


Figure 3. Collector Cut-Off Region

2N5400 2N5401

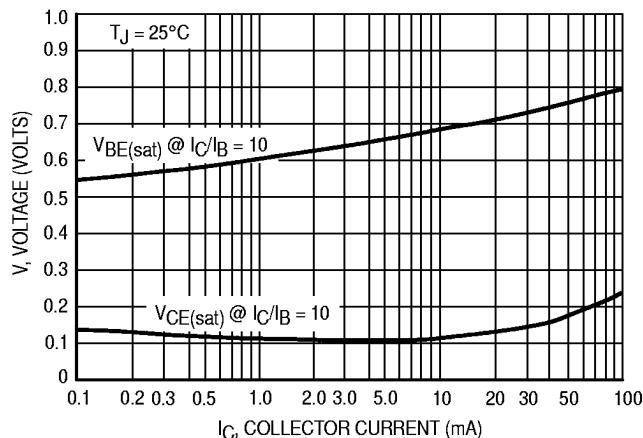


Figure 4. "On" Voltages

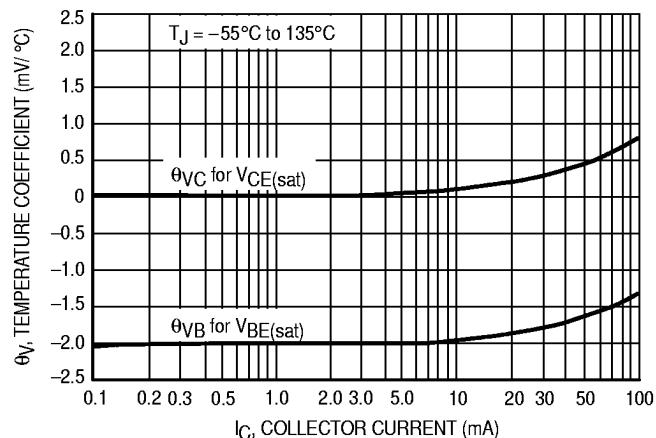


Figure 5. Temperature Coefficients

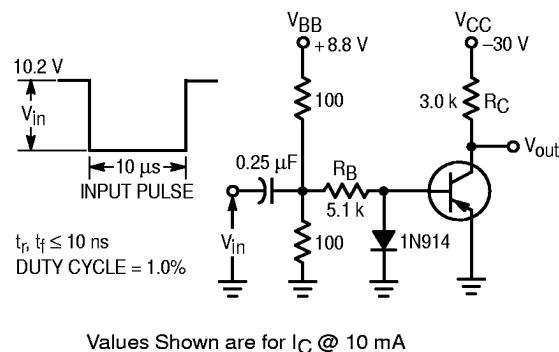


Figure 6. Switching Time Test Circuit

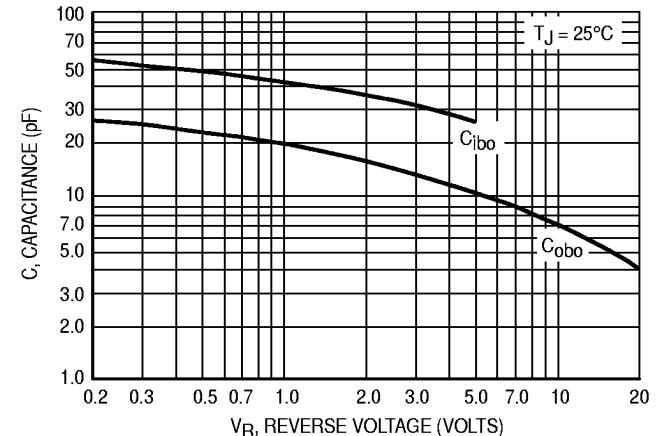


Figure 7. Capacitances

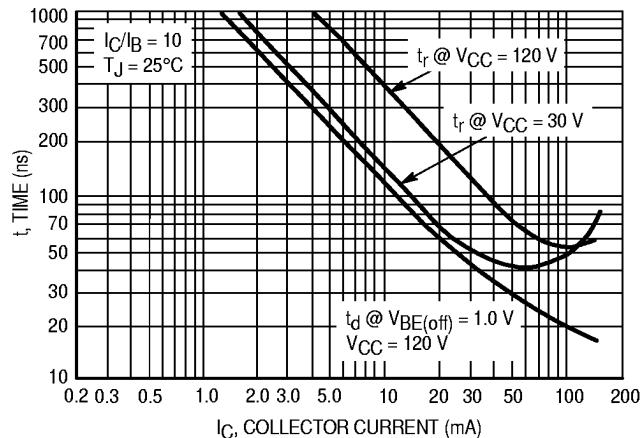


Figure 8. Turn-On Time

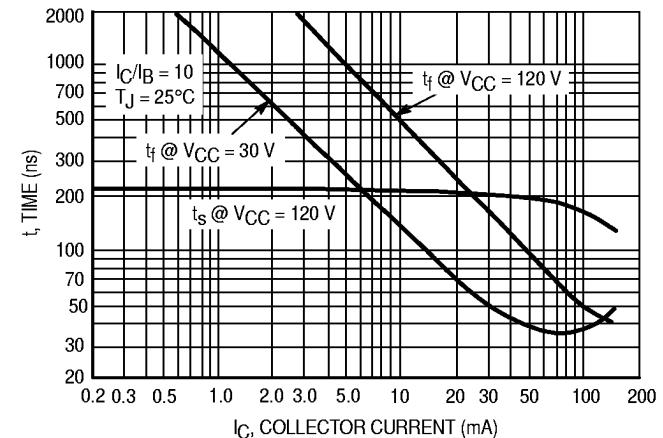
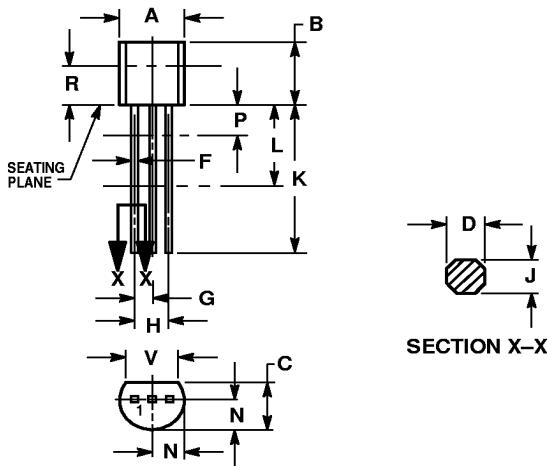


Figure 9. Turn-Off Time

PACKAGE DIMENSIONS



**CASE 029-04
(TO-226AA)
ISSUE AD**

NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. DIMENSION F APPLIES BETWEEN P AND L.
DIMENSION D AND J APPLY BETWEEN L AND K
MINIMUM. LEAD DIMENSION IS UNCONTROLLED
IN P AND BEYOND DIMENSION K MINIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.175	0.205	4.45	5.20
B	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.022	0.41	0.55
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
H	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500	—	12.70	—
L	0.250	—	6.35	—
N	0.080	0.105	2.04	2.66
P	—	0.100	—	2.54
R	0.115	—	2.93	—
V	0.135	—	3.43	—

STYLE 1:
PIN 1. Emitter
2. Base
3. Collector