

T-67-21-51

54AC11151, 74AC11151
1 OF 8 DATA SELECTORS/MULTIPLEXERS

T10105—D3348, JUNE 1989—REVISED JANUARY 1990

- 8-Line to 1-Line Multiplexers can Perform as: Boolean Function Generators, Parallel-to-Serial Converters, Data Source Selectors
- Flow-Through Architecture to Optimize PCB Layout
- Center-Pin VCC and GND Configurations to Minimize High-Speed Switching Noise
- EPIC™ (Enhanced-Performance Implanted CMOS) 1-μm Process
- 500-mA Typical Latch-Up Immunity at 125°C
- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs

description

These monolithic data selectors/multiplexers provide full binary decoding to select one of eight data sources. The strobe input ($\bar{G}$) must be at a low logic level to enable the inputs. A high level at the strobe terminal forces the W output high and the Y output low.

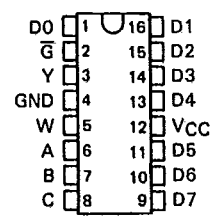
The 54AC11151 is characterized for operation over the full military temperature range of -55°C to 125°C. The 74AC11151 is characterized for operation from -40°C to 85°C.

FUNCTION TABLE

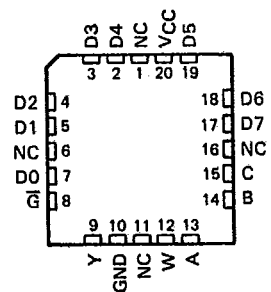
INPUTS				OUTPUTS	
SELECT			STROBE	Y	W
C	B	A	$\bar{G}$		
X	X	X	H	L	H
L	L	L	L	D0	$\bar{D}0$
L	L	H	L	D1	$\bar{D}1$
L	H	L	L	D2	$\bar{D}2$
L	H	H	L	D3	$\bar{D}3$
H	L	L	L	D4	$\bar{D}4$
H	L	H	L	D5	$\bar{D}5$
H	H	L	L	D6	$\bar{D}6$
H	H	H	L	D7	$\bar{D}7$

H = high level, L = low level,
X = irrelevant.
D0, D1, ... D7 = the level of the respective D input.

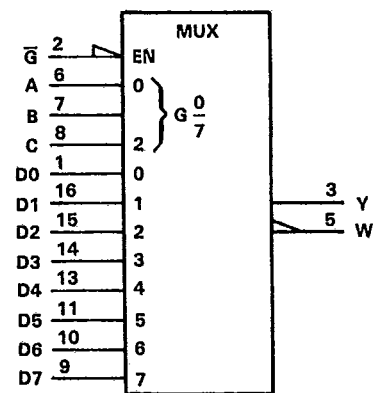
54AC11151 ... J PACKAGE
74AC11151 ... D OR N PACKAGE
(TOP VIEW)



54AC11151 ... FK PACKAGE
(TOP VIEW)



logic symbol†



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.
Pin numbers shown are for D, J, and N packages.

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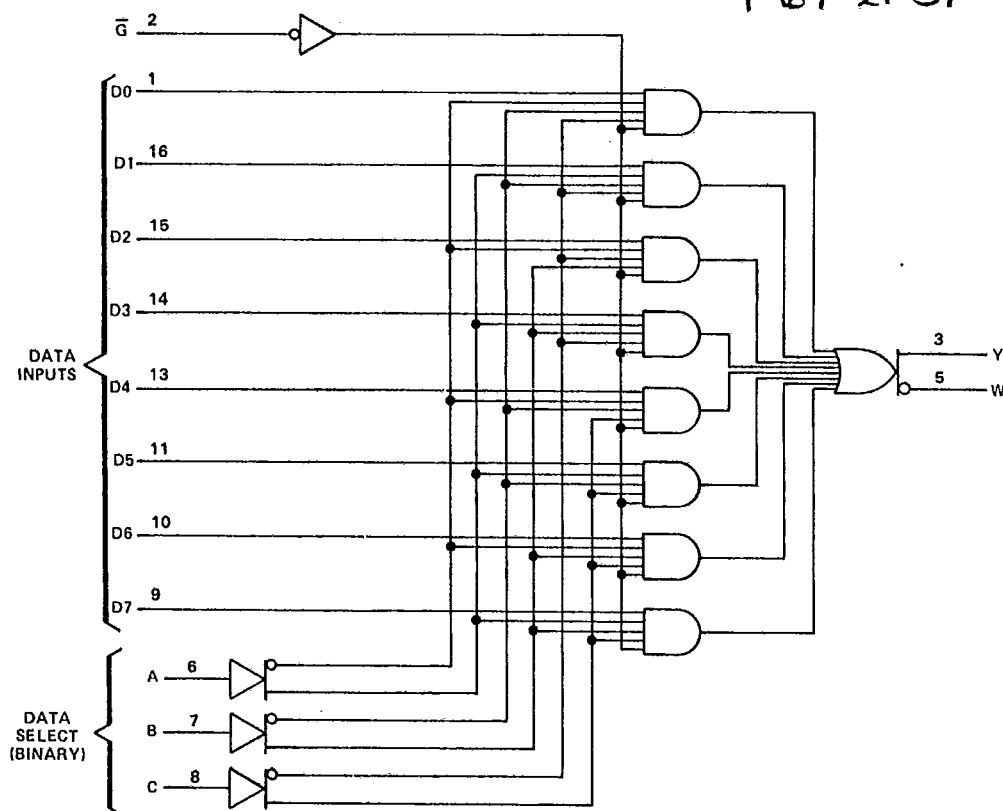
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logic diagram (positive logic)

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Pin numbers shown are for D, J, and N packages.

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage range, V_{CC}	−0.5 V to 7 V
Input voltage range, V_I (see Note 1)	−0.5 to $V_{CC} + 0.5$ V
Output voltage range, V_O (see Note 1)	−0.5 to $V_{CC} + 0.5$ V
Input clamp current, I_{IK} ($V_I < 0$ or $V_I > V_{CC}$)	±20 mA
Output clamp current, I_{OK} ($V_O < 0$ or $V_O > V_{CC}$)	±50 mA
Continuous output current, I_O ($V_O = 0$ to V_{CC})	±50 mA
Continuous current through V_{CC} or GND pins	±100 mA
Storage temperature range	−65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

recommended operating conditions

				54AC11151			74AC11151			UNIT
				MIN	NOM	MAX	MIN	NOM	MAX	
V _{CC}	Supply voltage			3	5	5.5	3	5	5.5	V
V _{IH}	High-level input voltage			V _{CC} = 3 V			2.1			V
				V _{CC} = 4.5 V			3.15			
				V _{CC} = 5.5 V			3.85			
V _{IL}	Low-level input voltage			V _{CC} = 3 V			0.9			V
				V _{CC} = 4.5 V			1.35			
				V _{CC} = 5.5 V			1.65			
V _I	Input voltage			0	V _{CC}		0	V _{CC}		V
V _O	Output voltage			0	V _{CC}		0	V _{CC}		V
I _{OH}	High-level output current			V _{CC} = 3 V			−4			mA
				V _{CC} = 4.5 V			−24			
				V _{CC} = 5.5 V			−24			
I _{OL}	Low-level output current			V _{CC} = 3 V			12			mA
				V _{CC} = 4.5 V			24			
				V _{CC} = 5.5 V			24			
Δt/Δv	Input transition rise or fall rate			0	10		0	10		ns/V
T _A	Operating free-air temperature			−55	125		−40	85		°C

PRODUCT PREVIEW Information concerns products in the formative or design phase of development. Characteristic data and other specifications are design goals. Texas Instruments reserves the right to change or discontinue these products without notice.

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TEXAS
INSTRUMENTS

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TI0105—D3348, JUNE 1989—REVISED JANUARY 1990

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	VCC	TA = 25°C			54AC11151		74AC11151		UNIT
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
VOH	IOH = -50 µA	3 V	2.9			2.9		2.9		V
		4.5 V	4.4			4.4		4.4		
		5.5 V	5.4			5.4		5.4		
	IOH = -4 mA	3 V	2.58			2.4		2.48		
		4.5 V	3.94			3.7		3.8		
		5.5 V	4.94			4.7		4.8		
	IOH = -50 mA†	5.5 V				3.85				
		5.5 V						3.85		
VOL	IOL = 50 µA	3 V		0.1		0.1		0.1		V
		4.5 V		0.1		0.1		0.1		
		5.5 V		0.1		0.1		0.1		
	IOL = 12 mA	3 V		0.36		0.5		0.44		
		4.5 V		0.36		0.5		0.44		
		5.5 V		0.36		0.5		0.44		
	IOL = 50 mA†	5.5 V				1.65				
		5.5 V						1.65		
I _I	V _I = VCC or GND	5.5 V		±0.1		±1		±1		µA
I _{CC}	V _I = VCC or GND, I _O = 0	5.5 V		8		160		80		µA
C _I	V _I = VCC or GND	5 V		3.5						pF

† Not more than one output should be tested at a time and the duration of the test should not exceed 10 ms.

switching characteristics over recommended operating free-air temperature range,
VCC = 3.3 V ± 0.3 V (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TA = 25°C			54AC11151		74AC11151		UNIT
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
tPLH	A, B, or C	Y	3.2	8.9	10.7	3.2	12.9	3.2	12	ns
tPHL			3.4	8.9	10.8	3.4	12.9	3.4	12.1	
tPLH	A, B, or C	W	3.2	8.6	10.3	3.2	12.4	3.2	11.6	ns
tPHL			3.1	8.7	10.7	3.1	12.9	3.1	12	
tPLH	Any D	Y	1.9	6.5	8.1	1.9	9.8	1.9	9.2	ns
tPHL			1.9	6.4	8.1	1.9	9.4	1.9	8.9	
tPLH	Any D	W	1.7	6.1	7.7	1.7	9.1	1.7	8.6	ns
tPHL			1.9	6.4	8	1.9	9.5	1.9	8.8	
tPLH	G	Y	1.3	4.0	5.5	1.3	6.3	1.3	6	ns
tPHL			1.6	4.5	5.9	1.6	6.9	1.6	6.5	
tPLH	G	W	2	5.2	6.7	2	7.8	2	7.4	ns
tPHL			1.7	4.7	6.2	1.7	7	1.7	6.7	

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switching characteristics over recommended operating free-air temperature range,
VCC = 5 V ± 0.5 V unless otherwise noted (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TA = 25°C			54AC11151		74AC11151		UNIT
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
tPLH	A, B, or C	Y	2.5	5.4	7.3	2.5	8.9	2.5	8.3	ns
tPHL			2.7	5.6	7.5	2.7	9.1	2.7	8.5	
tPLH	A, B, or C	W	2.6	5.3	7.2	2.6	8.6	2.6	8.1	ns
tPHL			2.6	5.4	7.4	2.6	9.1	2.6	8.4	
tPLH	Any D	Y	1.5	4.1	5.8	1.5	6.9	1.5	6.5	ns
tPHL			1.5	4.0	5.7	1.5	6.8	1.5	6.4	
tPLH	Any D	W	1.4	3.7	5.5	1.4	6.4	1.4	6	ns
tPHL			1.6	4.1	5.8	1.6	6.9	1.6	6.4	
tPLH	$\overline{Q}$	Y	1.1	2.7	4.2	1.1	4.8	1.1	4.6	ns
tPHL			1.4	3.1	4.6	1.4	5.3	1.4	5	
tPLH	$\overline{Q}$	W	1.7	3.5	5.1	1.7	6	1.7	5.6	ns
tPHL			1.4	3.1	4.6	1.4	5.3	1.4	5	

operating characteristics, VCC = 5 V, TA = 25°C

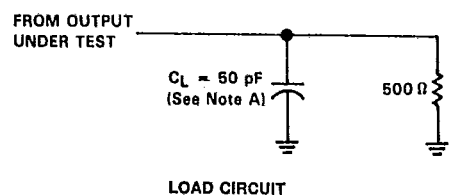
PARAMETER		TEST CONDITIONS	TYP	UNIT
Cpd	Power dissipation capacitance	CL = 50 pF, f = 1 MHz	52	pF

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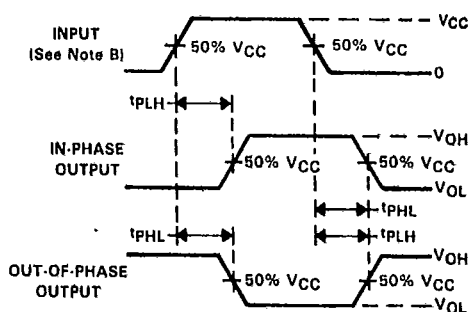
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1 OF 8 DATA SELECTORS/MULTIPLEXERS

TIO105—D3348, JUNE 1989—REVISED JANUARY 1990

PARAMETER MEASUREMENT INFORMATION



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PROPAGATION DELAY TIMES

NOTES: A. C_L includes probe and jig capacitance.

B. Input pulses are supplied by generators having the following characteristics: PRR $\leq 10 \text{ MHz}$, $Z_0 = 50 \Omega$, $t_r = 3 \text{ ns}$, $t_f = 3 \text{ ns}$.

C. The outputs are measured one at a time with one input transition per measurement.

FIGURE 1. LOAD CIRCUIT AND VOLTAGE WAVEFORMS